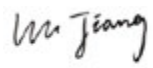




Test Report issued under the responsibility of:



TEST REPORT IEC 60947-2 Low-voltage switchgear and controlgear - Part 2: Circuit-breakers	
Report Number.....:	CN23IMEV 001
Date of issue.....:	30.12.2023
Total number of pages.....:	189
Name of Testing Laboratory preparing the Report.....:	Hunan Electric Research Institute Testing Group Co., Ltd.
Applicant's name	Elmark Industries SC
Address	2 Dobrudzha blvd., Dobrich, BULGARIA
Test specification:	
Standard.....:	IEC 60947-2:2016, IEC 60947-2:2016/AMD1:2019, for use in conjunction IEC 60947-1:2007, IEC 60947-1:2007/AMD1:2010, IEC 60947-1:2007/AMD2:2014
Test procedure	CB Scheme
Non-standard test method	N/A
Test Report Form No.:	IEC60947_2K
Test Report Form(s) Originator:	DEKRA Certification B.V.
Master TRF.....:	Dated 2023-09-14
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General disclaimer:	
The test results presented in this report relate only to the object tested. This report shall not be reproduced, except in full, without the written approval of the Issuing NCB. The authenticity of this Test Report and its contents can be verified by contacting the NCB, responsible for this Test Report.	

Test item description	Moulded Case Circuit Breaker	
Trade Mark(s).....		
Manufacturer	2 Dobrudzha blvd., Dobrich, BULGARIA	
Model/Type reference	DS 1	
Ratings	Cat. B; Type of release: electronic; 3P; Ue=415VAC; In=400A; Icu=85kA; Ics=50kA; Uimp=6kV; Ui=690V	
Responsible Testing Laboratory (as applicable), testing procedure and testing location(s):		
☒	CB Testing Laboratory:	Hunan Electric Research Institute Testing Group Co., Ltd.
Testing location/ address.....		199 Xinxiangxi Road, Xiangxiang Kunlunqiao, Xiangtan, Hunan Province, China
Tested by (name, function, signature)		Wu Jiang(test engineer) 
Approved by (name, function, signature)....		Li Bin(Approver) 
☐	Testing procedure: CTF Stage 1:	
Testing location/ address.....		
Tested by (name, function, signature):		
Approved by (name, function, signature)....		
☐	Testing procedure: CTF Stage 2:	
Testing location/ address.....		
Tested by (name + signature)		
Witnessed by (name, function, signature)...		
Approved by (name, function, signature)....		
☐	Testing procedure: CTF Stage 3:	
☐	Testing procedure: CTF Stage 4:	
Testing location/ address.....		
Tested by (name, function, signature):		
Witnessed by (name, function, signature)...		
Approved by (name, function, signature)....		
Supervised by (name, function, signature) :		

List of Attachments (including a total number of pages in each attachment):

Attachment 1: Photo documentation – 4 pages

Summary of testing:

In case of alternative test programs for circuit breakers with a different number of poles, the following program is used:

☐ Programme 1 (three pole fully tested)

☐ Programme 2 (four pole fully tested)

☒ Alternative program not applicable

Numbers of tests for test procedure, according to table 9a, table 9c and table 10.

Type	No. of Pole	Ue (V)	In (A)	Instantaneous current Ii (A)	Test current (kA)		Test sequence and number of samples								
					Ics	Icu	I	II	II/III	III	IV	VI	Annex H	Annex F	Annex N
DS 1	3P	415	400	-	50	85	1	1	-	1	1	-	1	1	1

Tests performed (name of test and test clause):

See summary of testing, 3rd page

Test results are based on Hunan Electric Research Institute CB report, and Intertek CB report No. 180600076SHA_001.

Test sequence I

DS 1, 3P, 415VAC, 400A, 1 sample page 30

Test sequence II

DS 1, 3P, 415VAC, 400A, 50kA, 1 sample page 48

Test sequence III

DS 1, 3P, 415VAC, 400A, 85kA, 1 sample page 56

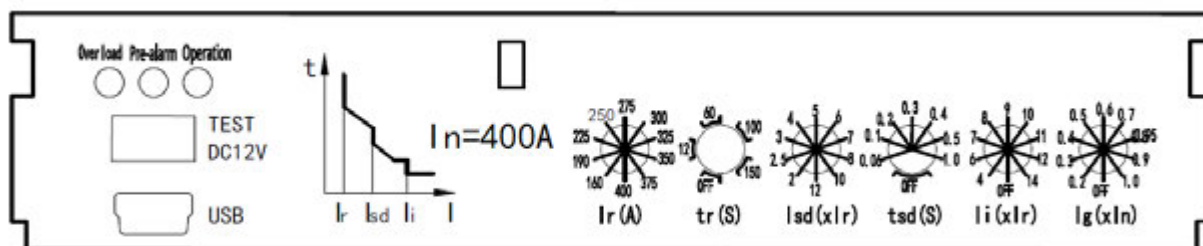
Testing location:

(CBTL, SPTL, CTF, Subcontractor Hunan Electric Research Institute Testing Group Co., Ltd. / 199 Xinxiangxi Road, Xiangxiang Kunlunqiao, Xiangtan, Hunan Province, China

Test sequence IV DS 1, 3P, 415VAC, 400A, 5kA/1s, 1 sample	page 59	Testing location: (CBTL, SPTL, CTF, Subcontractor Intertek Testing Services Shanghai. / Inspection Center of Products' Quality of Low Voltage Electric Apparatus in Zhejiang Province
Annex F DS 1, 3P, 415VAC, 400A, 85kA, 1 sample	page 127	
Annex H DS 1, 3P, 415VAC, 400A, 85kA, 1 sample	page 132	
Annex N DS 1, 3P, 415VAC, 400A, 85kA, 1 sample	page 177	
Summary of compliance with National Differences (List of countries addressed):		
☒ The product fulfils the requirements of <u>EN 60947-2: 2017+A1</u> (insert standard number and edition and delete the text in parenthesis, leave it blank or delete the whole sentence, if not applicable)		
Use of uncertainty of measurement for decisions on conformity (decision rule) :		
☒ No decision rule is specified by the IEC standard, when comparing the measurement result with the applicable limit according to the specification in that standard. The decisions on conformity are made without applying the measurement uncertainty ("simple acceptance" decision rule, previously known as "accuracy method").		
☐ Other: ... (to be specified, for example when required by the standard or client, or if national accreditation requirements apply)		
Information on uncertainty of measurement: The uncertainties of measurement are calculated by the laboratory based on application of criteria given by OD-5014 for test equipment and application of test methods, decision sheets and operational procedures of IECCE. IEC Guide 115 provides guidance on the application of measurement uncertainty principles and applying the decision rule when reporting test results within IECCE scheme, noting that the reporting of the measurement uncertainty for measurements is not necessary unless required by the test standard or customer. Calculations leading to the reported values are on file with the NCB and testing laboratory that conducted the testing.		

Copy of marking plate:

The artwork below may be only a draft. The use of certification marks on a product must be authorized by the respective NCBs that own these marks.



Test item particulars: test item vs. test requirements	
3. Classification	
3.1. Selectivity category: (A or B)	B
3.2. Interruption medium: (air, vacuum, gas break).....	air
3.3. Design: (open construction, moulded case)	Moulded case
3.4. Method of controlling the operation mechanism: (dependent manual, independent manual, dependent power, independent power, stored energy operation).....	Independent manual
3.5. Suitability for isolation: (suitable, not suitable).....	Suitable
3.6. Provision for maintenance: (maintainable, non- maintainable).....	non-maintainable
3.7. Method of installation: (fixed, plug-in, withdrawable.....	Fixed
3.8. Degree of protection of enclosure: (IP code).....	IP20(normal installed)
4.7. Type of release (thermo-magnetic / electronic)	Electronic
4.8. Integral fuses (integrally fused circuit-breakers)	N/A
Type and characteristics of SCPD	
7.3 Electromagnetic compatibility (EMC)	N/A
Environment A or B.....	
Circuit-breaker for use on phase-earthed systems.....	N/A
Circuit-breaker for use in IT systems	Yes
Rated and limiting values, main circuit	
- rated operational voltage: U_e (V).....	AC415V
- rated insulation voltage: U_i (V)	690V
- rated impulse withstand voltage: U_{imp} (kV)	6,0
- rated current: I_n (A)	400A
- kind of current	AC
- conventional free air thermal current: I_{th} (A).....	400A
- conventional enclosed thermal current: I_{the} (A).....	N/A
- current rating for four-pole circuit-breakers: (A).....	N/A
- number of poles	3P
- rated frequency: (Hz).....	50/60Hz
- integral fuses (rated values)	N/A
Rated duty :	
- eight-hour duty	N/A
- uninterrupted duty: I_u (A).....	Same as I_n
Short-circuit characteristic :	
rated short-time making capacity: I_{cm} (kA)	N/A

rated ultimate short-circuit breaking capacity: I_{cu} (kA): 85kA
 rated service short-circuit breaking capacity: I_{cs} (kA).....: 50kA
 rated short-time withstand current: I_{cw} (kA/s): 5kA/1s

Control circuits : N/A

Electrical control circuits :

- kind of current: (AC, DC):
 - rated frequency: (Hz).....:
 - rated control circuit voltage: U_c (nature, frequency, V):
 - rated control supply voltage: U_s (nature, frequency V):
 Air supply control circuits: (pneumatic or electro-pneumatic) :
 - rated pressure and its limit:
 - volumes of air, at atmospheric pressure, required for
 each closing and each opening operation

Auxiliary circuits :

Rated and limiting values, auxiliary circuits:
 - rated operational voltage U_e (V).....:
 - rated insulation voltage: U_i (V):
 - rated operational current: I_e (A).....:
 - kind of current:
 - rated frequency: (Hz).....:
 - number of circuits:
 - number and kind of contact elements:
 - rated uninterrupted current: I_u (A).....:
 - utilization category: (AC, DC, current and voltage):
 Short-circuit characteristic :
 - Rated conditional short-circuit current (kA).....:
 - kind of protective device

Releases :

- 1) shunt release: N/A
- 2) Over-current release.....: Yes
- a) instantaneous.....: Yes
- b) definite time delay: N/A
- c) inverse time delay: Yes
- independent of previous load: Yes
- dependent on previous load; (for example thermal type release).....: N/A
- 3) Undervoltage release (for opening): N/A
- 4) Closing releases: N/A
- 5) Other releases:

Characteristics :

- 1) Shunt release and undervoltage release (for opening), and closing release: See below
- rated control circuit voltage: U_c (nature, frequency, V): N/A
- kind of current: N/A
- rated frequency: (if AC): N/A
- 2) Over-current release.....: See blow
- rated current.....: 400A
- kind of current: AC
- rated frequency: (if AC): 50/60Hz
- current setting (or range of settings): Inverse time delay: $I_{r1}=200-400A$
short time delay: $I_{r2}=(2-12)I_{r1}$
Instantaneous: $I_{r3}=(4-14)I_{r1}$
earth protection: $I_g=(0,7-1,0)I_{r1}$
- time settings (or range of settings).....: Inverse time delay: $1,05I_{r1}: \geq 1h; 1,30I_{r1}: <1h$
Inverse time setting: $t1: OFF, 12-150s;$
short time delay: $0,8 \times I_{r2}: \geq 0,2s; 0,8 \times I_{r2}: <0,2s;$
Short time setting: $t2: OFF, 12-150s;$
Instantaneous: $0,8 \times I_{r3}: \geq 0,2s; 0,8 \times I_{r3}: <0,2s;$

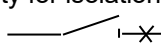
Test item particulars..... :	
Classification of installation and use..... : Fixed installation	
Supply Connection : N/A	
..... :	
Possible test case verdicts:	
- test case does not apply to the test object..... : N/A	
- test object does meet the requirement..... : P (Pass)	
- test object does not meet the requirement..... : F (Fail)	
Testing..... :	
Date of receipt of test item : 15.09.2023	
Date (s) of performance of tests : 16.09.2023~25.10.2023	
General remarks:	
"(See Enclosure #)" refers to additional information appended to the report. "(See appended table)" refers to a table appended to the report. Throughout this report a ☒ comma / ☐ point is used as the decimal separator. ☐ This Test Report Form contains requirements according to IEC/ISO Standard dated and includes Corrigendum dated (Note: The above text maybe removed if not applicable)	
Manufacturer's Declaration per sub-clause 4.2.5 of IEC 60947-2:	
The application for obtaining a CB Test Certificate includes more than one factory location and a declaration from the Manufacturer stating that the sample(s) submitted for evaluation is (are) representative of the products from each factory has been provided :	☐ Yes ☒ Not applicable
When differences exist; they shall be identified in the General product information section.	
Name and address of factory (ies) : Elmark Industries SC / 2 Dobrudzha blvd., Dobrich, BULGARIA	

General product information and other remarks:

Type Designation: DS 1

Cat. B; Type of release: electronic; 3P; $U_e=415\text{VAC}$; $I_n=400\text{A}$; $I_{cu}=85\text{kA}$; $I_{cs}=50\text{kA}$; $U_{imp}=6\text{kV}$; $U_i=690\text{V}$

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

5.2	MARKING		
	Visible from the front when the circuit-breaker is installed as in service and actuator is accessible:		
1.1	- rated current (I_n):	400A	P
1.2	- suitability for isolation, if applicable, with the symbol 		P
1.3	- indication of the open and closed position: with $\bigcirc$ and I respectively, if symbols are used		P
	Marked on the circuit-breaker:		
2.1	- manufacturer's name or trade mark		P
2.2	- type designation or catalogue reference	DS 1	P
2.3	- IEC 60947-2, if the manufacturer claims compliance with this standard.		P
2.4	- selectivity category A or B	B	P
2.5	- rated operational voltage(s) U_e	AC415V	P
2.6	- unsuitability for IT systems, if applicable, with the symbol 		N/A
2.7	- rated impulse withstand voltage (U_{imp});	6kA	P
2.8	- value (or range) of the rated frequency and/or the indication "d.c" (or the symbol )	AC	P
2.9	- rated service short-circuit breaking capacity (I_{cs}) at the corresponding rated voltage (U_e)	50kA	P
2.10	- rated ultimate short-circuit breaking capacity (I_{cu}) at the corresponding rated voltage (U_e)	85kA	P
2.11	- rated short-time withstand current (I_{cw}) and associated short-time delay, for selectivity category B	5kA/1s	P
2.12	- range of the current setting (I_r) of the adjustable overload release (may be displayed)	$I_r = 200-400A$	P
2.13	- range of the rated instantaneous short-circuit current setting (I_i), for adjustable overload releases (may be displayed)	$I_i = 10I_n \pm 20\%$	P
2.14	- ref. temperature for non-compensated thermal releases, if different from 30 °C	30 °C	N/A
2.15	- terminals identification, according to 7.1.8.4 of IEC 60947-1:2007:		P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- terminal of coils (A/B)		N/A
	- terminal of shunt release (C)		N/A
	- terminals of under-voltage release (D)		N/A
	- terminals of interlocking electromagnets (E)		N/A
	- terminals of indicated light devices (X)		N/A
	- terminals of contact elements for switching devices (no.)		N/A
2.16	- line and load terminals, if applicable	"1, 3, 5", "2, 4, 6"	P
2.17	- neutral pole terminals, if applicable, by the letter N		N/A
2.18	- protective earth terminal, where applicable, by the symbol  (see 7.1.10.3 of part 1)		N/A
	Provided in the manufacture's literature:		
3.1	- rated short-circuit making capacity (I_{cm}), if higher than that specified in 4.3.6.1		N/A
3.2	- rated insulation voltage. (U_i), if higher than the maximum rated operational voltage	690V	P
3.3	- pollution degree if other than 3		N/A
3.4	- conventional enclosed thermal current (I_{the}) if different from the rated current:		N/A
3.5	- IP Code, where applicable:	IP20(except for the terminals)	P
3.6	- minimum enclosure size and ventilation data (if any) to which marked ratings apply:		N/A
3.7	- details of minimum distance between circuit-breaker and earthed metal parts for circuit-breaker intended for use without enclosure:		N/A
3.8	- suitability for environment A or B per annex J, as applicable		N/A
3.9	- RMS sensing, if applicable, accordance with F.4.1.1		N/A
3.10	- minimum cable cross-section, if different from Table 9 of IEC 60947-1, for ratings ≤ 20 A according to rated ultimate short-circuit breaking capacity I_{cu}		N/A
3.11	- values of tightening torque for the circuit-breaker terminals.	10,0	P
3.12	- current derating for terminals and connections, if applicable		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Marked on the auxiliaries or on the circuit-breaker, if marking space is sufficient; additionally, data shall be made available in the manufacturer's literature:		
4.1	- for closing releases (see 2.23) and/or motor-operators, rated control circuit voltage, kind of current and rated frequency for a.c		N/A
4.2	- rated control circuit voltage of the shunt release and/or of the under-voltage release (or of the no-voltage release), kind of current and rated frequency for a.c:		N/A
4.3	- rated current of indirect over-current releases:		N/A
4.4	- number and type of auxiliary contacts, rated operational currents at the rated operational voltages, and rated frequency for a.c.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
7.1	CONSTRUCTION		
7.1.2 part 1	Materials		
7.1.2.2	Glow wire testing		
	The requirements of 7.1.2 of IEC 60947:2007/AMD1:2010/AMD2:2014 do not apply to parts with a mass lower than 2 g (insignificant mass, in accordance with 3.14 of IEC 60695-2-11:2014). For products containing a plurality of small parts, the total mass of non-tested parts located in close proximity to each other shall not exceed 10 g. Proximity shall be based on engineering judgment that takes into consideration the risk of propagation of fire.		P
	The suitability of materials used is verified by making tests on : or	- the equipment - sections taken from the equipment - samples of identical material having representative thickness	P
	- providing data from the insulating material supplier fulfilling the requirements according to IEC 60695-2-12		P
	Glow-wire test according to IEC 60695-2-10 and IEC 60695-2-11		
	Parts made of insulating material necessary to retain current-carrying parts in position: test temperature 960 °C for main circuit		P
	No visible flame and no sustained glowing		P
	Flames and glowing extinguish within 30 s		P
	No ignition of the tissue paper		P
	Parts made of insulating material necessary to retain current-carrying parts in position: test temperature 850 °C for other circuits		N/A
	No visible flame and no sustained glowing		N/A
	Flames and glowing extinguish within 30 s		N/A
	No ignition of the tissue paper		N/A
	Parts of insulating material not necessary to retain current-carrying parts in position, even though in contact with them: test temperature 650 °C		P
	No visible flame and no sustained glowing		P
	Flames and glowing extinguish within 30 s		P
	No ignition of the tissue paper		P
7.1.2.3	Test based on flammability category		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	For parts of insulating materials, hot wire ignition and, where applicable, arc ignition tests as specified in 8.2.1.1.2, shall be made based on flammability category		N/A
	Tests on materials are made in accordance with Annex M		N/A
	The hot wire ignition (HWI) and arc ignition (AI) test value requirements related to the material flammability category shall conform to Table M.1 or M.2		N/A
	Alternatively, the manufacturer may provide data from the insulating material supplier fulfilling the requirements given in Annex M		N/A
7.1.3 part 1	Current-carrying parts and their connections		
	Current-carrying parts have the necessary mechanical strength and current-carrying capacity for their intended use		P
	For electrical connections, no contact pressure is transmitted through insulating material other than ceramic or other material with characteristics not less suitable, unless there is sufficient resiliency in the metallic parts to compensate for any possible shrinkage or yielding of the insulation material		P
7.1.5 part 1	Actuator		
7.1.5.1	Insulation		
	The actuator of the equipment shall be insulated from the live parts for the rated insulation voltage and, if applicable, the rated impulse withstand voltage		P
	If it is made of metal, it shall be capable of being satisfactorily connected to a protective conductor unless it is provided with additional reliable insulation		N/A
	If it is made of or covered by insulating material, any internal metal part, which might become accessible in the event of insulation failure, shall also be insulated from live parts for the rated insulation voltage		P
7.1.5.2	Direction of movement		
	The direction of operation for actuators of devices shall normally conform to IEC 60447.		P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Where devices cannot conform to these requirements, e.g. due to special applications or alternative mounting positions, they shall be clearly marked such that there is no doubt as to the "I" and "O" positions and the direction of operation		P
7.1.6 part 1	Indication of contact position		
7.1.6.1	Indicating means		
	When an equipment is provided with means for indicating the closed and open positions, these positions shall be unambiguous and clearly indicated		P
	This is done by means of a position indicating device (see 2.3.18)		P
	If symbols are used, they shall indicate the closed and open position respectively, in accordance with IEC 60417-2:		
	- 60417-2-IEC-5007 I On (power)		P
	- 60417-2-IEC-5007 O Off (power)		P
	For equipment operated by means of two push-buttons, only the push-button designated for the opening operation shall be red or marked with the symbol "O"		N/A
	Red colour shall not be used for any other push-button		N/A
	The colours of other push-buttons, illuminated push-buttons and indicator lights shall be in accordance with IEC 60073		N/A
7.1.6.2	Indication by the actuator		
	When the actuator is used to indicate the position of the contacts, it shall automatically take up or stay, when released, in the position corresponding to that of the moving contacts; in this case, the actuator shall have two distinct rest positions corresponding to those of the moving contacts, but for automatic opening a third distinct position of the actuator may be provided		P
7.1.8 part 1	Terminals		
7.1.8.1	All parts of terminals which maintain contact and carry current shall be of metal having adequate mechanical strength		P
	Terminal connections shall be such that necessary contact pressure is maintained		P
	Terminals shall be so constructed that the conductor is clamped between suitable surfaces without damage to the conductor and terminal		P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Terminal shall not allow the conductor to be displaced or to be displaced themselves in a manner detrimental to the operator of equipment and the insulation voltage shall not be reduced below the rated value		P
7.1.8.2	Connection capacity		
	type of conductors :	Copper bars	P
	minimum cross-sectional area of conductor (mm ²) :	240	P
	maximum cross-sectional area of conductor (mm ²) :	240	P
	number of conductors simultaneously connectable to the terminal :	1	P
7.1.8.3	Connection		
	terminals for connection to external conductors shall be readily accessible during installation		P
	clamping screws and nuts shall not serve to fix any other component		P
7.1.8.4	Terminal identification and marking		
	terminal intended exclusively for the neutral conductor		N/A
	protective earth terminal		N/A
	other terminals		P
7.1.10 part 1	Provisions for protective earthing		
7.1.10.1	The exposed conductive parts (e.g. chassis, framework and fixed parts of metal enclosures) other than those which cannot constitute a danger shall be electrically interconnected and connected to a protective earth terminal for connection to an earth electrode or to an external protective conductor		N/A
	This requirement can be met by the normal structural parts providing adequate electrical continuity and applies whether the equipment is used on its own or incorporated in an assembly		N/A
	Exposed conductive parts are considered not to constitute a danger if they cannot be touched on large areas or grasped with the hand or if they are of small size (approximately 50 mm x 50 mm) or are so located as to exclude any contact with live parts		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
7.1.10.2	Protective earth terminal		
	The protective earth terminal shall be readily accessible and so placed that the connection of the equipment to the earth electrode or to the protective conductor is maintained when the cover or any other removable part is removed		N/A
	The protective earth terminal shall be suitably protected against corrosion		N/A
	In the case of equipment with conductive structures, enclosures, etc., means shall be provided, if necessary, to ensure electrical continuity between the exposed conductive parts the equipment and the metal sheathing of connecting conductors		N/A
	The protective earth terminal shall have no other function, except when it is intended to be connected to a PEN conductor (see 2.1.1.5 – Note). In this case, it shall also have the function of a neutral terminal in addition to meeting the requirements applicable to the protective earth terminal		N/A
7.1.10.3	Protective earth terminal marking and identification		
	The protective earth terminal shall be clearly and permanently identified by its marking		N/A
	The identification shall be achieved by colour (green-yellow mark) or by the notation PE, or PEN, as applicable, in accordance with IEC 60445, subclause 5.3, or, in the case of PEN, by a graphical symbol for use on equipment		N/A
	Graphical symbol to be used: 60417-2-IEC-5019  Protective earth (ground) in accordance with IEC 60417-2		N/A
7.1.11 part 1	Enclosure for equipment		
7.1.11.1	Design		
	The enclosure, when it is opened: all parts requiring access for installation and maintenance are readily accessible		N/A
	Sufficient space shall be provided inside the enclosure		N/A
	The fixed parts of a metal enclosure shall be electrically connected to the other exposed conductive parts of the equipment and connected to a terminal which enables them to be earthed or connected to a protective conductor		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Under no circumstances shall a removable metal part of the enclosure be insulated from the part carrying the earth terminal when the removable part is in place		N/A
	The removable parts of the enclosure shall be firmly secured to the fixed parts by a device such that they cannot be accidentally loosened or detached owing to the effects of operation of the equipment or vibrations		N/A
	When an enclosure is so designed as to allow the covers to be opened without the use of tools, means shall be provided to prevent loss of the fastening devices		N/A
	If the enclosure is used for mounting push-buttons, it shall not be possible to remove the buttons from the outside of the enclosure		N/A
7.1.11.2	Insulation		
	If, in order to prevent accidental contact between a metallic enclosure and live parts, the enclosure is partly or completely lined with insulating material, then this lining shall be securely fixed to the enclosure		N/A
7.1.12 part 1	Degree of protection of enclosed equipment		
	Degree of protection.	IP20	
	Test for first characteristic.	IP20	
	Test for first numeral (1, 2, 3, 4, 5, 6).....:	2	P
	Test for second characteristic		
	Test for second numeral (1, 2, 3, 4, 5, 6, 7, 8).....:		N/A
7.1.13 part 1	Conduit pull-out, torque and bending with metallic conduits		
	Polymeric enclosures of equipment, whether integral or not, provided with threaded conduit entries, intended for the connection of extra heavy duty, rigid threaded metal conduits complying with IEC 60981, shall withstand the stresses occurring during its installation such as pull-out, torque, bending		N/A
7.1.2	Withdrawable circuit-breaker		N/A
	In the disconnected position (main- and auxiliary circuits)		
	Isolating distances for circuit-breaker suitable for isolating comply with the requirements specified for the isolating function		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Mechanism fitted with a reliable indicating device with indicates the position of the isolating contacts.		N/A
	Mechanism fitted with interlocks which only permit the isolating contacts to be separate or re-closed when main contacts are open		N/A
	Mechanism fitted with interlock, which only permit the main contacts to be closed when the isolating contacts are fully closed.		N/A
	Mechanism fitted with interlock, which only permit the main contacts to be closed when in disconnected position.		N/A
	In disconnected position, the isolating distances between the isolating contacts cannot be inadvertently reduced.		N/A
7.1.3	Additional requirements for circuit-breakers suitable for isolation		P
7.1.7 part 1	Additional safety requirements for equipment suitable for isolation		
7.1.7.1	Additional constructional requirements for equipment suitable for isolation ($U_e > 50 \text{ V}$):		
	Equipment suitable for isolation shall provide in the open position an isolation distance in acc. with the requirements necessary to satisfy the isolating function. Indication of the main contacts shall be provide by one or more of the following means:		
	- the position of the actuator		P
	- a separate mechanical indicator		P
	- visibility of the moving contacts		N/A
	When means are provided or to lock the equipment in the open position, locking only be possible when contacts are in the open position		N/A
	Actuator front-plate fitted to the equipment in a manner which ensures correct contact position indication and locking		N/A
	The indicated open position is the only position in which the specified isolation distances between the contacts is ensured.		P
	- minimum clearances across open contacts (see Table XIII, Part 1) (mm) :	5,5	
	- measured clearances (mm) :	>8,0	P
	- test Uimp across gap (kV) :	9,8	P
7.1.7.2	Supplementary requirements for equipment with provision for electrical interlocking with contactors or circuit-breakers:		
	auxiliary switch shall be rated according to IEC 60 947-5-1		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	If equipment suitable for isolation is provided with an auxiliary switch for the purpose of electrical interlocking with contactor (s) or circuit-breaker(s) and intended to be used in motor circuits, the following requirements shall apply unless the equipment is rated for AC-23 utilization category		N/A
	The time interval between the opening of the contacts of the auxiliary switch and the contacts of the main poles shall be sufficient to ensure that the associated contactor or circuit-breaker interrupts the current before the main poles of the equipment open		N/A
	Unless otherwise stated in the manufacturer's technical literature, the time interval shall be not less than 20 ms when the equipment is operated according to the manufacturer instructions		N/A
	Compliance shall be verified by measuring the time interval between the instant of opening of the auxiliary switch and the instant of opening of the main poles under no-load conditions when the equipment is operated according to the manufacturer's instructions		N/A
	During the closing operation the contacts of the auxiliary switch shall close after or simultaneously with the contacts of the main poles		N/A
	A suitable opening time interval may also be provided by an intermediate position (between the ON and OFF position) at which the interlocking contact(s) is (are) open and the main poles remain closed		N/A
7.1.7.3	Supplementary requirements for equipment provided with means for padlocking the open position:		
	the locking means shall be designed in such a way that it cannot be removed with the appropriate padlock(s) installed		N/A
	Alternatively, the design may provide padlockable means to prevent access to the actuator		N/A
	test force F applied to the actuator in an attempt to operate to the closed position (N) :		N/A
	rated impulse withstand voltage (kV) :		N/A
	test Uimp on open main contacts at the test force		N/A
	If the tripped position is not the indicated open position, it should be clearly identified.		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
7.1.4	Clearances and creepage distances:		
	For circuit-breakers for which the manufacturer has declared a value of rated impulse withstand voltage. (Uimp.)		
	Clearances distances:		
	- Uimp is given as:	6kV	
	- max. value of rated operational voltage to earth	415V	
	- nominal voltage of supply system:	415V	
	- overvoltage category:	III	
	- pollution degree:	3	
	- field-in or homogeneous:	inhomogeneous	
	- minimum clearances (mm):	5,5	
	- measured clearances (mm):	See table 7.1.4	P
	Creepage distances:		
	- rated insulation voltage Ui (V)	690V	
	- pollution degree	3	
	- comparative tracking index (V)	175V	
	- material group	IIIa	
	- minimum creepage distances (mm)	12,5	
	- measured creepage distances (mm)	See table 7.1.4	P
7.1.5	Requirements for the safety of the operator		
	There shall be no path or opening which allows incandescent particles to be discharged from the area of the manual operating means:		P
7.1.7	Additional requirements for equipment provided with a neutral pole		
7.1.9 part 1	When equipment is provided with a pole intended only for connecting the neutral, this pole shall be clearly identified to that effect by the letter N (see 7.1.7.4.).		N/A
	A switched neutral pole shall break not before and shall make not after the other poles		N/A
	For equipment having a value of conventional thermal current (free air or enclosed, see 4.3.2.1 and 4.3.2.2) not exceeding 63 A, this value shall be identical for all poles		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	For higher conventional thermal current values, the neutral pole may have a value of conventional thermal current different from that of the other poles, but not less than half that value or 63 A, whichever is the higher		N/A
	If a pole with an appropriate making and breaking capacity is used as a neutral pole, then all poles, incl. the neutral pole, may operate substantially together.		N/A
7.1.8	Digital inputs and outputs for use with programmable logic controllers (PLCs)		
	Compliant with Annex S of IEC 60947-1:2007		N/A
	Annex S does not apply to digital inputs and outputs dedicated to devices other than PLCs		N/A
7.2	Performance requirements		
7.2.1	Operating condition		
7.2.1.1	Closing		
	For a circuit-breaker to be closed safely on to the making current corresponding to its rated short-circuit making capacity, it is essential that it should be operated with the same speed and the same firmness as during the type test for proving the short-circuit making capacity		P
7.2.1.1.2	Dependent manual closing		
	For a circuit-breaker having a dependent manual closing mechanism, it is not possible to assign a short-circuit making capacity rating irrespective of the conditions of mechanical operation		N/A
	Such a circuit-breaker should not be used in circuits having a prospective peak making current exceeding 10 kA		N/A
	However, this does not apply in the case of a circuit-breaker having a dependent manual closing mechanism and incorporating an integral fast-acting opening release which causes the circuit-breaker to break safely, irrespective of the speed and firmness with which it is closed on to prospective peak currents exceeding 10 kA; in this case, a rated short-circuit making capacity can be assigned		N/A
7.2.1.1.3	Independent manual closing		
	A circuit-breaker having an independent manual closing mechanism can be assigned a short-circuit making capacity rating irrespective of the conditions of mechanical operation		P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
7.2.1.1.4	Dependent power closing		
	At 110% of the rated control supply voltage, the closing operation performed on no-load shall not cause any damage to the circuit-breaker.		N/A
	At 85% of the rated control supply voltage, the closing operation shall be performed when the current established by the circuit-breaker is equal to its rated making capacity within the limits allowed by the operation of its relays or releases and, if a maximum time is stated for the closing operation, in a time not exceeding this maximum time limit.		N/A
7.2.1.1.5	Independent power closing		
	A circuit-breaker having an independent power closing operation can be assigned a rated short-circuit making capacity irrespective of the conditions of power closing		N/A
	Means for charging the operating mechanism, as well as the closing control components, shall be capable of operating in accordance with the manufacturer's specification		N/A
7.2.1.1.6	Stored energy closing		
	Capable ensuring closing of the circuit-breaker in any condition between no-load and its rated making capacity		N/A
	- when the stored energy is retained within the circuit-breaker, a device is provided which indicates when the storing mechanism is fully charged.		N/A
	- means for charging the operating mechanism and closing control components operates when auxiliary supply voltage is between 85% and 110% of the rated control supply voltage.		N/A
	- not possible for the moving contacts to move from the open position, unless the charge is sufficient for satisfactory completion of the closing operation.		N/A
	- by manually operated circuit-breaker is the direction of operation indicated. (not for circuit-breaker with an independent manual closing operation.)		N/A
	- For trip free circuit-breaker it shall not be possible to maintain the contacts in the touching or closed position when the release is in the position to trip the circuit-breaker.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
7.2.1.2	Opening		
7.2.1.2.1	Circuit-breakers which open automatically shall be trip-free and, unless otherwise agreed between manufacturer and user, shall have their energy for the tripping operation stored prior to the completion of the closing operation		
7.2.1.2.2	Opening by undervoltage releases		
7.2.1.3. part 1	Limits of operation of under-voltage relays and releases		
7.2.1.3. a	Operating voltage		
	An under-voltage relay or release, when associated with a switching device, shall operate to open the equipment even on a slowly falling voltage within the range between 70% and 35% of its rated voltage		N/A
	An under-voltage relay or release shall prevent the closing of the equipment when the supply voltage is below 35% of the rated voltage of the relay or release; it shall permit closing of the equipment at supply voltages equal to or above 85% of its rated value		N/A
	Unless otherwise stated in the relevant product standard, the upper limit of the supply voltage shall be 110% of its rated value		N/A
7.2.1.3. b	Operating time		
	For a time-delay under-voltage relay or release, the time-lag shall be measured from the instant when the voltage reaches the operating value until the instant when the relay or release actuates the tripping device of the equipment		N/A
7.2.1.2.3	Opening by shunt releases		N/A
7.2.1.4 part 1	Limits of operation of shunt releases		
	A shunt release for opening shall cause tripping under all operating conditions of an equipment when the supply voltage of the shunt release measured during the tripping operation remains between 70% and 110% of the rated control supply voltage and, if a.c., at the rated frequency		
7.2.1.2.4	Opening by over-current releases		
a)	Opening under short-circuit conditions		
	The short-circuit release shall cause tripping of the circuit-breaker with an accuracy of 20% of the tripping current value of the current setting for all values of the current setting of the short-circuit current release		P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Where necessary for over-current co-ordination the manufacturer shall provide information (usually curves) showing		N/A
	- maximum cut-off (let-through) peak current as a function of prospective current (r.m.s. symmetrical)		N/A
	- I^2t characteristics for circuit-breakers of utilization category A and, if applicable, B for circuit-breakers with instantaneous override (see note to 8.3.5)		N/A
b)	Opening under overload conditions		
1)	Instantaneous or definite time-delay operation		N/A
	The release shall cause tripping of the circuit-breaker with an accuracy of $\pm 10\%$ of the tripping current value of the current setting for all values of current setting of the overload release		N/A
2)	Inverse time-delay operation		
	At the reference temperature and at 1,05 times the current setting with the conventional non-tripping current, the opening release being energized on all poles, tripping shall not occur in less than the conventional time from the cold state, i.e. with the circuit-breaker at the reference temperature		P
	Moreover, when at the end of the conventional time the value of current is immediately raised to 1,30 times the current setting, i.e. with the conventional tripping current, tripping shall then occur in less than the conventional time later		P
	If a release is declared by the manufacturer as substantially independent of ambient temperature, the current values of table 6 shall apply within the temperature band declared by the manufacturer, within a tolerance of 0,3%/K		N/A
	The width of the temperature band shall be at least 10 K on either side of the reference temperature		N/A
7.2.4.2	Operational performance capability		
7.2.4.2 part 1	The operational performance off-load for which the tests are made with the control circuits energized and the main circuit not energized, in order to demonstrate that the equipment meets the operating conditions specified at the upper and lower limits of supply voltage and/or pressure specified for the control circuit during closing and opening operations		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	The operational performance on-load during which the equipment shall make and break the specified current corresponding, where relevant, to its utilization category for the number of operations stated in the relevant product standard		P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

8	TESTS		
8.2.1 part 1	Materials		
8.2.1.1	Test of resistance to abnormal heat and fire		
8.2.1.1.1	Glow wire test (on equipment)		
	The glow-wire test according to IEC 60695-2-10 and IEC 60695-2-11 are performed on		
	the equipment; or		P
	sections taken from the equipment; or		P
	any parts of identical material having representative thickness		P
8.2.1.1.2	Flammability, hot wire ignition and arc ignition tests (on materials)		
	flammability test, in accordance with IEC 60695-11-10;		N/A
	hot wire ignition (HWI) test, as described in Annex M;		N/A
	arc ignition (AI) test, as described in Annex M.		N/A
8.2.4 part 1	Mechanical properties of terminals		
	Mechanical strength of terminals		
	maximum cross-sectional area of conductor (mm ²) :	240	
	diameter of thread (mm) :	8,0	
	torque (Nm) :	1,1×6,0	
	5 times on 2 separate clamping units	5 times	P
	Testing for damage to and accidental loosening of conductor (flexion test)		
	conductor of the smallest cross-sectional area (mm ²) :		
	number of conductors of the smallest cross section :		
	diameter of bushing hole (mm) :		
	height between the equipment and the platen :		
	mass at the conductor(s) (kg) :		
	135 continuous revolutions: the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A
	Pull-out test		
	force (N) :		
	1 min, the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	conductor of the largest cross-sectional area (mm ²) :		
	number of conductors of the largest cross section :		
	diameter of bushing hole (mm) :		
	height between the equipment and the platen :		
	mass at the conductor(s) (kg) :		
	135 continuous revolutions: the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A
	Pull-out test		
	force (N) :		
	1 min, the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A
	conductor of the largest and smallest cross-sectional area (mm ²) :		
	number of conductors of the smallest cross section, number of conductors of the largest cross section :		
	diameter of bushing hole (mm) :		
	height between the equipment and the platen :		
	mass at the conductor(s) (kg) :		
	135 continuous revolutions: the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A
	Pull-out test		
	force (N) :		
	1 min, the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

8.3.3	TEST SEQUENCE I: GENERAL PERFORMANCE CHARACTERISTICS DS 1, 3P, 415V, 400A, 1 sample		
8.3.3.2	Test of tripping limits and characteristic		
8.3.3.2.2	Short circuit releases		
	Sample no:	I-1	
	Rated operational voltage: Ue (V)	415	
	Rated current: In (A)	400	
	Ambient temperature 10-40 °C :	24,0°C	P
	Value of the tripping current declared by the manufacturer for a single pole, at which value they shall operate.	Instantaneous: Ir3=(4-14)Ir1	P
	Range of adjustable setting current. (A)	Ir3=(4-14)Ir1	P
	Time delay stated by the manufacturer, in the case of definite time delay releases.		P
	Electromagnetic over current releases		
	Test current: 80% of the rated, or minimum adjustable setting current: (A)		N/A
	Operating time: >0,2s in case of instantaneous releases: L1-L2: L1-L3: L2-L3: N-Lx:		N/A
	Operating time: > twice time delay stated by the manufacturer, in the case of definite time delay releases: L1-L2: L1-L3: L2-L3: N-Lx:		N/A
	Test current: 120% of the rated, or minimum adjustable setting current: (A)		N/A
	Operating time: <0,2s in case of instantaneous releases: L1-L2: L1-L3: L2-L3: N-Lx:		N/A
	Operating time: < twice time delay stated by the manufacturer, in the case of definite time delay releases: L1-L2: L1-L3: L2-L3: N-Lx:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Test current: 80% of the maximum adjustable setting current: (A)		N/A
	Operating time: >0,2s in case of instantaneous releases: L1-L2: L1-L3: L2-L3: N-Lx:		N/A
	Operating time: > twice time delay stated by the manufacturer, in the case of definite time delay releases: L1-L2: L1-L3: L2-L3: N-Lx:		N/A
	Test current: 120% of the maximum adjustable setting current: (A)		N/A
	Operating time: <0,2s in case of instantaneous releases: L1-L2: L1-L3: L2-L3: N-Lx:		N/A
	Operating time: < twice time delay stated by the manufacturer, in the case of definite time delay releases: L1-L2: L1-L3: L2-L3: N-Lx:		N/A
	Test current: tripping current declared for single pole operation (A)		N/A
	Operating time: < 0,2 s in case of instantaneous release: L1: L2: L3: N:		N/A
	Operating time: < twice time delay stated by manufacturer in case of definite time delay releases L1: L2: L3: N:		N/A
	Electronic over current releases		
	For circuit-breakers with an electronic over current release, the operation of the short-circuit releases shall be verified by one test only on each pole individually.		N/A
	Test current: 80% of the rated , or minimum adjustable setting current: (A)	$80\% \times (I_n = 4 I_{r1}) = 640A$	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Operating time: >0,2s in case of instantaneous releases: L1: L2: L3: N:	>0,2s >0,2s >0,2s -	P
	Operating time: > twice time delay stated by the manufacturer, in the case of definite time delay releases: L1: L2: L3: N:		N/A
	Test current: 120% of the rated , or minimum adjustable setting current: (A)	$120\% \times (I_n = 4I_{r1}) = 960A$	P
	Operating time: <0,2s in case of instantaneous releases: L1: L2: L3: N:	14ms 11ms 12ms -	P
	Operating time: < twice time delay stated by the manufacturer, in the case of definite time delay releases: L1: L2: L3: N:		N/A
	Test current: 80% of the maximum adjustable setting current: (A)	$80\% \times (I_n = 14I_n) = 4480A$	P
	Operating time: >0,2s in case of instantaneous releases: L1: L2: L3: N:	>0,2s >0,2s >0,2s -	P
	Operating time: > twice time delay stated by the manufacturer, in the case of definite time delay releases: L1: L2: L3: N:		N/A
	Test current: 120% of the maximum adjustable setting current: (A)	$80\% \times (I_n = 14I_n) = 6720A$	P
	Operating time: <0,2s in case of instantaneous releases: L1: L2: L3: N:	13ms 13ms 12ms -	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Operating time: < twice time delay stated by the manufacturer, in the case of definite time delay releases: L1: L2: L3: N:		N/A
8.3.3.2.3	Overload releases		
a)	Instantaneous or definite time-delay releases		
	Sample no:		
	Rated operational voltage: Ue (V)		
	Rated current: In (A)		
	Ambient temperature 10-40 °C :		N/A
	Value of the tripping current declared by the manufacturer for a single pole, at which value they shall operate.		N/A
	Range of adjustable setting current. (A)		N/A
	Time delay stated by the manufacturer, in the case of definite time delay releases.		N/A
	Test current: 90% of the rated, or minimum adjustable setting current: (A)		N/A
	Operating time: >0,2s in case of instantaneous releases:		N/A
	Operating time: > twice time delay stated by the manufacturer, in the case of definite time delay releases.		N/A
	Test current: 90% of the maximum adjustable setting current: (A)		N/A
	Operating time: >0,2s in case of instantaneous releases		N/A
	Operating time: > twice time delay stated by the manufacturer, in the case of definite time delay releases.		N/A
	Test current: 110% of the rated, or minimum adjustable setting current: (A) circuit-breaker with neutral pole: 1,2x110% (A)		N/A
	Operating time: <0,2s in case of instantaneous releases:		N/A
	Operating time: < twice time delay stated by the manufacturer, in the case of definite time delay releases.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Test current: 110% of the maximum adjustable setting current: (A) circuit-breaker with neutral pole: 1,2x110% (A)		N/A
	Operating time: <0,2s in case of instantaneous releases		N/A
	Operating time: < twice time delay stated by the manufacturer, in the case of definite time delay releases.		N/A
b)	Inverse time delay releases		
	Sample no:	I-1	
	Rated operational voltage: Ue (V)	415	
	Rated current: In (A)	400	
	For releases dependent of ambient air temperature: Reference temperature		N/A
	Test ambient temperature (°C)	24,0	P
	For releases dependent on ambient air temperature, the operating characteristics shall be verified at the reference temperature, the release being energized on all phase poles. If the test made at a different ambient temperature, a correction shall be made in accordance with the manufacturer's correction temperature/current data		N/A
	For thermal-magnetic releases independent of ambient temperature: Tests shall be made at 30°C and 20°C or 40°C, the release being energized on all phase poles		N/A
	For electronic releases, the operating characteristic shall be verified at the ambient temperature of the test room (see 6.1.1 of IEC 60947-1), the release being energised on all phase poles.		N/A
	Test ambient air temperature:	24,0	P
	Range of adjustable setting current: (A)	I _r =200-400A	P
	Releases, dependent of ambient air temperature: Reference temperature (°C)		N/A
	Thermal Magnetic releases, independent of ambient air temperature: at 30°C		N/A
	Test current: 105% of the rated, or minimum adjustable setting current: (A)	1,05×200A=210A	P
	Conventional non-tripping time: 1h when In < 63A, 2h when In > 63 A	>2h	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Test current: 130% of the rated, or minimum adjustable setting current: (A)	1,30×200A=260A	P
	For circuit-breakers having an identified neutral pole provided with an overload release (see 8.3.3.1.1), the test current at the conventional tripping current shall be multiplied by the factor 1,2.		N/A
	Conventional tripping time: <1h when $I_n < 63A$, <2h when $I_n > 63 A$	29,3s	P
	Test current: 105% of the maximum adjustable setting current: (A)	1,05×400A=420A	N/A
	Conventional non-tripping time: 1h when $I_n < 63A$, 2h when $I_n > 63 A$	>2h	N/A
	Test current: 130% of the maximum adjustable setting current: (A)	1,30×400A=520A	N/A
	For circuit-breakers having an identified neutral pole provided with an overload release (see 8.3.3.1.1), the test current at the conventional tripping current shall be multiplied by the factor 1,2.		N/A
	Conventional tripping time: <1h when $I_n < 63A$, <2h when $I_n > 63 A$	6min37s	N/A
	Thermal Magnetic releases, independent of ambient air temperature: at 20°C or 40°C		
	Test ambient air temperature:		N/A
	Test current: 105% of the rated, or minimum adjustable setting current: (A)		N/A
	Conventional non-tripping time: 1h when $I_n < 63A$, 2h when $I_n > 63 A$		N/A
	Test current: 130% of the rated, or minimum adjustable setting current: (A)		N/A
	For circuit-breakers having an identified neutral pole provided with an overload release (see 8.3.3.1.1), the test current at the conventional tripping current shall be multiplied by the factor 1,2.		N/A
	Conventional tripping time: <1h when $I_n < 63A$, <2h when $I_n > 63 A$		N/A
	Test current: 105% of the maximum adjustable setting current: (A)		N/A
	Conventional non-tripping time: 1h when $I_n < 63A$, 2h when $I_n > 63 A$		N/A
	Test current: 130% of the maximum adjustable setting current: (A)		N/A
	For circuit-breakers having an identified neutral pole provided with an overload release (see 8.3.3.1.1), the test current at the conventional tripping current shall be multiplied by the factor 1,2.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Conventional tripping time: <1h when $I_n < 63A$, <2h when $I_n > 63 A$		N/A
	An additional test, at a current specified by the manufacturer to verify the time/current characteristic of the releases conform to the curves provided by the manufacturer		
	Releases, dependent of ambient air temperature: Reference temperature (°C)		N/A
	Releases, independent of ambient air temperature: at 30°C		N/A
	Test ambient air temperature:		N/A
	Test current: at current specified by the manufacturer to verify the time/current characteristic of the releases conform to the curves provided by the manufacturer. % at the rated, or minimum adjustable setting current: (% or A)		N/A
	Tripping time acc. time/current characteristic of the releases conform to the curves provided by the manufacturer. (within the stated tolerances)		N/A
	Releases, independent of ambient air temperature: at 20°C or 40°C		
	Test ambient air temperature:		N/A
	Test current: at current specified by the manufacturer to verify the time/current characteristic of the releases conform to the curves provided by the manufacturer. % at the rated, or minimum adjustable setting current: (% or A)		N/A
	Tripping time acc. time/current characteristic of the releases conform to the curves provided by the manufacturer. (within the stated tolerances)		N/A
8.3.3.2.4	Additional test for definite time-delay releases		
a)	Time delay		
	Test is made at a current equal to 1,5 times the current setting. If the test current overlaps with another tripping characteristic (e.g. an instantaneous tripping characteristic), the trip setting and the test current shall be reduced as necessary to prevent premature tripping.		
	<u>overload releases</u> : (all phase poles loaded)		N/A
	for circuit-breakers having an identified neutral pole provided with an overload release, the test current for this release shall be 1,5 times the current setting;		N/A
	<u>short-circuit releases</u>		P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Electromagnetic release: two poles in series carrying the test current, using successively all possible combinations of poles having a short-circuit release.		N/A
	Electronic releases: on one pole chosen at random.		N/A
	Test current: 1,5 times of the rated, or minimum adjustable setting current: (A)		N/A
	Operating time, <u>overload releases</u> : (s)		N/A
	Time-delay: between the limits stated by the manufacturer:		N/A
	Operating time, <u>short-circuit releases (electromagnetic)</u> : (s) L1-L2: L1-L3: L2-L3:		N/A
	Time-delay: between the limits stated by the manufacturer:		N/A
	Operating time, <u>short-circuit releases (electronic)</u> : (s)L L2: L3:		N/A
	Time-delay: between the limits stated by the manufacturer:		N/A
	Test current: 1,5 times of the maximum adjustable setting current: (A)		N/A
	Operating time, <u>overload releases</u> : (s)		N/A
	Time-delay: between the limits stated by the manufacturer:		N/A
	Operating time, <u>short-circuit releases (electromagnetic)</u> : (s) L1-L2: L1-L3: L2-L3:		N/A
	Time-delay: between the limits stated by the manufacturer:		N/A
	Operating time, <u>short-circuit releases (electronic)</u> : (s)L L2: L3:		N/A
	Time-delay: between the limits stated by the manufacturer:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
b)	Non-tripping duration		
	Firstly, the test current equal to 1,5 times the current setting is maintained for a time interval equal to the non-tripping duration stated by the manufacturer.		
	Then, the current is reduced to the rated current and maintained at this value for twice the time-delay stated by the manufacturer. The circuit-breaker shall not trip.		
	<u>overload releases</u> : (all phase poles loaded)		N/A
	for circuit-breakers having an identified neutral pole provided with an overload release, the test current for this release shall be 1,5 times the current setting;		N/A
	<u>short-circuit releases</u>		N/A
	Electromagnetic release: two poles in series carrying the test current, using successively all possible combinations of poles having a short-circuit release.		N/A
	Electronic releases: on one pole chosen at random.		N/A
	Test current: 1,5 times of the minimum adjustable setting current: (A)		N/A
	non-tripping duration stated by the manufacturer for overload release: (s)		N/A
	non-tripping duration stated by the manufacturer for short-circuit release thermal magnetic: (s)		N/A
	non-tripping duration stated by the manufacturer for short-circuit release electronic: (s)		N/A
	Time duration of current when reduced to the rated current: shall be twice the delay-time stated by the manufacturer: (s)		N/A
	Rated current		N/A
	Operating time, <u>overload releases</u> : the circuit-breaker does not trip:		N/A
	Operating time, <u>short-circuit releases</u> (electromagnetic), shall not trip: (s) L1-L2: L1-L3: L2-L3:		N/A
	Operating time, <u>short-circuit releases (electronic)</u> , shall not trip: (s) L1: L2: L3:		N/A
	Test current: 1,5 times of maximum adjustable setting current: (A)		N/A
	non-tripping duration stated by the manufacturer for overload release: (s)		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	non-tripping duration stated by the manufacturer for short-circuit release thermal magnetic: (s)		N/A
	non-tripping duration stated by the manufacturer for short-circuit release electronic: (s)		N/A
	Time duration of current when reduced to the rated current: shall be twice the delay-time stated by the manufacturer: (s)		N/A
	Rated current		N/A
	Operating time, <u>overload releases</u> : the circuit-breaker does not trip:		N/A
	Operating time, <u>short-circuit releases</u> (electromagnetic), shall not trip: (s) L1-L2: L1-L3: L2-L3:		N/A
	Operating time, <u>short-circuit releases (electronic)</u> , shall not trip: (s) L1: L2: L3:		N/A
8.3.3.3	Test of dielectric properties, impulse withstand voltage:		
8.3.3.4 part1	The 1,2/50µs impulse voltage shall be applied five times for each polarity at intervals of 1s minimum		
	- rated impulse withstand voltage (kV) :	6,0	P
	- sea level of the laboratory:	10m	P
	- test Uimp main circuits (kV) :	9,8	P
	- test Uimp auxiliary circuits (kV) :		N/A
	- test Uimp control circuits (kV) :		N/A
	- test Uimp on open main contacts (equipment suitable for isolating) (kV) :	12,5	P
a)	Application of test voltage		P
	i) Between all terminals of the main circuit connected together (incl. control and auxiliary circuits connected to the main circuit) and the enclosure or mounting plate, with the contacts in all normal positions of operation.		P
	ii) Between each pole of the main circuit and the other poles connected together and to the enclosure or mounting plate, with the contacts in all normal positions of operation.		P
	iii) Between each control and auxiliary circuit not normally connected to the main circuit and: - the main circuit		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- other circuits		N/A
	- exposed conductive parts		N/A
	- enclosure of mounting plate		N/A
	iv) equipment suitable for isolation		P
	equipment not suitable for isolation		N/A
	- no unintentional disruptive discharge during the test's		P
	Test of dielectric properties, power frequency withstand voltage:		
	- rated insulation voltage (V) :	690	P
	- main circuits, test voltage for 1 min (V)	2200V AC	P
	- auxiliary circuits, test voltage for 1 min (V)		N/A
	- control circuits, test voltage for 1 min (V)		N/A
8.3.3.2.2	Application of test voltage		
1)	with circuit-breaker in the closed position		
	- between all live parts of all poles connected together and the frame of the circuit-breaker .	2200V AC	P
	- between each pole and all the other poles connected to the frame of the circuit-breaker	2200V AC	P
2)	with the circuit-breaker in the open position and, additionally, in the tripped position, if any.		P
	- between all live parts of all poles connected together and the frame of the circuit-breaker.	2200V AC	P
	- between the terminals of one side connected together and the terminals of the other side connected together.	2200V AC	P
b)	Control and auxiliary circuits		
1)	- between all the control and auxiliary circuits which are not normally connected to the main circuit, connected together, and the frame of the circuit-breaker.		N/A
2)	- where appropriate, between each part of the control an auxiliary circuits which may be isolated from the other parts during normal operation and all the other parts connected together.		N/A
	No unintentional disruptive discharge during the tests		N/A
(i)	the normal positions of operation include the tripped position, if any;		N/A
(ii)	circuits incorporating solid-state devices connected to the main circuit shall be disconnected for the test;		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
(iii)	circuit-breakers not declared as suitable for isolation shall be tested with the test voltage applied across the poles of the main circuit, the line terminals being connected together and the load terminals being connected together.		N/A
(iv)	For circuit-breaker suitable for isolation, the leakage current shall be measured through each pole with the contacts in the open position, at a test voltage of 1,1 U_e , and shall not exceed 0,5mA.	457V, <0,01mA	P
(v)	circuit-breakers having a rated insulation voltage greater than 1 000 V a.c. shall be tested at a voltage of $U_i + 1\,200\text{ V a.c. r.m.s.}$ or $2\,U_i$ whichever is the greater		N/A
(vi)	withdrawable circuit-breakers shall be subject to verification of impulse withstand voltage and shall be applied between the withdrawable unit's main contacts and their associated fixed contacts, in the disconnected position.		N/A
8.3.3.4	Mechanical operation and operational performance capability		
8.3.3.4.2	Construction and mechanical operation		
8.3.3.4.2.1	Construction		
	A withdrawable circuit-breaker shall be checked for the requirements stated in 7.1.2		N/A
	A circuit-breaker with stored energy operation shall be checked for compliance with 7.2.1.1.6, regarding the charge indicator and the direction of operation of manual energy storing		N/A
8.3.3.4.2.2	Mechanical operation		
	A circuit-breaker with dependent power operation shall comply with the requirements stated in 7.2.1.1.4		N/A
	A circuit-breaker with dependent power operation shall operate with the operating mechanism charged to the minimum and maximum limits stated by the manufacturer		N/A
	A circuit-breaker with stored energy operation shall comply with the requirements stated in 7.2.1.6 with the auxiliary supply voltage at 85% and 110% of the rated control supply voltage.		N/A
	It shall also be verified that the moving contacts cannot be moved from the open position when the operating mechanism is charged to slightly below the full charge as evidenced by the indicating device		N/A
	For a trip-free circuit-breaker it shall not be possible to maintain the contacts in the touching or closed position when the tripping release is in the position to trip the circuit-breaker		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	If the closing and opening times of a circuit-breaker are stated by the manufacturer, such times shall comply with the stated values		N/A
8.3.3.4.2.3	Undervoltage releases		
	Undervoltage releases shall comply with the requirements of 7.2.1.3 of Part 1. For this purpose, the release shall be fitted to a circuit-breaker having the maximum current rating for which the release is suitable		N/A
i)	Drop out voltage		
	It shall be verified that the release operates to open the circuit-breaker between the voltage limits specified		N/A
	The voltage shall be reduced from rated voltage at a rate to reach 0 V in approximately 30 s		N/A
	The test for the lower limit is made without current in the main circuit and without previous heating of the release coil		N/A
	In the case of a release with a range of rated voltages, this test applies to the maximum voltage of the range		N/A
	The test for the upper limit is made starting from a constant temperature corresponding to the application of rated control supply voltage to the release and rated current in the main poles of the circuit-breaker		N/A
	This test may be combined with the temperature-rise test of 8.3.3.7		N/A
	In the case of a release with a range of rated voltages, this test is made at both the minimum and maximum rated control supply voltages		N/A
ii)	Test for limits of operation		
	Starting with the circuit-breaker open, at the temperature of the test room, and with the supply voltage at 30% rated maximum control supply voltage, it shall be verified that the circuit-breaker cannot be closed by the operation of the actuator		N/A
	When the supply voltage is raised to 85% of the minimum control supply voltage, it shall be verified that the circuit-breaker can be closed by the operation of the actuator		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
iii)	Performance under overvoltage conditions		
	With the circuit-breaker closed and without current in the main circuit, it shall be verified that the undervoltage release will withstand the application of 110% rated control supply voltage for 4 h without impairing its functions		N/A
8.3.3.4.2.4	Shunt releases(AC220/230V, 50Hz)		
	Shunt releases shall comply with the requirements of 7.2.1.4 of Part 1. For this purpose, the release shall be fitted to a circuit-breaker having the maximum rated current for which the release is suitable		N/A
	It shall be verified that the release will operate to open the circuit-breaker at 70% rated control supply voltage when tested at an ambient temperature of + 55 °C $\pm$ 2 °C without current in the main poles of the circuit-breaker		N/A
	In the case of a release having a range of rated control supply voltages, the test voltage shall be 70% of the minimum rated control supply voltage		N/A
8.3.3.4.3	Operational performance capability without current.		
	Type designation or serial number catalogue reference	DS 1	
	Sample no:	I-1	
	Rated current I _n (A)	400	
	Rated operational voltage: U _e (V)	415	
	Rated control supply voltage of closing releases: U _c (V)	-	
	Rated control supply voltage of shunt releases: U _c (V)	-	
	Rated control supply voltage undervoltage releases: U _c (V)	-	
	Ambient temperature 10-40 °C :	27,0	P
	Number of operating cycles per hour	60	P
	Number of cycles without current (total) (closing releases energized at the rated U _c)	4000	P
	Number of cycles without current (without releases)		N/A
	Applied voltage of closing releases (V)		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	10% of total cycles for circuit-breaker with fitted shunt release: (50% at the beginning- and 50% at the end of the test.) Energized at the rated U_c		N/A
	Applied voltage: shunt releases (V)		N/A
	10% of total cycles for circuit-breaker with undervoltage releases: (50% at the beginning- and 50% at the end of the test.) Energized at the minimum rated U_c		N/A
	10 attempts to close the breaker without applied voltage at the undervoltage releases. (Shall not possible to close the circuit-breaker.)		N/A
	Applied voltage: undervoltage releases (V)		N/A
	In the case of circuit-breakers fitted with electrical or pneumatic closing devices, these devices shall be supplied at their rated control supply voltage or at their rated pressure.		N/A
	Precautions shall be taken to ensure that the temperature rises of the electrical components do not exceed the value indicated in tab. 7.		N/A
8.3.3.4.4	Operational performance capability with current.		
	Rated current: I_n (A)	400	
	Maximum rated operational voltage: U_e (V)	415	
	Conductor cross-sectional area (mm^2) :	240	P
	Number of operating cycles per hour	60	P
	Number of cycles with current (total) (closing releases energized at the rated U_c)	1000	P
	Applied voltage: closing releases (V)		N/A
	For circuit-breaker fitted with adjustable releases, test shall be made with the overload setting at maximum and short-circuit setting at minimum.		N/A
	Conditions, make/break operations:		P
	- test voltage $U/U_e = 1,0$ (V)L1:L2:L3:	421 420 420	P
	- test current $I/I_e = 1,0$ (A)L1:L2:L3:	411 410 409	P
	- power factor/time constant:	0,78	P
	- frequency: (Hz)	50Hz	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- on-time (ms):	228	P
	- off-time (s):	60	P
	In the case of circuit-breakers fitted with electrical or pneumatic closing devices, these devices shall be supplied at their rated control supply voltage or at their rated pressure.		P
	Precautions shall be taken to ensure that the temperature rises of the electrical components do not exceed the value indicated in tab. 7.		P
8.3.3.4.5	Additional test of operational performance capability without current for withdrawable circuit-breaker.		
	Number of operations cycles : 100		N/A
	After test, the isolating contacts, withdrawable mechanism and interlocks shall be suitable for further service.		N/A
8.3.3.5	Overload performance		
	this test applies to circuit-breaker of rated current up to and including 630 A		
	Type designation or serial number catalogue reference	DS 1	
	Sample no:	I-1	
	Rated current I_n (A)	400	
	Rated operational voltage: U_e (V)	415	
	Rated control supply voltage of closing releases: U_c (V)		
	Rated control supply voltage of shunt releases: U_c (V)		
	Rated control supply voltage undervoltage releases: U_c (V)		
	Ambient temperature 10-40 °C :	27	P
	Maximum rated operational voltage: U_e (V)	415	P
	Number of operating cycles per hour	60	P
	Number of cycles with current (total) (closing releases energized at the rated U_c)	12	P
	Applied voltage: closing releases (V)		N/A
	For circuit-breaker fitted with adjustable releases, test shall be made with the overload/short-circuit settings at maximum.		N/A
	Conditions, overload operations:		P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- test voltage $U/U_e = 1,05$ (V)L1:L2:L3:	441 440 440	P
	- test current AC/DC: $I/I_e = 6,0/2.5$ (A)L1:L2:L3:	2451 2450 2449	P
	- power factor/time constant:	0,48	P
	- Number of cycles manually opened: 9	9	P
	- Number of cycles automatically opened by an overload release: 3	3	P
	for circuit-breakers having a short-circuit release of a maximum setting less than the test current		
	all 12 operations automatic		N/A
	If the testing means do not withstand the let-through energy occurring during the automatic operation		
	- 12 manual operations - three additional operations with automatic opening, made at any convenient voltage		N/A
	- frequency: (Hz)		N/A
	- on-time max 2s:		N/A
	Operating rate if different from Table 8		N/A
8.3.3.6	Verification of dielectric withstand		
	- equal to twice the rated operational voltage with a minimum of 1000 V for 5 seconds	1000V	P
	- no breakdown or flashover		P
	For circuit-breaker suitable for isolation, the leakage current shall be measured through each pole with the contacts in the open position, at a test voltage of 1,1 U_e , and shall not exceed 2 mA.	457V, Max. <0,03mA	P
8.3.3.7	Verification of temperature-rise		
	- the values of temperature-rise do not exceed those specified in tab. 7.		P
	Temperature rise of main circuit terminals ≤ 80 K (K) :	Max. 64	P
	conductor cross-sectional area (mm ²) :	240	P
	test current I_e (A) :	400	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
8.3.3.8	Verification of overload releases		
	Test current: 1.45 times the value of their current setting at the reference temperature: (A)	1,45×400=580A	P
	Conventional tripping time: <1h when $I_n < 63A$, <2h when $I_n > 63 A$	4min51s <2h	P
8.3.3.9	Verification of undervoltage and shunt releases		
	Circuit-breaker fitted with undervoltage releases. The release shall not operate at 70% of the minimum control supply voltage -		N/A
	and shall operate at 35% of the maximum control supply voltage.		N/A
	Circuit-breaker fitted with shunt releases. The release shall operate at 70% of the minimum rated control supply voltage. Test made at room temperature.		N/A
8.3.3.10	Verification of the main contact position for circuit-breakers for isolation		P
	actuating force for opening (N) :	128N	—
	test force with blocked main contacts for 10 s (N) :	400N, 10s	—
	Dependent power operation		N/A
	Supply voltage of 110% of rated voltage (V).....:		N/A
	Three attempts of 5 s to operate the equipment at intervals of 5 min.		N/A
	Independent power operation		N/A
	Three attempts to operate the equipment by the stored energy.		N/A
	Lock ability of driving mechanism in OFF-position at test force and blocked main contacts :		N/A
	Position indicator does not show OFF-position after capture of test force at blocked main contacts		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

8.3.4	TEST SEQUENCE II (Ics): Rated service short-circuit breaking capacity DS 1, 3P, 415V, 400A, 50kA, 1 sample		
8.3.4.2	Test of rated service short-circuit breaking capacity		
	Test sequence of operation: O – t – CO – t – CO		
	Type designation or serial number catalogue reference	DS 1	
	Sample no:	II-1	
	Rated current: In (A)	400	
	Corresponding rated voltage: Ue (V)	415	
	Rated service short-circuit breaking capacity: (kA)	50	
	Rated control supply voltage of closing releases: Uc (V)		
	Rated control supply voltage of shunt release: Uc (V)		
	Rated control supply voltage of undervoltage releases: Uc (V)		N/A
	For circuit-breaker fitted with adjustable releases, test shall be made with the current and time settings at maximum.	$I_r=1,0I_n$, $I_i=14I_n$	P
	Closing releases energized with 85% at the rated Uc: (V)		N/A
	The circuit-breaker is mounted complete on its own support or an equivalent support.		P
	Test made in free air:		P
	Distances of the metallic screen's: (all sides)	Front: 0mm back: 0mm Left: 0mm; right: 0mm Top: 100mm; bottom: 100mm	P
	The characteristics of the metallic screen:		
	- woven wire mesh		N/A
	- perforated metal		P
	- expanded metal		N/A
	- ratio hole area/total area: 0,45-0,65	0,5	P
	- size of hole: <30mm ²	25	P
	- finish: bare or conductive plating	Bare	P
	Test made in specified individual enclosure: Details of these tests, including the dimensions of the enclosure:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Fuse "F": copper wire: diameter 0,8 mm, 50 mm long		P
	Circuit is earthed at: (load-star- or supply-star point)	Supply-star point	P
	Conductor cross-sectional area (mm ²) :	240	P
	If terminals unmarked: line connected at: (underside/upside)		N/A
	Tightening torques: (Nm)	10	P
	Test sequence of operation: O – t – CO – t – CO		P
	- test voltage U/U _e = 1,05 (V)L1:L2:L3:	441 439 440	P
	- r.m.s. test current AC/DC: (A)L1:L2:L3:	50,9×10 ³ 50,9×10 ³ 50,9×10 ³	P
	power factor/time constant :	0,23	P
	- Factor "n"	2,1	P
	- peak test current (A) :	108	P
	Test sequence "O"		
	- max. let-through current: (kA _{peak})L1:L2:L3:	28,1 23,4 23,8	P
	- Joule integral I ² dt (A ² s)L1:L2:L3:	1,50×10 ⁶ 1,20×10 ⁶ 1,28×10 ⁶	P
	Pause, t: (min)	3min	P
	Test sequence "CO"		
	- max. let-through current: (kA _{peak})L1:L2:L3:	23,2 26,8 30,1	P
	- Joule integral I ² dt (A ² s)L1:L2:L3:	1,14×10 ⁶ 1,21×10 ⁶ 1,70×10 ⁶	P
	Pause, t: (min)	3min	P
	Test sequence "CO"		
	- max. let-through current: (kA _{peak})L1:L2:L3:	29,3 18,6 25,3	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- Joule integral I^2dt (A ² s)L1:L2:L3:	1,40×10 ⁶ 1,01×10 ⁶ 1,25×10 ⁶	P
	Melting of the fusible element	3min	P
	Damage to insulation on conductors	No	P
	Holes in the PE-sheet for test sequence "O"	No	P
	Cracks observed	No cracks	P
8.3.4.3	Operational performance capability with current.		
	Rated current: I_n (A)	400	
	Maximum rated operational voltage: U_e (V)	415	
	Conductor cross-sectional area (mm ²) :	240	
	Number of operating cycles per hour	60	P
	Number (5% of the number given in column 4, tab. 8) of cycles with current (total) (closing releases energized at the rated U_c)	50	P
	Applied voltage: closing releases (V)		N/A
	For circuit-breaker fitted with adjustable releases, test shall be made with the overload setting at maximum and short-circuit setting at minimum.		N/A
	Conditions, make/break operations:		
	- test voltage $U/U_e = 1,0$ (V)L1:L2:L3:	421 420 420	P
	- test current $I/I_e = 1,0$ (A)L1:L2:L3:	406 406 406	P
	- power factor/time constant:	0,78	P
	- frequency: (Hz)	50	P
	- on-time (ms):	209	P
	- off-time (s):	60	P
8.3.4.4	Verification of dielectric withstand		
	- equal to twice the rated operational voltage with a minimum of 1000 V	1000V	P
	- no breakdown or flashover		P
	- the leaking current for circuit-breaker suitable for isolation: (<2mA / 1.1 U_e)	457V, max. <0,4mA	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
8.3.4.5	Verification of temperature-rise		
	- the values of temperature-rise do not exceed those specified in tab. 7.		P
	Temperature rise of main circuit terminals. $\leq 80 \text{ K (K)}$:	Max. 68	P
	conductor cross-sectional area (mm^2) :	240	P
	test current I_e (A) :	400	P
8.3.4.6	Verification of overload releases		
	Test current: 1.45 times the value of their current setting at the reference temperature: (A)	$1,45I_n=580\text{A}$	P
	Conventional tripping time: <1h when $I_n < 63\text{A}$, <2h when $I_n > 63 \text{ A}$	4min53s	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

8.3.4	TEST SEQUENCE II/III (Ics=Icu):		
8.3.4.2	Test of rated service short-circuit breaking capacity		
	Test sequence of operation: O – t – CO – t – CO		
	Type designation or catalogue reference		
	Sample no:		
	Rated current: In (A)		
	Corresponding rated voltage: Ue (V)		
	Rated service short-circuit breaking capacity: (kA)		
	Rated control supply voltage of closing releases: Uc (V)		
	Rated control supply voltage of shunt release: Uc (V)		
	Rated control supply voltage of undervoltage releases: Uc (V)		
	For circuit-breaker fitted with adjustable releases, test shall be made with the current and time settings at maximum.		N/A
	Closing releases energized with 85% at the rated Uc: (V)		N/A
	The circuit-breaker is mounted complete on its own support or an equivalent support.		N/A
	Test made in free air:		N/A
	Distances of the metallic screen's: (all sides)		N/A
	The characteristics of the metallic screen:		
	- woven wire mesh		N/A
	- perforated metal		N/A
	- expanded metal		N/A
	- ratio hole area/total area: 0,45-0,65		N/A
	- size of hole: <30mm ²		N/A
	- finish: bare or conductive plating		N/A
	Test made in specified individual enclosure: Details of these tests, including the dimensions of the enclosure:		N/A
	Fuse "F": copper wire: diameter 0,8 mm, 50 mm long		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Circuit is earthed at: (load-star- or supply-star point)		N/A
	Conductor cross-sectional area (mm ²) :		N/A
	If terminals unmarked: line connected at: (underside/upside)		N/A
	Tightening torques: (Nm)		N/A
8.3.5.2	The operation of overload releases shall be verified at twice the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:		N/A
	- Operation time: (s)L1:L2:L3:N :		N/A
8.3.4.2	Test of rated service short-circuit breaking capacity		N/A
	Test sequence of operation: O – t – CO – t – CO		N/A
	- test voltage U/U _e = 1,05 (V)L1:L2:L3:		N/A
	- r.m.s. test current AC/DC: (A)L1:L2:L3:		N/A
	power factor/time constant :		N/A
	- Factor "n"		N/A
	- peak test current (A) :		N/A
	Test sequence "O"		
	- max. let-through current: (kA _{peak})L1:L2:L3:		N/A
	- Joule integral I ² dt (A ² s)L1:L2:L3:		N/A
	Pause, t: (min)	3min	N/A
	Test sequence "CO"		
	- max. let-through current: (kA _{peak})L1:L2:L3:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- Joule integral I^2dt (A ² s)L1:L2:L3:		N/A
	Pause, t: (min)		N/A
	Test sequence "CO"		
	- max. let-through current: (kA _{peak})L1:L2:L3:		N/A
	- Joule integral I^2dt (A ² s)L1:L2:L3:		N/A
	Melting of the fusible element		N/A
	Damage to insulation on conductors		N/A
	Holes in the PE-sheet for test sequence "O"		N/A
	Cracks observed		N/A
8.3.4.3	Operational performance capability with current.		
	Rated current: I _n (A)		
	Maximum rated operational voltage: U _e (V)		
	Conductor cross-sectional area (mm ²) :		
	Number of operating cycles per hour		N/A
	Number (5% of the number given in column 4, tab. 8) of cycles with current (total) (closing releases energized at the rated U _c)		N/A
	Applied voltage: closing releases (V)		N/A
	For circuit-breaker fitted with adjustable releases, test shall be made with the overload setting at maximum and short-circuit setting at minimum.		N/A
	Conditions, make/break operations:		
	- test voltage U/U _e = 1,0 (V)L1:L2:L3:		N/A
	- test current I/I _e = 1,0 (A)L1:L2:L3:		N/A
	- power factor/time constant:		N/A
	- frequency: (Hz)		N/A
	- on-time (ms):		N/A
	- off-time (s):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
8.3.4.4	Verification of dielectric withstand		
	- equal to twice the rated operational voltage with a minimum of 1000 V		N/A
	- no breakdown or flashover		N/A
	- the leaking current for circuit-breaker suitable for isolation: ($<2\text{mA} / 1,1 U_e$)		N/A
8.3.4.5	Verification of temperature-rise		
	- the values of temperature-rise do not exceed those specified in tab. 7.		N/A
	Temperature rise of main circuit terminals. $\leq 80 \text{ K (K)}$:		N/A
	conductor cross-sectional area (mm^2) :		N/A
	test current I_e (A) :		N/A
8.3.4.6	Verification of overload releases		
	Test current: 1,45 times the value of their current setting at the reference temperature: (A)		N/A
	Conventional tripping time: $<1\text{h}$ when $I_n < 63\text{A}$, $<2\text{h}$ when $I_n > 63 \text{ A}$		N/A
8.3.5.5	Verification of overload releases		
	The operation of overload releases shall be verified at 2,5 times the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:		N/A
	- Operation time: (s) L1: L2: L3: N :		N/A

IEC 60947-2			
Clause	Requirement + Test		Verdict
8.3.5	TEST SEQUENCE III (Icu): Rated ultimate short-circuit breaking capacity DS 1, 3P, 415V, 400A, 85kA, 1 sample		
	Rated ultimate short-circuit breaking		
	Except where the combined test sequence applies, this test sequence applies to circuit-breaker of utilization category A and to circuit-breaker of utilization B having a rated ultimate short-circuit breaking capacity higher than the rated short-time withstand current.		
	For circuit-breakers of utilization B having a rated short-time withstand current equal to their rated ultimate short-circuit breaking capacity, this test sequence need not be made, since, in this case, the ultimate short-circuit breaking capacity, is verified when carrying out test sequence IV.		
	For integrally fused circuit-breakers, test sequence V applies in place of this sequence.		
	Type designation or catalogue reference	DS 1	
	Sample no:	/III-1	
	Rated current: In (A)	400	
	Corresponding rated voltage: Ue (V)	415	
	Rated ultimate short-circuit breaking capacity: (kA)	85	
	Rated control supply voltage of closing releases: Uc (V)	-	
	Rated control supply voltage of shunt release: Uc (V)	-	
	Rated control supply voltage of undervoltage releases: Uc (V)	-	
	This test sequence need not be made when Icu = Ics		
8.3.5.2	Verification of overload releases		
	The operation of overload releases shall be verified at twice the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:	≤10min, 2×In	P
	- Operation time: (s)L1:L2:L3: N :	2min35s 2min34s 2min38s -	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
8.3.5.3	Test of rated ultimate short-circuit breaking capacity		
	The test sequence of operations is O – t – CO		
	For circuit-breaker fitted with adjustable releases, test shall be made with the current and time settings at maximum.		P
	Closing releases energized with 85% at the rated U_c : (V)		N/A
	The circuit-breaker is mounted complete on its own support or an equivalent support.		N/A
	Test made in free air:		P
	Distances of the metallic screen's: (all sides)	Front: 0mm back: 0mm Left: 0mm; right: 0mm Top: 100mm; bottom: 100mm	P
	The characteristics of the metallic screen:		
	- woven wire mesh		N/A
	- perforated metal		P
	- expanded metal		N/A
	- ratio hole area/total area: 0,45-0,65	0,5	P
	- size of hole: <30mm ²	25	P
	- finish: bare or conductive plating	Bare	P
	Test made in specified individual enclosure: Details of these tests, including the dimensions of the enclosure:		N/A
	Fuse "F": copper wire: diameter 0,8 mm, 50 mm long		P
	Circuit is earthed at: (load-star- or supply-star point)	supply-star	P
	Conductor cross-sectional area (mm ²) :	240	P
	If terminals unmarked: line connected at: (underside/upside)		N/A
	Tightening, torques: (Nm)	10	P
	Test sequence of operation: O – t – CO		P
	- test voltage $U/U_e = 1,05$ (V)L1:L2:L3:	440 441 439	P
	- r.m.s. test current AC/DC: (A).....L1:L2:L3:	85,4 85,3 85,2	P
	power factor/time constant:	0,18	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- Factor "n"	2,2	P
	- peak test current (Amax) :	190	P
	Test sequence "O"		
	- max. let-through current: (kA _{peak})L1:L2:L3:	45,8 49,3 39,2	P
	- Joule integral I ² dt (A ² s)L1:L2:L3:	4,67×10 ⁶ 3,03×10 ⁶ 2,77×10 ⁶	P
	Pause, t: (min)	3min	P
	Test sequence "CO"		
	- max. let-through current: (kA _{peak})L1:L2:L3:	46,9 45,5 29,6	P
	- Joule integral I ² dt (A ² s)L1:L2:L3:	4,65×10 ⁶ 2,74×10 ⁶ 1,37×10 ⁶	P
	Melting of the fusible element	3min	P
	Damage to insulation on conductors	No	P
	Holes in the PE-sheet for test sequence "O"	No	P
	Cracks observed	No cracks	P
8.3.5.4	Verification of dielectric withstand		
	- equal to twice the rated operational voltage with a minimum of 1000 V for 5 seconds	1000V	P
	- no breakdown or flashover		P
	- the leaking current for circuit-breaker suitable for isolation: (<6mA / 1,1 U _e)	457V, max. <0,7mA	P
8.3.5.5	Verification of overload releases		
	The operation of overload releases shall be verified at 2,5 times the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:	≤ 10min, 2,5I _n	P
	- Operation time: (s)L1:L2:L3:N :	1min41s 1min37s 1min39s -	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

8.3.6	TEST SEQUENCE IV (Icw): Rated short-time withstand current DS 1, 3P, 415V, 400A, 5kA/1s, 1 sample		
	Except where the combined test sequence applies, this test sequence applies to circuit-breakers of utilization category B and to those circuit-breaker of category A with an intentional short time delay at the assigned short time delay		
	Where integrally fused circuit-breaker are of utilization category B, they shall meet the requirements of this sequence.		
	Type designation or catalogue reference	DS 1	P
	Sample no:	IV-1	P
	Rated current: In (A)	400	P
	Corresponding rated voltage: Ue (V)	415	P
	Rated short-time withstand current: (kA/s)	5	P
	Rated frequency: (Hz)	50	P
8.3.6.2	Verification of overload releases		
	The operation of overload releases shall be verified at twice the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:	≤10min, 2×In	P
	- Operation time: (s)L1:L2:L3: N :	2min40s 2min36s 2min37s -	P
8.3.6.3	Test of rated short-time withstand current.		
	For this test, any over-current release, including the instantaneous override, if any, likely to operate during the test, shall be rendered inoperative.		
	- test frequency: (Hz)	50	P
	- duration of the test: (s)	1	P
	- power factor / time constant (ms):	0,70	P
	- factor "n"	1,5	P
	- test voltage: (V)L1:L2:L3:	438 438 438	P
	- r.m.s. test current: (kA)L1:L2:L3:	5,06 5,06 5,06	P
	- highest peak current: (kA)	7,5	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
8.3.6.4	Verification of temperature-rise		
	- the values of temperature-rise do not exceed those specified in tab. 7.		P
	Temperature rise of main circuit terminals. $\leq 80 \text{ K (K)}$:	Max. 70K	P
	conductor cross-sectional area (mm ²) :	240	P
	test current I_e (A) :	400	P
8.3.6.5	Test of short-circuit breaking capacity at the max. short-time withstand current.		
	Rated short-time withstand current: (kA/s)		
	Test sequence: O – t – CO		
	max. available time setting of the short-time delay short-circuit release. (s)	0,4	P
	- test voltage $U/U_e = 1,05 \text{ (V)}$L1:L2:L3:	438 438 438	P
	- r.m.s. test current AC/DC: (A)L1:L2:L3:	5,06kA 5,06kA 5,06kA	P
	- test frequency: (Hz)	50	P
	- power factor / time constant (ms):	0,68	P
	- factor "n"	1,5	P
	Test sequence "O"		
	- max. let-through current: (kA _{peak})L1:L2:L3:	7,14 7,13 7,16	P
	- Joule integral I^2dt (A ² s)L1:L2:L3:	$10,6 \times 10^6$ $10,6 \times 10^6$ $10,8 \times 10^6$	P
	- the circuit-breaker shall remain closed for the short-time corresponding to the max. available time setting of the short-time delay short-circuit release and -		P
	- the instantaneous override, if any, shall not operate.		P
	-pause: t (min)	3	P
	Test sequence "CO"		
	- max. let-through current: (kA _{peak})L1:L2:L3:	7,15 7,16 7,16	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- Joule integral I^2dt (A ² s)L1:L2:L3:	10,9×10 ⁶ 10,9×10 ⁶ 11,0×10 ⁶	P
	- the circuit-breaker shall remain closed for the short-time corresponding to the max. available time setting of the short-time delay short-circuit release and -		P
	- the instantaneous override, if any, shall not operate.		P
	- if the circuit-breaker has a making current release, this requirement does not apply to the CO operation, if the prospective current exceeds the pre-determined value, since it will then operate; this exception also applies to the test on the fourth pole and the adjacent pole, in accordance with 8.3.2.6.4.3.		N/A
8.3.6.6	Verification of dielectric withstand		P
	- equal to twice the rated operational voltage with a minimum of 1000 V	1000V	
	- no breakdown or flashover		P
	- For circuit-breaker suitable for isolation, the leakage current shall be measured through each pole with the contacts in the open position, at a test voltage of 1,1 U _e , and shall not exceed 2 mA.	457V, max. <0,04mA	P
8.3.6.7	Verification of overload releases		P
	The operation of overload releases shall be verified at 2,5 times the value of their current setting on each pole separately.		
	The operating time shall not exceed the maximum value stated by the manufacturer for twice the value of the current setting, at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:		
	- Operation time: (s)L1:L2:L3: N :	2min07s 2min01s 2min03s -	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

8.3.7	TEST SEQUENCE V: Performance of integrally fused circuit-breakers		
	STAGE 1		
	Type designation or catalogue reference		
	Sample no:		
	Rated current: I_n (A)		
	Corresponding rated voltage: U_e (V)		
	Value of prospective current equal to the selectivity limit current, as declared by the manufacturer. (kA)		
	Type of integrated fuses (all details)		
	Rated control supply voltage of closing releases: U_c (V)		
	Rated control supply voltage of shunt release: U_c (V)		
	Rated control supply voltage of undervoltage releases: U_c (V)		
8.3.7.2	Short-circuit at the selectivity limit current		
	Test sequences "O"		
	Fuses shall be fitted		N/A
	For circuit-breaker fitted with adjustable releases, test shall be made with the current and time settings at maximum.		
	Closing releases energized with 85% at the rated U_c : (V)		N/A
	The circuit-breaker is mounted complete on its own support or an equivalent support.		N/A
	Test made in free air:		N/A
	Distances of the metallic screen's: (all sides)		N/A
	The characteristics of the metallic screen:		
	- woven wire mesh		N/A
	- perforated metal		N/A
	- expanded metal		N/A
	- ratio hole area/total area: 0,45-0,65		N/A
	- size of hole: <30mm ²		N/A
	- finish: bare or conductive plating		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Test made in specified individual enclosure: Details of these tests, including the dimensions of the enclosure:		N/A
	Fuse "F": copper wire: diameter 0,8 mm, 50 mm long		N/A
	Circuit is earthed at: (load-star- or supply-star point)		N/A
	Conductor cross-sectional area (mm ²) :		N/A
	If terminals unmarked: line connected at: (underside/upside)		N/A
	Tightening torques: (Nm)		N/A
	- test voltage U/U _e = 1,05 (V)L1:L2:L3:		N/A
	- r.m.s. test current AC/DC: (A)L1:L2:L3:		N/A
	power factor/time constant :		N/A
	- factor "n"		N/A
	- peak test current (A _{max}) :		N/A
	Test sequence "O"		
	- max. let-through current: (kA _{peak})L1:L2:L3:		N/A
	- Joule integral I ² dt (A ² s)L1:L2:L3:		N/A
	- fuses shall still intactL1:L2:L3:		N/A
8.3.7.3	Verification of temperature-rise		N/A
	- the values of temperature-rise do not exceed those specified in tab. 7.		N/A
	Temperature rise of main circuit terminals. ≤ 80 K (K) :		N/A
	conductor cross-sectional area (mm ²) :		N/A
	test current I _e (A) :		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
8.3.7.4	Verification of dielectric withstand		
	- equal to twice the rated operational voltage with a minimum of 1000 V		N/A
	- no breakdown or flashover		N/A
	- the leaking current for circuit-breaker suitable for isolation: (<2mA / 1,1 Ue)		N/A

8.3.7	TEST SEQUENCE V: Performance of integrally fused circuit-breakers		
	STAGE 2		
	Type designation or catalogue reference		
	Sample no:		
	Rated current: In (A)		
	Corresponding rated voltage: Ue (V)		
	1.1 time the value of prospective current equal to the selectivity limit current, as declared by the manufacturer. (kA)		
	Type of integrated fuses (all details)		
	Rated control supply voltage of closing releases: Uc (V)		
	Rated control supply voltage of shunt release: Uc (V)		
	Rated control supply voltage of undervoltage releases: Uc (V)		
8.3.7.5	Verification of overload releases		N/A
	The operation of overload releases shall be verified at twice the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:		
	- Operation time: (s)L1:L2:L3: N :		N/A
8.3.7.6	Short-circuit at 1,1 times the take-over current		
	Test sequences "O"		
	Fuses shall be fitted		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	For circuit-breaker fitted with adjustable releases, test shall be made with the current and time settings at maximum.		
	closing releases energized with 85% at the rated U_c : (V)		N/A
	The circuit-breaker is mounted complete on its own support or an equivalent support.		N/A
	Test made in free air:		N/A
	Distances of the metallic screen's: (all sides)		N/A
	The characteristics of the metallic screen:		
	- woven wire mesh		N/A
	- perforated metal		N/A
	- expanded metal		N/A
	- ratio hole area/total area: 0,45-0,65		N/A
	- size of hole: <30mm ²		N/A
	- finish: bare or conductive plating		N/A
	Test made in specified individual enclosure: Details of these tests, including the dimensions of the enclosure:		N/A
	Fuse "F": copper wire: diameter 0.8 mm, 50 mm long		N/A
	Circuit is earthed at: (load-star- or supply-star point)		N/A
	Conductor cross-sectional area (mm ²) :		N/A
	If terminals unmarked: line connected at: (underside/upside)		N/A
	Tightening torques: (Nm)		N/A
	1.1 time the value of prospective current equal to the selectivity limit current, as declared by the manufacturer. (kA)		
	- test voltage $U/U_e = 1,05$ (V)L1:L2:L3:		N/A
	- r.m.s. test current AC/DC: (A)L1:L2:L3:		N/A
	power factor/time constant :		N/A
	- factor "n"		N/A
	- peak test current (A_{max}) :		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Test sequence "O"		
	- max. let-through current: (kA _{peak}) L1: L2: L3:		N/A
	- Joule integral I ² dt (A ² s) L1: L2: L3:		N/A
	- at least two of the fuses shall have blown L1: L2: L3:		N/A
8.3.7.7	Short-circuit at rated ultimate short-circuit breaking capacity		
	Type designation or catalogue reference		
	Sample no:		
	Rated current: I _n (A)		
	Rated operational voltage: U _e (V)		
	Rated ultimate short-circuit breaking capacity. (kA)		
	Type of integrated fuses (all details)		
	Rated control supply voltage of closing releases: U _c (V)		
	Rated control supply voltage of shunt release: U _c (V)		
	Test sequences: O – t – CO		
	Fuses shall be fitted		N/A
	For circuit-breaker fitted with adjustable releases, test shall be made with the current and time settings at maximum.		
	Closing releases energized with 85% at the rated U _c : (V)		N/A
	The circuit-breaker is mounted complete on its own support or an equivalent support.		N/A
	Test made in free air:		N/A
	Distances of the metallic screen's: (all sides)		N/A
	The characteristics of the metallic screen:		
	- woven wire mesh		N/A
	- perforated metal		N/A
	- expanded metal		N/A
	- ratio hole area/total area: 0,45-0,65		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- size of hole: <30mm ²		N/A
	- finish: bare or conductive plating		N/A
	Test made in specified individual enclosure: Details of these tests, including the dimensions of the enclosure:		N/A
	Fuse "F": copper wire: diameter 0,8 mm, 50 mm long		N/A
	Circuit is earthed at: (load-star- or supply-star point)		N/A
	Conductor cross-sectional area (mm ²) :		N/A
	If terminals unmarked: line connected at: (underside/upside)		N/A
	Tightening torques: (Nm)		N/A
	- test voltage U/U _e = 1,05 (V)L1:L2:L3:		N/A
	- r.m.s. test current AC/DC: (A)L1:L2:L3:		N/A
	power factor/time constant :		N/A
	- factor "n"		N/A
	- peak test current (A) :		N/A
	Test sequence "O"		
	- max. let-through current: (kA _{peak})L1:L2:L3:		N/A
	- Joule integral I ² dt (A ² s)L1:L2:L3:		N/A
	Pause: t (s)		N/A
	new fitted fuses		N/A
	Test sequence "CO"		
	- max. let-through current: (kA _{peak})L1:L2:L3:		N/A
	- Joule integral I ² dt (A ² s)L1:L2:L3:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
8.3.7.8	Verification of dielectric withstand		
	- equal twice time rated operational voltage with a minimum of 1000 V (new fuses fitted)		N/A
	- no breakdown or flashover		N/A
	- the leaking current for circuit-breaker suitable for isolation: (< 6mA / 1,1 Ue)		N/A
8.3.7.9	Verification of overload releases		
	The operation of overload releases shall be verified at 2.5 times the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:		
	- Operation time: (s)L1:L2:L3:N :		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

8.3.8	TEST SEQUENCE VI: Combined test sequence		
	At the discretion of, or in agreement with the manufacturer, this sequence may be applied to circuit-breaker of utilization cat. B:		
	Type designation or catalogue reference		N/A
	Sample no:		N/A
	Rated current: In (A)		N/A
	Corresponding rated voltage: Ue (V)		N/A
	Rated short-time withstand current: (kA/s)		N/A
	Rated frequency: (Hz)		N/A
8.3.8.2	Verification of overload releases		
	The operation of overload releases shall be verified twice times the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:		
	- Operation time: (s)L1:L2:L3: N :		N/A
8.3.8.3	Test of rated short-time withstand current.		
	For this test, any over-current release, including the instantaneous override, if any, likely to operate during the test, shall be rendered inoperative.		
	- test frequency: (Hz)		N/A
	- duration of the test: (s)		N/A
	- power factor / time constant (ms) :		N/A
	- factor "n"		N/A
	- test voltage: (V)L1:L2:L3:		N/A
	- r.m.s. test current: (kA)L1:L2:L3:		N/A
	- highest peak current: (kA)		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
8.3.8.4	Test of rated service short-circuit breaking capacity		
	At the highest voltage applicable to the rated short-time current.		
	Test sequence of operation: O – t – CO – t – CO		
	Type designation or catalogue reference		
	Sample no:		
	Rated current: In (A)		
	Corresponding rated voltage: Ue (V)		
	Rated service short-circuit breaking capacity: (kA)		
	Rated control supply voltage of closing releases: Uc (V)	-	
	Rated control supply voltage of shunt release: Uc (V)	-	
	Rated control supply voltage of undervoltage releases: Uc (V)	-	
	For circuit-breaker fitted with adjustable releases, test shall be made with the current and time settings at maximum.		
	closing releases energized with 85% at the rated Uc: (V)		N/A
	The circuit-breaker is mounted complete on its own support or an equivalent support.		N/A
	Test made in free air:		N/A
	Distances of the metallic screen's: (all sides)		N/A
	The characteristics of the metallic screen:		
	- woven wire mesh		N/A
	- perforated metal		N/A
	- expanded metal		N/A
	- ratio hole area/total area: 0,45-0,65		N/A
	- size of hole: <30mm ²		N/A
	- finish: bare or conductive plating		N/A
	Test made in specified individual enclosure: Details of these tests, including the dimensions of the enclosure:		N/A
	Fuse "F": copper wire: diameter 0,8 mm, 50 mm long		N/A
	Circuit is earthed at: (load-star- or supply-star point)		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Conductor cross-sectional area (mm ²) :		N/A
	If terminals unmarked: line connected at: (underside/upside)		N/A
	Tightening torques: (Nm)		N/A
	Test sequence of operation: O – t – CO – t – CO		N/A
	The highest voltage applicable to the rated short-time current.		N/A
	- test voltage U/Ue = 1,05 (V)L1:L2:L3:		N/A
	- r.m.s. test current AC/DC: (A)L1:L2:L3:		N/A
	power factor/time constant :		N/A
	- Factor "n"		N/A
	- peak test current (A) :		N/A
	Test sequence "O"		
	- max. let-through current: (kA _{peak})L1:L2:L3:		N/A
	- Joule integral I ² dt (A ² s)L1:L2:L3:		N/A
	Pause, t: (min)	3min	N/A
	Test sequence "CO"		
	- max. let-through current: (kA _{peak})L1:L2:L3:		N/A
	- Joule integral I ² dt (A ² s)L1:L2:L3:		N/A
	Pause, t: (min)	3min	N/A
	Test sequence "CO"		
	- max. let-through current: (kA _{peak})L1:L2:L3:		N/A
	- Joule integral I ² dt (A ² s)L1:L2:L3:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	The circuit-breaker shall remain closed for the short-time corresponding to the max. available time setting of the short-time delay short-circuit release.		N/A
	During this test the instantaneous override shall not operate		N/A
	- and the making current release shall operate		N/A
8.3.8.5	Operational performance capability with current.		
	Rated current: I_n (A)		N/A
	Maximum rated operational voltage: U_e (V)		N/A
	Conductor cross-sectional area (mm^2) :		N/A
	Number of operating cycles per hour		N/A
	Number (5% of the number given in column 4, tab. 8) of cycles with current (total) (closing releases energized at the rated U_c)		N/A
	Applied voltage: closing releases (V)		N/A
	For circuit-breaker fitted with adjustable releases, test shall be made with the overload setting at maximum and short-circuit setting at minimum.		N/A
	Conditions, make/break operations:		N/A
	- test voltage $U/U_e = 1,0$ (V) L1: L2: L3:		N/A
	- test current $I/I_e = 1,0$ (A) L1: L2: L3:		N/A
	- power factor/time constant:		N/A
	- frequency: (Hz)		N/A
	- on-time (ms):		N/A
	- off-time (s):		N/A
8.3.8.6	Verification of dielectric withstand		N/A
	- equal to twice the rated operational voltage with a minimum of 1000 V		
	- no breakdown or flashover		N/A
	- the leaking current for circuit-breaker suitable for isolation: ($<2\text{mA} / 1,1 U_e$)		N/A
8.3.8.7	Verification of temperature-rise		
	- the values of temperature-rise do not exceed the those specified in tab. 7.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Temperature rise of main circuit terminals. $\leq 80 \text{ K (K)}$:		N/A
	conductor cross-sectional area (mm ²) :		N/A
	test current I_e (A) :		N/A
8.3.8.8	Verification of overload releases		
	Test current: 1,45 times the value of their current setting at the reference temperature: (A)		N/A
	Conventional tripping time: <1h when $I_n < 63\text{A}$, <2h when $I_n > 63 \text{ A}$		N/A
	The operation of overload releases shall be verified at 2,5 times the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:		
	- Operation time: (s)L1:L2:L3: N :		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

8.3.9	Critical d.c. load current test		
	This test applies only to circuit-breakers with d.c. ratings.		
	The test shall be made at the maximum operational d.c. voltage (Ue max) assigned by the manufacturer to the circuit-breaker.		N/A
	Ue max :		N/A
	For circuit-breakers fitted with adjustable releases, the test shall be made with the releases set at the maximum.		N/A
	Direction of current flow as specified by manufacturer		
	Direction of current flow as specified by manufacturer as indicated by the polarity and line/load marking:		N/A
	The circuit-breaker is closed and opened 5 times:		N/A
	Direction of current flow is not specified by manufacturer		
	5 operations in the forward direction and 5 in the reverse direction		N/A
	During each CO cycle, the circuit-breaker shall remain closed for a time sufficient to ensure that the full current is established, but not exceeding 2 s.		N/A
	Time constant operational performance		N/A
	Number of operating cycles per hour		N/A
	Arcing time during the test (shall not exceed 1 s)		N/A
	Test currents to find critical d.c. load current		
	The test current values shall be: 4 A, 8 A, 16 A, 32 A and 63 A d.c., with $\pm 10\%$ tolerance, but not exceeding the rated current		N/A
	the critical value is determined by taking the maximum mean arcing time, for each direction of current if applicable		N/A
	The highest and lowest values of test current shall demonstrate shorter mean arcing times than the critical value		N/A
	the range of test currents (upwards or downwards) to find the critical value		N/A
	No critical value of current is found		N/A
	Forward direction		
	Critical value		N/A
	Maximum mean arcing time		N/A
	Reversed direction		
	Critical value :		N/A
	Maximum mean arcing time		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Operational performance verification of 50 operations with critical current.		
	Conductor cross-sectional area (mm ²)		N/A
	Direction of critical current flow :		N/A
	Conditions operational performance		
	- test voltage $U/U_e = 1,0$ (V)		N/A
	- critical test current (A)		N/A
	- time constant.....		N/A
	- on-time (ms)		N/A
	- off-time (s)		N/A
8.3.3.6	Verification of dielectric withstand		
	- equal to twice the rated operational voltage with a minimum of 1415 Vdc for 5 seconds		N/A
	- no breakdown or flashover		N/A
	For circuit-breaker suitable for isolation, the leakage current shall be measured through each pole with the contacts in the open position, at a test voltage of $1,1 U_e$, and shall not exceed 2 mA.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Annex B	Circuit-breakers incorporating residual current protection		
B.3	Classification		
B.3.1	Classification according to the method of operation of the residual current function		
B.3.1.1	CBR functionally independent of line voltage		
B.3.1.2	CBR functionally dependent on line voltage		
B.3.1.2.1	Opening automatically in the case of failure of the line voltage with or without delay.		
B.3.1.2.2	Not opening automatically in the case of failure of line voltage.		
B.3.2	Classification according to the possibility of adjusting the residual operating current		
B.3.2.1	CBR with single rated residual operating current		
B.3.2.2	CBR with multiple settings of residual operating current	Fixed steps/continuous	
B.3.3	Classification according to time-delay of the residual current function		
B.3.3.1	CBR without time-delay: non-time-delayed type		
B.3.3.2	CBR with time-delay: time-delayed type		
B.3.3.2.1	CBR with non-adjustable time-delay		
B.3.3.2.2	CBR with adjustable time-delay	Fixed steps/continuous	
B.3.4	Classification according to behaviour in presence of a d.c. component	CBR of type AC / type A / type B	
B.4	Characteristics of CBRs concerning their residual current function		
B.4.1.1	Rated residual operating current ($I_{\Delta n}$)		
B.4.1.2	Rated residual non-operating current ($I_{\Delta no}$)		
B.4.1.3	Rated residual short-circuit making and breaking capacity ($I_{\Delta m}$)		
B.4.2	Preferred and limiting values		
	Preferred values of the rated residual operating current ($I_{\Delta n}$)		
	The minimum value of rated residual non-operating current is 0,5 $I_{\Delta n}$.		
	Limiting value of the non-operating overcurrent in the case of a single-phase load in a multiphase circuit		
B.4.2.4	Operating characteristics		
	The value of the rated voltage of the voltage source of CBRs		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	For a time-delay type, the limiting non-actuating time is defined at $2 I_{\Delta n}$ and shall be declared by the manufacturer.		N/A
	For CBR's having a limiting non-actuating time higher than 0,06 s, the manufacturer shall declare the maximum break time at $I_{\Delta n}$, $2 I_{\Delta n}$, $5 I_{\Delta n}$, and $10 I_{\Delta n}$.		N/A
	In the case of a CBR having an inverse current/time characteristic, the manufacturer shall state the residual current/break time characteristic.		N/A
B.4.3	Value of the rated residual short-circuit making and breaking capacity ($I_{\Delta m}$)		
	The minimum value of $I_{\Delta m}$ is 25 % of I_{cu} .		N/A
B.5.	Marking		
	Visible from the front when the circuit-breaker is installed as in service and the actuator is accessible		
B1.1	the operating means of the test device by the letter "T"		N/A
B1.2	operating characteristic in the case of residual currents in the presence or absence of a d.c. component: - for CBRs of type AC with the symbol  - for CBRs of type A with the symbol  - for CBRs of type B with the symbol  or   		N/A
B1.3	rated residual operating current $I_{\Delta n}$		N/A
	Marked on the product		
B2.1	settings of residual operating current, when applicable		N/A
B2.2	limiting non-actuating time at $2 I_{\Delta n}$, for time-delay type, by the symbol Δt followed by the limiting non-actuating time		N/A
B2.3	suitability for use with a 3-phase supply only, with the symbol 		N/A
B2.4	rated voltage(s) if different from the rated voltage(s) of the circuit-breaker (only for r.c. unit)		N/A
B2.5	value (or range) of the rated frequency if different from that of the circuit-breaker (only for r.c. unit)		N/A
B2.6	the indication $I_n \leq \dots A$ (I_n being the maximum current rating of the circuit-breaker with which the r.c. unit may be combined) (only for r.c. unit)		N/A
B2.7	manufacture's name or trade mark (only for r.c. unit)		N/A
B2.8	type designation or catalogue reference (only for r.c. unit)		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
B2.9	Identification of the circuit-breaker(s) with which the r.c. unit may be assembled, unless incorrect assembly (such as render the protection ineffective) is made impossible by design (only for r.c. unit)		N/A
B2.10	IEC 60947-2		N/A
	Provided in the manufacturer's literature		
B3.1	rated residual short-circuit making and breaking capacity $I_{\Delta m}$ if higher than 25 % of I_{cu}		N/A
B3.2	diagram of connections, including those of the test circuit and, if applicable, those to the line, for CBRs dependent on the line voltage		N/A
B3.3	Value of rated residual non-operating current $I_{\Delta no}$ if greater than 0,5 $I_{\Delta n}$		N/A
B.8.	Tests		
B.8.1.1	This clause specifies tests for CBRs having a rated residual operating current $I_{\Delta n}$ up to and including 30 A.		
	The applicability of the tests specified in this clause when $I_{\Delta n} > 30$ A is subject to agreement between manufacturer and user.		
	The instruments for the measurement of the residual current shall show (or permit to determine) the true r.m.s. value.		N/A
B.8.1.2	Tests to be made during the test sequences of clause 8		N/A
B.8.1.2.1	General performance characteristics (test sequence I)		
	For the verification of the short-circuit releases of electronic overcurrent releases, the pole under test shall be tested in series with one other pole chosen at random;		
	For the verification of the short-circuit releases of electromagnetic overcurrent releases, the additional verification in single pole need not be carried out;		
	For the verification of the overload release of the neutral pole, this pole shall be tested in series with 2 other poles connected in parallel;		
	Alternatively, when feasible and with the agreement of the manufacturer, the residual current function may be rendered inoperative;		
	The test conditions shall be stated in the test report.		
	During the operating cycles with current a third of the breaking operations shall be performed by actuating the test device, and a further third by applying a residual current of value $I_{\Delta n}$ (or, if applicable, of the lowest setting of the residual operating current) to any one pole.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	In the case of a reset-CBR, it is not possible to reclose the CBR after tripping without the intentional resetting action. This verification shall take place at the beginning and at the end of the operational performance capability test with current		N/A
	No failure to trip shall be admitted.		N/A
B.8.1.2.2	Verification of the withstand capability to short-circuit currents		
B.8.1.2.2.1	Rated service short-circuit breaking capacity (test sequence II)		
	Following the tests of 8.3.4, verification of the correct operation of the CBR in case of residual current shall be performed in accordance with B.8.2.4.2.		
B.8.2.4.2	Verification of operating in case of steady increase of the residual current (figure B.1)		
	Increase the residual current from 0,2 I Δ n to I Δ n in 30 sec. Required: value between I Δ n0 and I Δ n		N/A
	Min. setting I Δ n.(mA): Interm. setting I Δ n.(mA): Max. setting I Δ n.(mA):		N/A
B.8.1.2.2.2	Rated ultimate short-circuit breaking capacity (test sequence III)		
	The correct operation of the overload releases of 8.3.5.2 and 8.3.5.5 by two-pole tests, on all possible combinations of phase poles in turn		N/A
	Following the tests of 8.3.5, verification of the correct operation of the CBR shall be performed in accordance with B.8.2.4.4.		N/A
B.8.2.4.4	Verification of operating in case of a sudden appearance of the residual current (figure B.1)		N/A
	A residual current is sudden appear on the CBR of I Δ n Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the CBR of 2 I Δ n Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of ☐ 5 I Δ n or ☐ 0,25 A Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I Δ n or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
B.8.1.2.2.3	Rated short-time withstand current (test sequence IV or test sequence VI (combined))		
	a) Behaviour during rated short-time withstand current test No tripping shall occur during the test of 8.3.6.3 or 8.3.8.3, as applicable.		N/A
	b) Verification of overload releases test sequence IV For the purpose of verifying the correct operation of the overload releases in accordance with 8.3.6.2 and 8.3.6.7, the single pole tests specified in 8.3.5.2 shall be replaced by two-pole tests, made on all possible combinations of phase poles in turn.		N/A
	b) Verification of overload releases for combined test sequence. For the purpose of verifying the correct operation of the overload releases in accordance with 8.3.8.2, the single pole test specified in 8.3.5.2 shall be replaced by two-pole tests made on all possible combinations of phase poles in turn.		N/A
	b) For the purpose of verifying the correct operation of overload releases in accordance with 8.3.8.6, the test specified in 8.3.8.7 shall be made using a three-phase supply.		N/A
	c) Verification of the residual current tripping device Following the tests of 8.3.6 or 8.3.8, as applicable, verification of the residual current tripping device shall be performed in accordance with B.8.2.4.4.		N/A
B.8.2.4.4	Verification of operating in case of a sudden appearance of the residual current (figure B.1)		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}(\text{ms})$: Interm. setting $I_{\Delta n}(\text{ms})$: Max. setting $I_{\Delta n}(\text{ms})$:		N/A
	A residual current is sudden appear on the CBR of $2 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}(\text{ms})$: Interm. setting $I_{\Delta n}(\text{ms})$: Max. setting $I_{\Delta n}(\text{ms})$:		N/A
	A residual current is sudden appear on the CBR of $\square 5 I_{\Delta n}$ or $\square 0,25 \text{ A}$ Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}(\text{ms})$: Interm. setting $I_{\Delta n}(\text{ms})$: Max. setting $I_{\Delta n}(\text{ms})$:		N/A
	A residual current is sudden appear on the CBR of $\square 10 I_{\Delta n}$ or $\square 0,5 \text{ A}$ Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}(\text{ms})$: Interm. setting $I_{\Delta n}(\text{ms})$: Max. setting $I_{\Delta n}(\text{ms})$:		N/A
B.8.1.2.2.4	Integrally fused circuit-breakers (test sequence V)		
	For the purpose of verifying the correct operation of the overload releases, the single-pole tests specified in 8.3.7.5 and 8.3.7.9 shall be replaced by two-pole tests, on all possible combinations of phase poles in turn, the test conditions being as specified in 8.3.7.5 and 8.3.7.9 but applicable to two poles.		N/A
	Following the tests of 8.3.8, verification of the correct operation of the CBR shall be performed in accordance with B.8.2.4.4.		N/A
B.8.2.4.4	Verification of operating in case of a sudden appearance of the residual current (figure B.1)		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}(\text{ms})$: Interm. setting $I_{\Delta n}(\text{ms})$: Max. setting $I_{\Delta n}(\text{ms})$:		N/A
	A residual current is sudden appear on the CBR of $2 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}(\text{ms})$: Interm. setting $I_{\Delta n}(\text{ms})$: Max. setting $I_{\Delta n}(\text{ms})$:		N/A
	A residual current is sudden appear on the CBR of $\square 5 I_{\Delta n}$ or $\square 0,25 \text{ A}$ Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}(\text{ms})$: Interm. setting $I_{\Delta n}(\text{ms})$: Max. setting $I_{\Delta n}(\text{ms})$:		N/A
	A residual current is sudden appear on the CBR of $\square 10 I_{\Delta n}$ or $\square 0,5 \text{ A}$ Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}(\text{ms})$: Interm. setting $I_{\Delta n}(\text{ms})$: Max. setting $I_{\Delta n}(\text{ms})$:		N/A
B.8.1.2.2.5	Test sequence VI (combined)		N/A
	Following the tests of 8.3.8, verification of the correct operation of the CBR shall be performed in accordance with B.8.2.4.4.		N/A
B.8.2.4.4	Verification of operating in case of a sudden appearance of the residual current (figure B.1)		N/A
	A residual current is sudden appear on the CBR of $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
	A residual current is sudden appear on the CBR of $2 I\Delta n$ Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
	A residual current is sudden appear on the CBR of $\square 5 I\Delta n$ or $\square 0,25 A$ Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
	A residual current is sudden appear on the CBR of $\square 10 I\Delta n$ or $\square 0,5 A$ Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
B I	Test sequence B I		
	Tests shall be made at the following values of voltage applied to the relevant terminals: - 0,85 times the minimum rated voltage for the tests specified in B.8.2.4 and B.8.2.5.2; - 1,1 times the maximum rated voltage for the tests specified in B.8.2.5.3.		N/A
	For CBRs having functions which do not depend on line voltage, tests of those functions may be carried out at any convenient voltage		N/A
	CBRs with more than one rated frequency or a range of rated frequencies shall be tested in each case at the highest and lowest rated frequency. However, for CBRs rated at 50 Hz and 60 Hz, tests at 50 Hz or 60 Hz are considered to cover the requirements.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	For tests where the poles are loaded with rated current or above (see for example B.8.2.5.3, B.8.7.2.3, B.8.8.8.2 and B.8.8.9), for practical reasons, it is acceptable to use a sample where the voltage pick-up conductors have been separated from the phase poles, to allow the use of a low-power source for the loading with current. The details of the modification shall be agreed between the manufacturer and the testing station, and shall be stated in the test report.		N/A
B.8.2.4	Off-load test at 20 °C ± 5 °C		
B.8.2.4.2	Verification of operating in case of steady increase of the residual current (figure B.1)		N/A
	Increase the residual current from 0,2 I Δ n to I Δ n in 30 sec. Required: value between I Δ no and I Δ n		N/A
	Min. setting I Δ n.(mA): Interm. setting I Δ n.(mA): Max. setting I Δ n.(mA):		N/A
B.8.2.4.3	Verification of operating in case of closing on residual current (figure B.1)		N/A
	The CBR is closes on I Δ n or each specified setting Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
B.8.2.4.4	Verification of operating in case of a sudden appearance of the residual current (figure B.1)		N/A
	A residual current is sudden appear on the CBR of I Δ n Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the CBR of 2 I Δ n Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of □ 5 I _{Δn} or □ 0,25 A Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I _{Δn} .(ms): Interm. setting I _{Δn} .(ms): Max. setting I _{Δn} .(ms):		N/A
	A residual current is sudden appear on the CBR of □ 10 I _{Δn} or □ 0,5 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I _{Δn} .(ms): Interm. setting I _{Δn} .(ms): Max. setting I _{Δn} .(ms):		N/A
B.8.2.4.5	Verification of the limiting non-operating time of time delayed type CBRs		
	A residual current is sudden appear on the CBR of 2 I _{Δn} for a time declared by the manufacturer Required : The CBR shall not operate		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
B.8.2.5	Tests at the temperature limits		N/A
B.8.2.5.1	General		
	Minimum temperature (°C)		
	Maximum temperature (°C)		
B.8.2.5.2	Verification of operating in case of a sudden appearance of the residual current at – 5°C or minimum temperature limit		N/A
	A residual current is sudden appear on the CBR of I _{Δn} Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I _{Δn} .(ms): Interm. setting I _{Δn} .(ms): Max. setting I _{Δn} .(ms):		N/A
	A residual current is sudden appear on the CBR of 2 I _{Δn} Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 5 $I_{\Delta n}$ or ☐ 0,25 A Required : no value exceeds the specified limiting value of Table B1: (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A
	Verification of the limiting non-operating time of time delayed type CBRs at -5°C or minimum temperature limit		
	A residual current is sudden appear on the CBR of 2 $I_{\Delta n}$ for a time declared by the manufacturer Required : The CBR shall not operate		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
B.8.3.5.3	Verification of operating in case of a sudden appearance of the residual current at $+40^{\circ}\text{C}$		N/A
	A residual current is sudden appear on the CBR of $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A
	A residual current is sudden appear on the CBR of 2 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of ☐ 5 I Δ n or ☐ 0,25 A Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I Δ n or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the CBR of 2 I Δ n for a time declared by the manufacturer Required : The CBR shall not operate		N/A
	Min. setting I Δ n. Max. setting time delay (ms):		N/A
B.8.3	Verification of dielectric properties		N/A
8.3.3.3	Verification of rated impulse withstand voltage		N/A
	rated impulse withstand voltage		
	test impulse voltage (see table 12 part 1)		
	test impulse voltage for isolating (see table 14 part 1)		
B.8.4	Verification of the operation of the test device at the limits of the rated voltage		N/A
	For CBRs having an adjustable time-delay the test is made at the maximum setting of time-delay:	_____ s	
B.8.4.a	Setting I Δ n or minimum setting of I Δ n	_____ A	
	Test voltage (1,1 x U _e max)	_____ V	
	Number of operations	25	
	Tripping	☐ Yes / ☐ No	N/A
B.8.4.b	Setting I Δ n or maximum setting of I Δ n	_____ A	
	Test voltage (0,85 x U _e min)	_____ V	
	Number of operations	3	
	Tripping	☐ Yes / ☐ No	N/A
B.8.4.c	Setting I Δ n or minimum setting of I Δ n	_____ A	

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Test voltage (1,1 x U _e max)	_____ V	
	Number of operations	1	
	Operating means of the test device held in close position	5 s	
	Tripping	☐ Yes / ☐ No	N/A
B.8.5	Verification of the limiting value of non-operating current under overcurrent conditions, in case of a single phase load.		N/A
	Setting I Δ n or minimum setting of I Δ n if adjustable	_____ A	
	Test current equal to the lower value of: ☐ 6 x I _n or ☐ 80 % of the maximum short-circuit release current setting	_____ A	
	Test voltage: ☐ rated voltage or ☐ any convenient voltage	_____ V	
	Test frequency	_____ Hz	
	Power factor (0,5)	_____	
	Current flow time	2 s	
	Interval time	60 s	
	Calibration plot number	_____	
	No tripping / change of state		N/A
B.8.6	Resistance against unwanted tripping due to surge currents resulting from impulse voltages		N/A
B.8.6.1	General		
	For CBRs with adjustable time delays (see B.3.3.2.2), the time delay shall be set at minimum.		N/A
B.8.6.2	Verification of the resistance to unwanted tripping in case of loading of the network capacitance		N/A
	Current surge test for CBR (0,5 μ s / 100kHz ring wave test)		
	One pole of the CBR is submitted to 10 applications of a surge current according to the following requirements:		
	- peak value: 200 A + 10/0%		
	- virtual front time: 0,5 μ s $\pm$ 30%		
	- period of the following oscillatory wave: 10 μ s $\pm$ 20%		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- each successive peak: about 60% of the preceding peak		
	The polarity shall be inverted after every two applications		
	The interval between two consecutive applications shall be about 30 s		
	During the test the CBR shall not trip:	-	N/A
B.8.6.4	Verification after the tests		N/A
	After the tests of B.8.6.2, the operation of the CBR shall be verified in accordance with B.8.2.4.4, with one measurement of break time, at $I_{\Delta n}$ only.		N/A
B.8.6.3	Verification of the resistance to unwanted tripping in case of flashover without follow-on current.		N/A
	Verification of behaviour at surge current up to 250 A (8/20 μ s surge current test)		N/A
	One pole of the CBR is submitted to 10 applications of a surge current according to the following requirements:		
	- peak value: 250 A + 10/0%		
	- virtual front time: 8 μ s $\pm$ 20%		
	- virtual time to half value: 20 μ s $\pm$ 20%		
	- peak of reverse current: less than 30% of peak value		
	The polarity shall be inverted after every two applications		
	The interval between two consecutive applications shall be about 30 s		
	During the test, the CBR shall not trip:		N/A
B.8.6.4	Verification after the tests		N/A
	After the tests of B.8.6.3, the operation of the CBR shall be verified in accordance with B.8.2.4.4, with one measurement of break time, at $I_{\Delta n}$ only.		N/A
	Verification of behaviour at surge current up to 3000 A (8/20 μ s surge current test)		N/A
	One pole of the CBR is submitted to 10 applications of a surge current according to the following requirements:		
	- peak value: 3000 A + 10/0%		
	- virtual front time: 8 μ s $\pm$ 20%		
	- virtual time to half value: 20 μ s $\pm$ 20%		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	The polarity shall be inverted after every two applications		
	The interval between two consecutive applications shall be about 30 s		
	During the test, the CBR with tripping allowed:		N/A
B.8.6.4	Verification after the tests		N/A
	After the tests of B.8.6.3, the operation of the CBR shall be verified in accordance with B.8.2.4.4, with one measurement of break time, at $I_{\Delta n}$ only.		N/A
B.8.7	Additional verifications for CBRs of types A and B		N/A
B.8.7.1	Test conditions		
	The test conditions of B.8 and B.8.2.1, B.8.2.2 and B.8.2.3 apply, except that the test circuits shall be those shown in Figure B.8 and Figure B.9, as applicable.		N/A
	Tests shall be made at the following values of voltage applied to the relevant terminals: - 0,85 times the minimum rated voltage for the tests specified in B.8.2.4 and B.8.2.5.2; - 1,1 times the maximum rated voltage for the tests specified in B.8.2.5.3.		N/A
	For CBRs having functions which do not depend on line voltage, tests of those functions may be carried out at any convenient voltage		N/A
	CBRs with more than one rated frequency or a range of rated frequencies shall be tested in each case at the highest and lowest rated frequency. However, for CBRs rated at 50 Hz and 60 Hz, tests at 50 Hz or 60 Hz are considered to cover the requirements.		N/A
	For tests where the poles are loaded with rated current or above (see for example B.8.2.5.3, B.8.7.2.3, B.8.8.8.2 and B.8.8.9), for practical reasons, it is acceptable to use a sample where the voltage pick-up conductors have been separated from the phase poles, to allow the use of a low-power source for the loading with current. The details of the modification shall be agreed between the manufacturer and the testing station, and shall be stated in the test report.		N/A
B.8.7.2.1	Verification of operation in case of a continuous rise of a residual pulsating direct current		N/A
	Rated voltage	_____ V	

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- steady increase from zero to: 1,4 I _{Δn} for I _{Δn} > 0,030 A with 1,4 I _{Δn} /30 A/s (mA)	_____ mA	
	- steady increase from zero to: 2 I _{Δn} for I _{Δn} ≤ 0,030 A with 2 I _{Δn} /30 A/s (mA)	_____ mA	
	- angle = 0 (+/-) :		
	- angle = 90 (+/-) :		
	- angle = 135 (+/-) :		
	No value exceeds the relevant specified limiting values		N/A
B.8.7.2.2	Verification of operation in case of a suddenly appearing residual pulsating direct current		N/A
	Verification of the correct operation in case of suddenly appearing residual pulsating direct currents by closing S2 (angle = 0°)		N/A
	Rated voltage	_____ V	
	CBR's with I _{Δn} > 0,030 A:		
	- maximum break time (ms) at: 1,4 I _{Δn} (+/-) :		
	- maximum break time (ms) at: 2,8 I _{Δn} (+/-) :		
	- maximum break time (ms) at: 7 I _{Δn} (+/-) :		
	- maximum break time (ms) at: 14 I _{Δn} (+/-) :		
	No value exceeds the relevant specified limiting value		N/A
	CBR's with I _{Δn} ≤ 0,030 A:		N/A
	- maximum break time (ms) at: 2 I _{Δn} (+/-) :		
	- maximum break time (ms) at: 4 I _{Δn} (+/-) :		
	- maximum break time (ms) at: 10 I _{Δn} (+/-) :		
	- maximum break time (ms) at: 20 I _{Δn} (+/-) :		
	No value exceeds the relevant specified limiting value		N/A
B.8.7.2.3	Verification of operation with load at reference temperature		N/A
B.8.7.2.1	Verification of operation in case of a continuous rise of a residual pulsating direct current		N/A
	Rated voltage	_____ V	
	- steady increase from zero to: 1,4 I _{Δn} for I _{Δn} > 0,030 A with 1,4 I _{Δn} /30 A/s (mA)	_____ mA	
	- steady increase from zero to: 2 I _{Δn} for I _{Δn} ≤ 0,030 A with 2 I _{Δn} /30 A/s (mA)	_____ mA	

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- angle = 0 (+/-) :		
	- angle = 90 (+/-) :		
	- angle = 135 (+/-) :		
	No value exceeds the relevant specified limiting values		N/A
B.8.7.2.2	Verification of operation in case of a suddenly appearing residual pulsating direct current		N/A
	Verification of the correct operation in case of suddenly appearing residual pulsating direct currents by closing S2 (angle = 0°)		N/A
	Rated voltage	_____ V	
	CBR's with $I_{\Delta n} > 0,030$ A:		
	- maximum break time (ms) at: 1,4 $I_{\Delta n}$ (+/-) :		
	- maximum break time (ms) at: 2,8 $I_{\Delta n}$ (+/-) :		
	- maximum break time (ms) at: 7 $I_{\Delta n}$ (+/-) :		
	- maximum break time (ms) at: 14 $I_{\Delta n}$ (+/-) :		
	No value exceeds the relevant specified limiting value		N/A
	CBR's with $I_{\Delta n} \leq 0,030$ A:		N/A
	- maximum break time (ms) at: 2 $I_{\Delta n}$ (+/-) :		
	- maximum break time (ms) at: 4 $I_{\Delta n}$ (+/-) :		
	- maximum break time (ms) at: 10 $I_{\Delta n}$ (+/-) :		
	- maximum break time (ms) at: 20 $I_{\Delta n}$ (+/-) :		
	No value exceeds the relevant specified limiting value		N/A
B.8.7.2.4	Verification of operation of a residual pulsating direct current superimposed by a smooth direct current of 6 mA.		N/A
	Rated voltage	_____ V	
	- steady increase from zero to: 1,4 $I_{\Delta n}$ for $I_{\Delta n} > 0,030$ A with 1,4 $I_{\Delta n}/30$ A/s (mA) + 6 mA	_____ mA	
	- steady increase from zero to: 2 $I_{\Delta n}$ for $I_{\Delta n} \leq 0,030$ A with 2 $I_{\Delta n}/30$ A/s (mA) + 6 mA	_____ mA	
	- angle = 0 (+/-) :		
	No value exceeds the relevant specified limiting values		N/A
B.8.8	Additional verifications for CBRs of type B		N/A
	Tested voltage (V):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
B.8.8.1	Verification of the correct operation in the case of a steady increase of composite residual current		N/A
	The ratio of the different frequencies shall be maintained from the initial value up to the operating value		
	The test shall be repeated three times through one pole chosen at random.		
	Increase the residual current from $0,2 I_{\Delta n}$ to $1,4 I_{\Delta n}$ in 30 sec. Required: value between $0,5 I_{\Delta n}$ and $I_{\Delta n}$		N/A
	Min. setting $I_{\Delta n}$.(mA):		N/A
B.8.8.2	Verification of the correct operation in the case of a steady increase of composite residual current		N/A
	The CBR is closes on $5 \times 1,4 I_{\Delta n}$		
	Required : non-time-delayed type, no value exceeds 40 ms		
	Required : time-delayed type, no value exceeds the specified limiting value of Table B.2 (150 ms) or the value declared by the manufacturer for $5 I_{\Delta n}$		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	Time-delayed CBRs shall additionally be tested to verify the non-actuating time:		N/A
	A residual current is sudden appear on the CBR of $2 I_{\Delta n}$ for a duration of 0,06 s or declared by the manufacturer		
	Required : Three tests are made, at an interval of at least 1 min. The CBR shall not operate		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
B.8.8.3	Verification of the correct operation in the case of residual sinusoidal alternating currents up to 1000 Hz		N/A
a)	The test shall be carried out on one pole taken at random and repeated five times:		
	Test switch S_1 and S_2 and CBR in closed position		
	Test at 150Hz:		
	steady increase from max. $0,2 I_{\Delta n}$ to $2,4 I_{\Delta n}$ within 30s		
	- tripping current between $0,5 I_{\Delta n}$ and $2,4 I_{\Delta n}$ (mA)		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$ (mA):		N/A
	Test at 400Hz:		
	steady increase from max. $0,2 I_{\Delta n}$ to $6 I_{\Delta n}$ within 30s		
	- tripping current between $0,5 I_{\Delta n}$ and $6 I_{\Delta n}$ (mA)		
	Min. setting $I_{\Delta n}$ (mA):		N/A
	Test at 1000Hz:		
	steady increase from max. $0,2 I_{\Delta n}$ to $14 I_{\Delta n}$ within 30s		
	- tripping current between $I_{\Delta n}$ and $14 I_{\Delta n}$ (mA)		
	Min. setting $I_{\Delta n}$ (mA):		N/A
b)	Two measurements of the break time are made on one pole chosen at random:		
	S1 and CBR in the closed position, residual current correspond to 1000Hz suddenly established by closing S2, the CBR is closes on $14 I_{\Delta n}$		
	Required : non-time-delayed type, no value exceeds 300 ms		
	Required : time-delayed type, no value exceeds 0,5 s or the value declared by the manufacturer for $I_{\Delta n}$		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
B.8.8.4	Verification of the correct operation in the case of a residual alternating current superimposed on a residual smooth direct current		N/A
	Test switch S ₁ and S ₂ and CBR in closed position		
	Residual smooth direct current applied through one pole chosen at random and adjusted to		
	- $0,4 I_{\Delta n}$ or		
	- 10 mA		
	whichever is the higher value		
	Residual alternating current at rated frequency applied to another pole and:		
	steady increase from max. $0,2 I_{\Delta n}$ to $I_{\Delta n}$ within 30 s		
	alternating tripping current $\leq I_{\Delta n}$ (mA)		
	CBR tested twice at positions I of S ₃ :		
	Min. setting $I_{\Delta n}$ (mA):		N/A
	CBR tested twice at positions II of S ₃ :		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$.(mA):		N/A
B.8.8.5	Verification of the correct operation in the case of a residual pulsating direct current superimposed on a residual smooth direct current		N/A
	Test switch S_1 and S_2 and CBR in closed position		
	Residual smooth direct current applied through one pole chosen at random and adjusted to		
	- 0,4 $I_{\Delta n}$ or		
	- 10 mA		
	whichever is the higher value		
	Residual pulsating direct current applied to another pole with a current delay angle of 0° and:		
	steady increase from max. 0,2 $I_{\Delta n}$ to 1,4 $I_{\Delta n}$ within 30 s (mA) for CBRs with $I_{\Delta n} \geq 0,030$ A		
	residual pulsating tripping current $\leq 1,4 I_{\Delta n}$ (mA) for CBRs with $I_{\Delta n} \geq 0,030$ A		
	steady increase from max. 0,2 $I_{\Delta n}$ to 2 $I_{\Delta n}$ within 30 s (mA) for RCBOs with $I_{\Delta n} < 0,030$ A		
	residual pulsating tripping current $\leq 2 I_{\Delta n}$ (mA) for CBRs with $I_{\Delta n} < 0,030$ A		
	CBR tested twice at position I of S_3		
	Min. setting $I_{\Delta n}$.(mA):		N/A
	CBR tested twice at position II of S_3		
	Min. setting $I_{\Delta n}$.(mA):		N/A
B.8.8.6	Verification of the correct operation in case of a residual direct currents that can result from rectifying circuits supplied from two phases		N/A
	The CBR shall be connected to the test circuit at two line terminals chosen at random		
a)	Test switch S_1 and S_2 and CBR in closed position		
	Residual pulsating direct current:		
	steady increase from max. 0,2 $I_{\Delta n}$ to 2 $I_{\Delta n}$ within 30 s		
	tripping current between 0,5 $I_{\Delta n}$ and 2 $I_{\Delta n}$		
	CBR tested five times at positions I of S_3 :		
	Min. setting $I_{\Delta n}$.(mA):		N/A
	CBR tested five times at positions II of S_3 :		
	Min. setting $I_{\Delta n}$.(mA):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
b)	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S_1 and the CBR being in the closed position, the residual current is suddenly establish by closing the switch S_2		
	Five measurements of the break time are made at each of these values of residual current at each position I and II of S_3		
	CBR tested at position I of S_3 :		
	A residual current is sudden appear on the CBR of $2 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $4 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $\square 10 I_{\Delta n}$ or $\square 0,5 A$ Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $\square 20 I_{\Delta n}$ or $\square 1,0 A$ Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	CBR tested at position II of S_3 :		
	A residual current is sudden appear on the CBR of $2 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of 4 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I _{Δn} or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 I _{Δn} or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
B.8.8.7	Verification of the correct operation in the case of a residual direct currents that can result from rectifying circuits supplied from three phases		N/A
	This test does not apply to 2-pole type B CBRs		
a)	Test switch S ₁ and S ₂ and CBR in closed position		
	Residual pulsating direct current:		
	steady increase from max. 0,2 I _{Δn} to 2 I _{Δn} within 30 s		
	tripping current between 0,5 I _{Δn} and 2 I _{Δn}		
	CBR tested five times at positions I of S ₃ :		
	Min. setting I _{Δn} .(mA):		N/A
	CBR tested five times at positions II of S ₃ :		
	Min. setting I _{Δn} .(mA):		N/A
b)	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S ₁ and the CBR being in the closed position, the residual current is suddenly establish by closing the switch S ₂		
	Five measurements of the break time are made at each of these values of residual current at each position I and II of S ₃		
	CBR tested at position I of S ₃ :		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of 2 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I _{Δn} or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 I _{Δn} or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	CBR tested at position II of S ₃ :		
	A residual current is sudden appear on the CBR of 2 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I _{Δn} or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 $I_{\Delta n}$ or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
B.8.8.8	Verification of the correct operation in the case of residual smooth direct current		N/A
B,8.8.8.1	Verification of the correct operation in the case of residual smooth direct current without load		N/A
	One pole of the CBR, chosen at random		
a)	Test switch S_1 and S_2 and CBR in closed position		
	Residual smooth direct current:		
	steady increase from max. 0,2 $I_{\Delta n}$ to 2 $I_{\Delta n}$ within 30 s		
	tripping current between 0,5 $I_{\Delta n}$ and 2 $I_{\Delta n}$		
	CBR tested twice at positions I of S_3 :		
	Min. setting $I_{\Delta n}$.(mA):		N/A
	CBR tested twice at positions II of S_3 :		
	Min. setting $I_{\Delta n}$.(mA):		N/A
b)	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S_1 and the CBR being in the closed position, the residual current is suddenly establish by closing the switch S_2		
	Two measurements of the break time are made at each of these values of residual current at each position I and II of S_3		
	CBR tested at position I of S_3 :		
	A residual current is sudden appear on the CBR of 2 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 $I_{\Delta n}$ or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	CBR tested at position II of S_3 :		
	A residual current is sudden appear on the CBR of 2 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 $I_{\Delta n}$ or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
B.8.8.8.2	Verification of the correct operation in the case of residual smooth direct current with load		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	The CBR being loaded with the rated current as in normal service for a sufficient time so as to reach thermal steady-state conditions		
	Test current:		
B.8.8.8.1 a)	Test switch S_1 and S_2 and CBR in closed position		
	Residual smooth direct current:		
	steady increase from max. $0,2 I_{\Delta n}$ to $2 I_{\Delta n}$ within 30 s		
	tripping current between $0,5 I_{\Delta n}$ and $2 I_{\Delta n}$		
	CBR tested twice at positions I of S_3 :		
	Min. setting $I_{\Delta n}$.(mA):		N/A
	CBR tested twice at positions II of S_3 :		
	Min. setting $I_{\Delta n}$.(mA):		N/A
B.8.8.9	Tests at the temperature limits		N/A
a)	Ambient air temperature:- 5 °C, without load		N/A
B.8.8.6 b)	Verification of the correct operation in case of a residual direct currents that can result from rectifying circuits supplied from two phases		N/A
	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S_1 and the CBR being in the closed position, the residual current is suddenly establish by closing the switch S_2		
	Five measurements of the break time are made at each of these values of residual current at each position I and II of S_3		
	CBR tested at position I of S_3 :		
	A residual current is sudden appear on the CBR of $2 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms)::		N/A
	A residual current is sudden appear on the CBR of $4 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 $I_{\Delta n}$ or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	CBR tested at position II of S_3 :		
	A residual current is sudden appear on the CBR of 2 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 $I_{\Delta n}$ or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
B.8.8.7 b)	Verification of the correct operation in the case of a residual direct currents that can result from rectifying circuits supplied from three phases		N/A
	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S_1 and the CBR being in the closed position, the residual current is suddenly establish by closing the switch S_2		
	Five measurements of the break time are made at each of these values of residual current at each position I and II of S_3		
	CBR tested at position I of S_3 :		
	A residual current is sudden appear on the CBR of $2 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $4 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $\square 10 I_{\Delta n}$ or $\square 0,5 A$ Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $\square 20 I_{\Delta n}$ or $\square 1,0 A$ Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	CBR tested at position II of S_3 :		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of 2 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I _{Δn} or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 I _{Δn} or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
B.8.8.8.1 b)	Verification of the correct operation in the case of residual smooth direct current		N/A
	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S ₁ and the CBR being in the closed position, the residual current is suddenly establish by closing the switch S ₂		
	Two measurements of the break time are made at each of these values of residual current at each position I and II of S ₃		
	CBR tested at position I of S ₃ :		
	A residual current is sudden appear on the CBR of 2 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 $I_{\Delta n}$ or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	CBR tested at position II of S_3 :		
	A residual current is sudden appear on the CBR of 2 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 $I_{\Delta n}$ or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
b)	Ambient air temperature: + 40 °C, withload		N/A
	Test current (A):		N/A
B.8.8.6 b)	Verification of the correct operation in case of a residual direct currents that can result from rectifying circuits supplied from two phases		N/A
	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S_1 and the CBR being in the closed position, the residual current is suddenly establish by closing the switch S_2		
	Five measurements of the break time are made at each of these values of residual current at each position I and II of S_3		
	CBR tested at position I of S_3 :		
	A residual current is sudden appear on the CBR of 2 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of □ 20 I _{Δn} or □ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	CBR tested at position II of S ₃ :		
	A residual current is sudden appear on the CBR of 2 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of □ 10 I _{Δn} or □ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of □ 20 I _{Δn} or □ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
B.8.8.7 b)	Verification of the correct operation in the case of a residual direct currents that can result from rectifying circuits supplied from three phases		N/A
	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S ₁ and the CBR being in the closed position, the residual current is suddenly establish by closing the switch S ₂		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Five measurements of the break time are made at each of these values of residual current at each position I and II of S ₃		
	CBR tested at position I of S ₃ :		
	A residual current is sudden appear on the CBR of 2 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I _{Δn} or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 I _{Δn} or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	CBR tested at position II of S ₃ :		
	A residual current is sudden appear on the CBR of 2 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of 4 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I _{Δn} or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 I _{Δn} or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
B.8.8.8.1 b)	Verification of the correct operation in the case of residual smooth direct current		N/A
	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S ₁ and the CBR being in the closed position, the residual current is suddenly establish by closing the switch S ₂		
	Two measurements of the break time are made at each of these values of residual current at each position I and II of S ₃		
	CBR tested at position I of S ₃ :		
	A residual current is sudden appear on the CBR of 2 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 $I_{\Delta n}$ or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	CBR tested at position II of S_3 :		
	A residual current is sudden appear on the CBR of 2 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 $I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 $I_{\Delta n}$ or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
B.8.8.10	Test of three- and four-pole CBRs supplied between two phase poles only		N/A
	The CBR being supplied between two phase poles chosen at random		
B.8.8.3	Verification of the correct operation in the case of residual sinusoidal alternating currents up to 1000 Hz		
B.8.8.3 a)	The test shall be carried out on one pole taken at random and repeated five times:		
	Test switch S_1 and S_2 and CBR in closed position		
	Test at 150Hz:		
	steady increase from max. $0,2 I_{\Delta n}$ to $2,4 I_{\Delta n}$ within 30s		
	- tripping current between $0,5 I_{\Delta n}$ and $2,4 I_{\Delta n}$ (mA)		
	Min. setting $I_{\Delta n}$.(mA):		N/A
	Test at 400Hz:		
	steady increase from max. $0,2 I_{\Delta n}$ to $6 I_{\Delta n}$ within 30s		
	- tripping current between $0,5 I_{\Delta n}$ and $6 I_{\Delta n}$ (mA)		
	Min. setting $I_{\Delta n}$.(mA):		N/A
	Test at 1000Hz:		
	steady increase from max. $0,2 I_{\Delta n}$ to $14 I_{\Delta n}$ within 30s		
	- tripping current between $I_{\Delta n}$ and $14 I_{\Delta n}$ (mA)		
	Min. setting $I_{\Delta n}$.(mA):		N/A
B.8.8.3 b)	Two measurements of the break time are made on one pole chosen at random:		
	S_1 and CBR in the closed position, residual current correspond to 1000Hz suddenly established by closing S_2 , the CBR is closes on $14 I_{\Delta n}$		
	Required : non-time-delayed type, no value exceeds 300 ms		
	Required : time-delayed type, no value exceeds 500 ms or the value declared by the manufacturer for $I_{\Delta n}$		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
B.8.8.1	Verification of the correct operation in the case of residual smooth direct current		N/A
B.8.8.8.1 a)	Test switch S_1 and S_2 and CBR in closed position		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Residual smooth direct current:		
	steady increase from max. $0,2 I_{\Delta n}$ to $2 I_{\Delta n}$ within 30 s		
	tripping current between $0,5 I_{\Delta n}$ and $2 I_{\Delta n}$		
	CBR tested twice at positions I of S_3 :		
	Min. setting $I_{\Delta n}$.(mA):		N/A
	CBR tested twice at positions II of S_3 :		
	Min. setting $I_{\Delta n}$.(mA):		N/A
B.8.8.8.1 b)	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S_1 and the CBR being in the closed position, the residual current is suddenly establish by closing the switch S_2		
	Two measurements of the break time are made at each of these values of residual current at each position I and II of S_3		
	CBR tested at position I of S_3 :		
	A residual current is sudden appear on the CBR of $2 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $4 I_{\Delta n}$ Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $\square 10 I_{\Delta n}$ or $\square 0,5 A$ Required : no value exceeds the specified limiting value of Table B.1 (40ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $\square 20 I_{\Delta n}$ or $\square 1,0 A$ Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	CBR tested at position II of S ₃ :		
	A residual current is sudden appear on the CBR of 2 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I _{Δn} Required : no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I _{Δn} or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 I _{Δn} or ☐ 1,0 A Required : no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-actuating time of 60 ms		
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
B.8.9	Verification of the behaviour of CBRs functionally dependent on line voltage classified under B.3.1.2.1		N/A
B.8.9.1	General		
	For CBRs having an adjustable residual operating current, the test is made at the lowest setting.		
	For CBRs with an adjustable time-delay, the test is made at any one of the time-delay settings.		
B.8.9.2	Determination of the limiting value of the line voltage		N/A
	A voltage equal to the rated voltage is applied to the line terminals of the CBR and is then progressively lowered to zero over a time period corresponding to the longer of the two values given hereinafter until automatic opening occurs: – about 30 s; – a period long enough with respect to the delayed opening of the CBR, if any (see B.7.2.11).		
	Three measurements are made. All the values shall be less than 0,85 times the minimum rated voltage of the CBR.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of $I_{\Delta n}$ (____ mA) at a value just above highest measured value Required : no value exceeds the specified limiting value of Table B1: 300 ms		N/A
	For any value of voltage less than the lowest value measured, it is not be possible to close the CBR by manual operating means.		N/A
B.8.9.3	Verification of the automatic opening in the case of failure of the line voltage		N/A
	The CBR being closed, a voltage equal to its rated voltage, or, in the case of a range of rated voltages, any one of the rated voltages is applied to its line terminals. The voltage is then switched off. The CBR shall trip. The time interval between the switching off and the opening of the main contacts is measured.		N/A
	for CBRs opening without delay no value shall exceed 0,2 s;		N/A
	for CBRs opening with delay the maximum and minimum values shall be situated within the range indicated by the manufacturer.		N/A
B.8.10	Verification of the behaviour of CBRs functionally dependent on line voltage classified under B.3.1.2.2		N/A
B.8.10.1	General		
	For CBRs having an adjustable residual operating current, the test is made at the lowest setting.		N/A
	For CBRs having an adjustable time-delay the test is made at any one of the time-delay settings.		N/A
B.8.10.2	Case of loss of one phase in a 3-phase system (for 3-pole and 4-pole CBRs)		N/A
	The CBR is connected according to figure B.3 and is supplied on the line side at 0,85 times the rated voltage, or, in the case of a range of rated voltages, at 0,85 times the lowest value of rated voltage.		N/A
	Verification with one phase is switched off		N/A
B.8.2.4.4	Verification of operating in case of a sudden appearance of the residual current		N/A
	A residual current is sudden appear on the CBR of $I_{\Delta n}$ Required: no value exceeds the specified limiting value of Table B1: (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of 2 I Δ n Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 5 I Δ n or ☐ 0,25 A Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I Δ n or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
			N/A
	Verification with other phase switched off		N/A
B.8.2.4.4	Verification of operating in case of a sudden appearance of the residual current (figure B.1)		N/A
	A residual current is sudden appear on the CBR of I Δ n Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the CBR of 2 I Δ n Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of ☐ 5 I Δ n or ☐ 0,25 A Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I Δ n or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	Test is repeated with resistor connected to other two phases in turn.		N/A
B.8.10.3	Case of voltage drop due to an overcurrent resulting from a low impedance fault to earth		N/A
	The CBR is connected according to figure B.3 and is supplied on the line side with the rated voltage or, in the case of a range of rated voltages, with the lowest rated voltage.		
	The supply is switched off. The CBR shall not trip.		N/A
	With supply connected the voltage is reduced as follows: a) for CBRs for use with a three-phase supply: to 70 % of the lowest rated voltage;		
	b) for CBRs for use with a single phase supply: to 85 V applied as follows: – for single-pole and two-pole CBRs: between poles; – for three-pole and four-pole CBRs, declared as suitable for use with a single-phase supply (see B.5 e)): between each combination of two poles, connected according to the manufacturer's specification.		
	A current of value I Δ n is then applied to a) and/or b), as applicable. The CBR shall trip.		N/A
BII	Test sequence BII		
B.8.11	Verification of the residual short-circuit making and breaking capacity		
B.8.11.2	Where applicable, the CBR is adjusted at the lowest setting of residual operating current and at the maximum setting of time-delay.		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	If the CBR has more than one value of I_{cu} , each one having a corresponding value of $I_{\Delta m}$, the test is made at the maximum value of $I_{\Delta m}$, at the corresponding phase-to-neutral voltage.		
	maximum value of $I_{\Delta m}$		
	Type designation or catalogue reference		
	Sample no:		
	Point of test circuit which is directly earthed:		
	Grid distance "a" (mm):		
	Fine wire diameter (mm):		
	Prospective current (A):		
	Prospective current obtained (A):		
	Power factor / ratio n :		
	Power factor / ratio n obtained:		
B.8.11.3	Test sequence: O-t-CO		
	I^2t (kA ² s); I_p (kA):	O operation: I_p : _____ kA I^2t ; _____ kA ² s Plot no. _____	
		CO operation: I_p : _____ kA I^2t ; _____ kA ² s Plot no.: _____	
	If tested at separate testing station see report		
	During tests no endangering of operator, no permanent arcing, no flashover and no melting of fuse F		N/A
B.8.11.4	Conditions of the CBR after test		N/A
a)	After the tests no damage impairing further use		N/A
	Withstanding a voltage equal to twice its maximum rated operational voltage, under the conditions of 8.3.3.4.1 item 4) of IEC 60947-1:2017:		
	Test voltage		N/A
	Making and breaking its rated current at its maximum rated operational voltage.		N/A
b)	The CBR shall be capable of performing satisfactorily the tests specified in B.8.2.4.4, but at a value of 1,25 $I_{\Delta n}$ and without measurement of break time. The test is made on any one pole, taken at random.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	If the CBR has an adjustable residual operating current, the test is made at the lowest setting, at a current of a value of 1,25 times that setting.		N/A
c)	Where applicable the CBR shall also be submitted to the test of B.8.2.4.5.		N/A
B.8.2.4.5	Verification of the limiting non-operating time of time delayed type CBRs		N/A
	A residual current is sudden appear on the CBR of 2 I Δ n for a time declared by the manufacturer Required : The CBR shall not operate		N/A
	Min. setting I Δ n. Max. setting time delay (ms):		N/A
d)	CBRs functionally dependent on line voltage shall also satisfy the tests of B.8.8 or B.8.9, as applicable.		N/A
B III	Test sequence B III		
B.8.12	Verification of the effects of environmental conditions		N/A
	The test is carried out according to IEC 60068-2-30.		
	The upper temperature shall be 55 °C $\pm$ 2 °C (variant 1) and the number of cycles shall be – 6 for I Δ n > 1 A – 28 for I Δ n $\leq$ 1 A		
	At the end of the cycles the CBR shall be capable of complying with the tests of B.8.2.4.4, but with a residual operating current of 1,25 I Δ n and without measurement of break time. Only one verification need be made.		N/A
	Where applicable the CBR shall also comply with the test of B.8.2.4.5. Only one verification need be made.		N/A
B.8.2.4.5	Verification of the limiting non-operating time of time delayed type CBRs		N/A
	A residual current is sudden appear on the CBR of 2 I Δ n for a time declared by the manufacturer Required : The CBR shall not operate		N/A
	Min. setting I Δ n. Max. setting time delay (ms):		N/A
B.8.13	Verification of electromagnetic compatibility (EMC)		
	See report:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Annex C	Individual pole short-circuit test sequence		
	Circuit-breaker for use on phase-earthed systems		
C.2	Test of individual pole short-circuit breaking capacity		
	A short-circuit test is made with a value of prospective current (I_{su}) equal to 25% of the ultimate rated short-circuit breaking capacity (I_{cu})		
	Type designation or catalogue reference		
	Sample no:		
	Rated current: I_n (A)		
	Rated operational voltage: U_e (V)		
	Rated ultimate short-circuit breaking capacity: (kA)		
	Rated control supply voltage of closing releases: U_c (V)		
	Rated control supply voltage of shunt release: U_c (V)		
	Rated control supply voltage of undervoltage releases: U_c (V)		
	The test sequence of operations is O – t - CO		
	For circuit-breaker fitted with adjustable releases, test shall be made with the current and time settings at maximum.		N/A
	Closing releases energized with 85% at the rated U_c : (V)		N/A
	The circuit-breaker is mounted complete on its own support or an equivalent support.		N/A
	Test made in free air:		N/A
	Distances of the metallic screen's: (all sides)		N/A
	The characteristics of the metallic screen:		
	- woven wire mesh		N/A
	- perforated metal		N/A
	- expanded metal		N/A
	- ratio hole area/total area: 0,45-0,65		N/A
	- size of hole: $<30\text{mm}^2$		N/A
	- finish: bare or conductive plating		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Test made in specified individual enclosure: Details of these tests, including the dimensions of the enclosure:		N/A
	Fuse "F": copper wire: diameter 0,8 mm, 50 mm long		N/A
	Circuit is earthed at: (load-star- or supply-star point)		N/A
	Conductor cross-sectional area (mm ²):		N/A
	If terminals unmarked: line connected at: (underside/upside)		N/A
	Tightening torques: (Nm)		N/A
	Test sequence of operation: O – t – CO		N/A
	Test circuit according figure: 9		N/A
	- test voltage U/U _e = 1,05 (V)L1:L2:L3:		N/A
	short-circuit test current (I _{su}): equal to 25% of the ultimate rated short-circuit breaking capacity (I _{cu})		N/A
	- r.m.s. test current AC/DC: (A):		N/A
	power factor/time constant:		N/A
	- Factor "n"		N/A
	- peak test current (A _{max}):		N/A
	Test sequence "O" L1		
	- max. let-through current: (kA _{peak})L1:		N/A
	- Joule integral I ² dt (A ² s)L1:		N/A
	Pause, t: (min)		N/A
	Test sequence "CO" L1		
	- max. let-through current: (kA _{peak})L1:		N/A
	- Joule integral I ² dt (A ² s)L1:		N/A
	Test sequence "O" L2		
	- max. let-through current: (kA _{peak})L2:		N/A
	- Joule integral I ² dt (A ² s)L2:		N/A
	Pause, t: (min)		N/A
	Test sequence "CO" L2		
	- max. let-through current: (kA _{peak})L2:		N/A
	- Joule integral I ² dt (A ² s)L2:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Test sequence "O" L3		
	- max. let-through current: (kA _{peak})L3:		N/A
	- Joule integral I ² dt (A ² s)L3:		N/A
	Pause, t: (min)		N/A
	Test sequence "CO" L3		
	- max. let-through current: (kA _{peak})L3:		N/A
	- Joule integral I ² dt (A ² s)L3:		N/A
	Melting of the fusible element		N/A
	Damage to insulation on conductors		N/A
	Holes in the PE-sheet for test sequence "O"		N/A
	Cracks observed		N/A
C.3	Verification of dielectric withstand		
	- equal to twice the rated operational voltage with a minimum of 1000 V		N/A
	- no breakdown or flashover		N/A
C.4	Verification of overload releases		
	The operation of overload releases shall be verified at 2.5 times the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:		
	- Operation time: (s)L1:L2:L3: N:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Annex D	Additional requirements for circuit-breakers intended for connection of aluminium conductors		
D.5.2	Marking		
	The suitability of the terminal for use with aluminium conductors only (Al) or copper and aluminium conductors (Cu/Al) shall be marked on the circuit-breaker or the circuit-breaker terminals, or in the manufacturer's literature. When the marking is on the circuit-breaker or terminals, the abbreviations 'AL' and 'CU/AL' may also be used.		N/A
	If the rated current of the terminals is lower than the rated current of the circuit-breaker, it shall be marked on the circuit-breaker or the circuit-breaker terminals, or in the manufacturer literature.		N/A
D.8	Test		
D.8.2	Current cycling test		N/A
D.8.2.1	This test shall be carried out on terminals alone, not on the circuit-breaker		N/A
D.8.2.2	Preparation		N/A
	The test shall be performed on four specimens, each one made by a couple of terminals, assembled in a manner which represents the use of the terminals in the circuit-breaker.		
	The terminals that have been removed from the product shall be attached to conducting parts of the same cross-section, shape, metal and finish as those on which they are mounted on the product.		
	The conducting part shall not exceed:		
	(i) three times the length of the terminal,		N/A
	(ii) the width of the terminal		N/A
	To avoid excessive oxidation of the conductor and ensure proper connection, a sufficient length of insulation shall be removed immediately prior to installation		
	The wire shall be positioned so that 6 mm to 13 mm of the bare conductor is exposed between the wire-entry face of the terminal and the beginning of the insulation. There shall be no mechanical removal or chemical treatment of any oxide on the surface of the conductor entering the terminal, unless it is explicitly required by the manufacturer's instructions, in which case it shall be stated in the test report.		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
D.8.2.3	Test arrangement		N/A
	The length of the test conductor from the point of entry to the terminal specimens to the equalizer shall be as in Table D.2		N/A
	Test conductors shall be connected in series with a reference conductor of the same cross-section. The length of the reference conductor shall be approximately twice the length of the test conductor.		N/A
	90 % of the value of torque stated by the manufacturer or, if not stated, selected in Table 4 of part 1, shall be used to connect the aluminium cables to the terminal specimens.		N/A
	The distance between the test and reference conductors shall be at least 150 mm.		N/A
	Thermal barriers shall be installed mid- way between the conductors and shall extend 25 mm $\pm$ 5 mm widthways and 150 mm $\pm$ 10 mm lengthways beyond the terminals (see Figure D.1).		N/A
	Thermal barriers are not required provided the specimens are separated by at least 450 mm.		N/A
	The specimens shall be located at least 600 mm from the floor, wall or ceiling.		N/A
	The test specimens shall be located in a substantially vibration-free and draught-free environment and at an ambient air temperature between 15 °C and 35 °C. Once the test is started, the maximum permissible variation is $\pm$ 4 K provided the range limitation is not exceeded.		N/A
D.8.2.4	Temperature measurements		N/A
	Temperature measurements are made by means of thermocouples. Positioning of the thermocouples shall not damage the terminal or the reference conductor.		
	For the measurement of the terminal temperature, the thermocouple shall be located on the conductor entry side of the terminal, close to the contact interface.		
	For the measurement of the temperature of the reference conductor, the thermocouple shall be located midway between the ends of the conductor, and under its insulation.		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	The ambient air temperature shall be measured with two thermocouples in such a manner as to achieve an average and stable reading in the vicinity of the test loop without undue external influence.		
	The thermocouples shall be located in a horizontal plane intersecting the specimens, at a minimum distance of 600 mm from them.		N/A
D.8.2.5	Test method		N/A
	The test loop shall be subjected to 500 cycles; each cycle consists of an on-period and an off period as specified,		N/A
	Starting at the test current value given in Table D.4.		N/A
	on-time:		N/A
	off-time:		N/A
	Near the end of each on-period of the first 24 cycles, the temperature of each terminal shall have attained a minimum of 105 °C. When necessary, the current shall be adjusted to achieve this condition.		N/A
	If the manufacturer demonstrates that the maximum temperature-rise of the terminals is less than 70 K, the 105 °C temperature may be reduced to this temperature-rise plus 35 °C.		N/A
	At the 25 th cycle, the test current shall be adjusted for the last time and the corresponding temperature, when stabilized at the final load current, shall be recorded as the first measurement. There shall be no further adjustment of the test current for the remainder of the test		
	The test temperature at the 25 th cycle:		N/A
	The test temperature at the 50 th cycle:		N/A
	The test temperature at the 75 th cycle:		N/A
	The test temperature at the 100 th cycle:		N/A
	The test temperature at the 125 th cycle:		N/A
	The test temperature at the 150 th cycle:		N/A
	The test temperature at the 175 th cycle:		N/A
	The test temperature at the 200 th cycle:		N/A
	The test temperature at the 225 th cycle:		N/A
	The test temperature at the 250 th cycle:		N/A
	The test temperature at the 275 th cycle:		N/A
	The test temperature at the 300 th cycle:		N/A
	The test temperature at the 325 th cycle:		N/A
	The test temperature at the 350 th cycle:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	The test temperature at the 425 th cycle:		N/A
	The test temperature at the 500 th cycle:		N/A
D.8.2.6	Acceptance criteria		N/A
	For each terminal:		
	- the temperature-rise shall not exceed 125 K, and		N/A
	- the stability factor Sf shall not exceed ± 10 K.		N/A
D.8.3	Mechanical properties of terminals		N/A
D.8.3.1	Flexion test		
	conductor of the smallest cross-sectional area (mm ²) :		
	number of conductors of the smallest cross section :		
	diameter of bushing hole (mm) :		
	height between the equipment and the platen :		
	mass at the conductor(s) (kg) :		
	135 continuous revolutions: the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A
D.8.3.2	Pull-out test		
	force (N) :		
	1 min, the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A
D.8.3.1	Flexion test		N/A
	conductor of the largest cross-sectional area (mm ²) :		
	number of conductors of the largest cross section :		
	diameter of bushing hole (mm) :		
	height between the equipment and the platen :		
	mass at the conductor(s) (kg) :		
	135 continuous revolutions: the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A
D.8.3.2	Pull-out test		
	force (N) :		
	1 min, the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A
D.8.3.1	Flexion test		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	conductor of the largest and smallest cross-sectional area (mm ²) :		
	number of conductors of the smallest cross section, number of conductors of the largest cross section :		
	diameter of bushing hole (mm) :		
	height between the equipment and the platen :		
	mass at the conductor(s) (kg) :		
	135 continuous revolutions: the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A
D.8.3.2	Pull-out test		
	force (N) :		
	1 min, the conductor shall neither slip out of the terminal nor break near the clamping unit		N/A
D.8.4	Test for insertability of unprepared round aluminium conductors having the maximum cross-section		N/A
	The test shall be carried out using the appropriate gauge form A or form B specified in Table 7.		N/A
	The measuring section of the gauge shall be able to penetrate freely into the terminal aperture to the full depth of the terminal		N/A
	Alternatively, the test can be carried out by inserting the largest conductor of type and rated cross-section among those recommended by the manufacturer, the diameter of which corresponds to the theoretical diameter according to Table 7a, after the insulation has been removed and the end has been reshaped. The stripped end of the conductor shall be able to enter completely within the clamping unit aperture, without use of undue force.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
Annex F	Additional tests for circuit-breakers with electronic over-current protection DS 1, 3P, 415V, 400A, 1 sample		
F4 and F5	Verification of electromagnetic compatibility (EMC)		
	See report	See below	P
F6	Suitability for multiple frequencies		
	The tests shall be performed at each rated frequency or, when a range of rated frequencies is declared, at the lowest and the highest rated frequencies.		P
F.6.2	Tests shall be performed on any pair of phase-poles chosen at random at any convenient voltage. Under-voltage releases, if any, shall either be energized or disabled. All other auxiliaries shall be disconnected during the test.		
	The short-time and instantaneous trip current settings shall each, if relevant, be adjusted to 2,5 times the current setting. If this setting is not available, the next closest higher setting shall be used.		
	A current of 0,95 times the conventional non-tripping current (see Table 6) is applied for a time equal to 10 times the tripping time which corresponds to 2,0 times the current setting.		
	Immediately following the test of a), a current of 1,05 times the conventional tripping current (see Table 6) is applied.		
	A further test starting from the cold state is made at 2,0 times the current setting.		
	For each test frequency, the overload tripping characteristics shall comply with the following requirements: – for test a) no tripping shall occur; – for test b) tripping shall occur within the conventional time (see Table 6); – for test c) tripping shall occur within 1,1 times the maximum and 0,9 times the minimum values of the manufacturer's stated time-current characteristic.		N/A
F.7.	Dry heat test		P
F.7.1	The test shall be performed on the circuit-breaker in accordance with 7.2.2 at the maximum rated current for a given frame size, on all phase poles, at an ambient temperature of 40 °C	In= __400_ A	
	The duration of the test, once temperature equilibrium is reached, shall be 168 h		
	Tightening torques applied to the terminals shall be in accordance with the manufacturers' instructions. In absence of such instructions, table 4 of IEC 60947-1 shall apply	Torque= _10_ Nm	

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	As an alternative, the test may be performed as follows:		
	- measure and record the highest temperature rise of the air surrounding the electronic components, during the temperature rise verification of test sequence 1	Ambient temperature during temperature rise test : __-5-40__ °C	
	- install the electronic controls in the chamber		
	- supply the electronic controls which there input energizing value		
	- adjust the temperature of the test chamber to a value of 40 K above the temperature rise recorded for the surrounding the electronic components and maintain this temperature for 168 h	Chamber temperature: ____ °C	
	Test carried out.....:	☒ normal ☐ alternative	
F.7.2	Test results		P
	The circuit-breaker and the electronic controls shall meet the following requirements:		
	- no tripping of the circuit-breaker shall occur		P
	- no operating of the electronic controls which would cause the circuit-breaker to trip shall occur		P
F.7.3	Verification of the overload releases		P
	Following the test F.7.1, the operation of the overload releases of the circuit-breaker shall be verified in accordance with 7.2.1.2.4, item b).	I test: 520A Ambient temperature: _27,8_ °C	P
7.2.1.2.4	Opening by over-current releases		P
b)	Opening under overload conditions		
1)	Instantaneous or definite time-delay operation		N/A
	The release shall cause tripping of the circuit-breaker with an accuracy of + 10% of the tripping current value of the current setting for all values of current setting of the overload release		N/A
2)	Inverse timer-delay operation		
	At the reference temperature and at 1,05 times the current setting with the conventional non-tripping current, the opening release being energized on all poles, tripping shall not occur in less than the conventional time from the cold state, i.e. with the circuit-breaker at the reference temperature	1,05×400 =420A no tripping ≥2h	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Moreover, when at the end of the conventional time the value of current is immediately raised to 1,30 times the current setting, i.e. with the conventional tripping current, tripping shall then occur in less than the conventional time later	1,30× 400 =520A operating time: 28,5s	P
	If a release is declared by the manufacturer as substantially independent of ambient temperature, the current values of table 6 shall apply within the temperature band declared by the manufacturer, within a tolerance of 0,3%/K		N/A
	The width of the temperature band shall be at least 10 K on either side of the reference temperature		N/A
F.8.	Damp heat test		P
F.8.1	Test procedure		P
	The test shall be performed according to IEC 60068-2-30 (12 +12 hours cycle)		
	Test Db temperature cycle between 25°C and upper temperature		
	The upper temperature shall be 55°C ± 2 °C (variant 1) and number of cycles shall be six.		
	The relative humidity is maintained at a high level at the upper temperature		
	The test may be performed with only the electronic controls in the test chamber		
	Test result.....:		P
F.8.2	Verification of the overload releases		P
	Following the test F.8.1, the operation of the overload releases of the circuit-breaker shall be verified in accordance with 7.2.1.2.4, item b).	I test: 520A Ambient temperature: _26,8_°C	P
7.2.1.2.4	Opening by over-current releases		P
b)	Opening under overload conditions		
1)	Instantaneous or definite time-delay operation		N/A
	The release shall cause tripping of the circuit-breaker with an accuracy of + 10% of the tripping current value of the current setting for all values of current setting of the overload release		N/A
2)	Inverse timer-delay operation		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	At the reference temperature and at 1,05 times the current setting with the conventional non-tripping current, the opening release being energized on all poles, tripping shall not occur in less than the conventional time from the cold state, i.e. with the circuit-breaker at the reference temperature	1,05×400 =420A no tripping ≥2h	P
	Moreover, when at the end of the conventional time the value of current is immediately raised to 1,30 times the current setting, i.e. with the conventional tripping current, tripping shall then occur in less than the conventional time later	1,30× 400 =520A operating time: 28,3s	P
	If a release is declared by the manufacturer as substantially independent of ambient temperature, the current values of table 6 shall apply within the temperature band declared by the manufacturer, within a tolerance of 0,3%/K		P
	The width of the temperature band shall be at least 10 K on either side of the reference temperature		P
F.9.	Temperature variation cycles at a specified rate of change		P
F.9.1	Test conditions		P
	Each design of electronic controls shall be submitted to temperature variation cycles in according with figure F.15		
	The rise and fall of temperature during the rate of variation shall be 1 K/min ± 0,2 K/min.		
	Their temperature, once reached, shall be maintained for at least 2 h.		
	The number of cycles shall be 28.		
F.9.2	Test procedure		P
	The test shall be carried out according IEC 60068-2-14.		
	For the these test, the electronic controls may be mounted inside the circuit-breaker or separately.		
	The electronic controls shall be energized to simulate service conditions.		
	Where the electronics controls are mounted inside the circuit-breaker, the main circuit shall not be energized.		
F.9.3	Test results		P
	The electronic controls shall meet the following requirement.		P
	No operation of the electronic controls which would cause the circuit-breaker to trip during the 28 cycles		P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	shall occur.		
F.9.4	Verification of overload releases		P
	Following the test F.8.1, the operation of the overload releases of the circuit-breaker shall be verified in accordance with 7.2.1.2.4, item b).	I test: 520A Ambient temperature: _26,3_ °C	P
7.2.1.2.4	Opening by over-current releases		P
b)	Opening under overload conditions		P
1)	Instantaneous or definite time-delay operation		N/A
	The release shall cause tripping of the circuit-breaker with an accuracy of + 10% of the tripping current value of the current setting for all values of current setting of the overload release		N/A
2)	Inverse timer-delay operation		P
	At the reference temperature and at 1,05 times the current setting with the conventional non-tripping current, the opening release being energized on all poles, tripping shall not occur in less than the conventional time from the cold state, i.e. with the circuit-breaker at the reference temperature	1,05×400 =420A no tripping ≥2h	P
	Moreover, when at the end of the conventional time the value of current is immediately raised to 1,30 times the current setting, i.e. with the conventional tripping current, tripping shall then occur in less than the conventional time later	1,30× 400 =520A operating time: 28,9s	P
	If a release is declared by the manufacturer as substantially independent of ambient temperature, the current values of table 6 shall apply within the temperature band declared by the manufacturer, within a tolerance of 0,3%/K		N/A
	The width of the temperature band shall be at least 10 K on either side of the reference temperature		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
Annex H	Individual pole short-circuit test sequence DS 1, 3P, 415V, 400A, 1 sample		
	Circuit-breaker for use in IT systems		
H.2	Test of individual pole short-circuit breaking capacity		
	A short-circuit test is made on the individual poles of a multipole circuit-breaker at a value of prospective current (I_{pr}) equal to 1,2 times the maximum setting of the short-time delay release tripping current or, in the absence of such a release, 1,2 time the max. setting of the tripping current of the instantaneous release, or, where relevant 1,2 times the maximum setting of the definite time delay release tripping current, but not less than 500 A nor exceeding 50kA.		
	Type designation or catalogue reference	DS 1	
	Sample no:	H-1	
	Rated current: I_n (A)	400	
	Rated operational voltage: U_e (V)	415	
	Rated ultimate short-circuit breaking capacity: (kA)	85	
	Rated control supply voltage of closing releases: U_c (V)		
	Rated control supply voltage of shunt release: U_c (V)		
	Rated control supply voltage of undervoltage releases: U_c (V)		
	The test sequence of operations is O – t - CO		
	For circuit-breaker fitted with adjustable releases, test shall be made with the current and time settings at maximum.	$I_r - I_n$ $I_i = 14 I_r$	P
	closing releases energized with 85% at the rated U_c : (V)		N/A
	The circuit-breaker is mounted complete on its own support or an equivalent support.		P
	Test made in free air:		P
	Distances of the metallic screen's: (all sides)	Front: 0mm back: 0mm Left: 0mm; right: 0mm Top: 100mm; bottom: 100mm	P
	The characteristics of the metallic screen:		
	- woven wire mesh		N/A
	- perforated metal		P
	- expanded metal		N/A
	- ratio hole area/total area: 0,45-0,65	0,5	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- size of hole: <30mm ²	25	P
	- finish: bare or conductive plating	Bare	P
	Test made in specified individual enclosure: Details of these tests, including the dimensions of the enclosure:		N/A
	Fuse "F": copper wire: diameter 0,8 mm, 50 mm long		P
	Circuit is earthed at: (load-star- or supply-star point)	supply-star	P
	Conductor cross-sectional area (mm ²):	240	P
	If terminals unmarked: line connected at: (underside/upside)		N/A
	Tightening torques: (Nm)	10	P
	Test sequence of operation: O – t – CO		P
	Test circuit according figure: 9		P
	- test voltage $U/U_e = 1,05$ (V) L1: L2: L3:	438 438 438	P
	Short-circuit test current (I_{IT}): equal to 1,2 times the max. setting of the short-time delay release tripping current,		N/A
	or, in the absence of such a release, 1,2 time the max. setting of the tripping current of the instantaneous release,	5,76kA	P
	or, where relevant 1,2 times the max. setting of the definite time delay release tripping current, but not exceeding 50kA.		N/A
	- r.m.s. test current AC/DC: (A)	5,76	P
	power factor/time constant:	0,67	P
	- Factor "n"	1,5	P
	- peak test current (A_{max}) :	8,64	P
	Test sequence "O" L1		
	- max. let-through current: (kA_{peak}) L1:	7,51	P
	- Joule integral I^2dt (A ² s) L1:	2,59	P
	Pause, t: (min)	3	P
	Test sequence "CO" L1		
	- max. let-through current: (kA_{peak}) L1:	6,97	P
	- Joule integral I^2dt (A ² s) L1:	1,81	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Test sequence "O" L2		
	- max. let-through current: (kA _{peak}) L2:	7,57	P
	- Joule integral I ² dt (A ² s) L2:	1,63	P
	Pause, t: (min)	3	P
	Test sequence "CO" L2		
	- max. let-through current: (kA _{peak}) L2:	7,54	P
	- Joule integral I ² dt (A ² s) L2:	1,60	P
	Test sequence "O" L3		
	- max. let-through current: (kA _{peak}) L3:	7,04	P
	- Joule integral I ² dt (A ² s) L3:	629	P
	Pause, t: (min)	3	P
	Test sequence "CO" L3		
	- max. let-through current: (kA _{peak}) L3:	7,54	P
	- Joule integral I ² dt (A ² s) L3:	603	P
	For 4-pole circuit-breakers with a protected neutral pole, the test voltage for that pole shall be phase-to-phase voltage divided by $\sqrt{3}$. This test is applicable only where the construction of the protected neutral pole differs from that of the phase poles.		N/A
	Test sequence "O" N		
	- max. let-through current: (kA _{peak}) N:		N/A
	- Joule integral I ² dt (A ² s) N:		N/A
	Pause, t: (min)		N/A
	Test sequence "CO" N		
	- max. let-through current: (kA _{peak}) N:		N/A
	- Joule integral I ² dt (A ² s) N:		N/A
	Melting of the fusible element		N/A
	Damage to insulation on conductors		N/A
	Holes in the PE-sheet for test sequence "O"		N/A
	Cracks observed		N/A
H.3	Verification of dielectric withstand		
	- equal to twice the rated operational voltage with a minimum of 1000 V	1000V	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- no breakdown or flashover		P
H.4	Verification of overload releases		
	The operation of overload releases shall be verified at 2.5 times the value of their current setting on each pole separately.		
	The operating time shall not exceed the max. value stated by the manufacturer for twice the current setting at the reference temperature, on a pole singly.		
	Time specified by the manufacturer:	2,5I _n	P
	- Operation time: (s) L1: L2: L3: N :	2min03s 1min56s 2min09s -	P
H.5	Marking		
	Rated voltage(s) not suitable for IT system, as follows: immediately following each corresponding rated voltage, e.g. 690 V  or immediately following the rated voltages, e.g. 415 V } 500 V }  690 V }		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Annex J	Electromagnetic compatibility (EMC) – Requirements and test methods for circuit-breakers		
	See report:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Annex L	Circuit-breakers not fulfilling the requirements for overcurrent protection		
L.3	Classification		
	- class X: with integral non-adjustable instantaneous short-circuit releases for self-protection; - class Y: without integral short-circuit releases.		
L.4	Rated values		
	Rated current: I_n (A)		
	Rated conditional short-circuit current (I_{cc})		
L.5	Product information		
	Visible from the front when the circuit-breaker is installed as in service and the actuator is accessible		
L.1.1	Suitability for isolation, if applicable, with the symbol 		N/A
L.1.2	Classification, with the marking  $I_i = \dots$ or  as applicable, where I_i is the rated instantaneous short-circuit current setting		N/A
	Provided in the manufacturer's literature		
L.3.1	Rated conditional short-circuit current (I_{cc}) and, if specified, overcurrent protective device (OCPD)		N/A
L.3.2	Statement that CBIs do not provide overcurrent protection		N/A
L.6	Constructional and performance requirements		
	A CBI, being derived from the equivalent circuit-breaker (see L.2.1), complies with all the applicable construction and performance requirements of Clause 7, except 7.2.1.2.4. NOTE A CBI may additionally comply with IEC 60947-3 and be marked accordingly.		
L.7	Tests		
L.7.2.2	OCPD specified		
L.7.2.2.2	Verification of I_{cc}		
	The test shall be made with a prospective current equal to I_{cc} of the CBI.		
	Each test shall consist of a O – t – CO sequence of operations made in accordance with 8.3.5.2, the CO operation being made by closing the CBI.		
	After each operation, the CBI shall be manually closed and opened three times.		
L.7.2.2.3	Verification of dielectric withstand		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Following the test of L.7.2.2.2, the dielectric withstand shall be verified in accordance with 8.3.5.3		
L.7.2.3	OCPD not specified		
L.7.2.3.2	Verification of I_{cc}		
	The test shall be made with a prospective current equal to I_{cc} of the CBI.		
	Each test shall consist of a O – t – CO sequence of operations made in accordance with 8.3.5.2, the CO operation being made by closing the CBI.		
	During the test, the current shall be maintained for three cycles and then disconnected at the power supply.		
	After each operation, the CBI shall be manually closed and opened three times.		
L.7.2.3.3	Verification of dielectric withstand		
	Following the test of L.7.2.3.2, the dielectric withstand shall be verified in accordance with 8.3.5.3		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Annex M	Modular residual current devices (without integral current breaking device)		
M.4.1	General characteristics		
	Rated frequency range (Hz)		
	Rated voltage: U_e (V)		
	Rated current: I_n (A)		
	Terminal type or through conductor type		
	MRCD with sensing means and processing device combined or separate	Combined/separate	
	MRCD with voltage source		
	Operating automatically in case of failure of the voltage source.	Yes/no	
	Rated insulation voltage (U_i)		
	Rated impulse withstand voltage (U_{imp})		
M.4.1.2	Characteristics of the voltage source of MRCDs		
	Rated values of the voltage source of MRCDs (U_s)		
	Rated values of the frequencies of the voltage source of MRCDs		
	Rated insulation voltage (U_i)		
	Rated impulse withstand voltage (U_{imp})		
M.4.1.3	Characteristics of auxiliary contacts		
M.4.2	Characteristics of MRCDs concerning their residual current function		
M.4.2.2	Operating characteristic in case of residual current with d.c. component		
	Type AC MRCD		
	Type A MRCD		
	Type B MRCD		
M.4.3	Behaviour under short-circuit conditions		
	Rated conditional short-circuit current (I_{cc})		
	Rated conditional residual short-circuit current ($I_{\Delta c}$)		
	Rated short-time withstand current (I_{cw})		
	Peak withstand current		
	Rated residual short-time withstand current ($I_{\Delta w}$)		
M.4.4	Preferred and limiting values		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Preferred values of the rated residual operating current ($I_{\Delta n}$)		
	Minimum value of the rated residual non-operating current ($I_{\Delta no}$)		
	Limiting value of the non-operating overcurrent in the case of a single-phase load in a multiphase circuit		
	Preferred values of rated voltage of the voltage source of MRCDs		
	Compliance with constructional requirements		N/A
M.5.	Marking		
	Visible from the front when the circuit-breaker is installed as in service and the actuator is accessible (single device and processing device only):		
M1.1	operating characteristic in the case of residual currents in the presence or absence of a d.c. component: - for MRCDs of type AC with the symbol  - for MRCDs of type A with the symbol  - for MRCDs of type B with the symbol  or   		N/A
M1.2	rated residual operating current (value(s) or range, as applicable) ($I_{\Delta n}$)		N/A
M1.3	limiting non-operating time (value or range) at $2 I_{\Delta n}$ for time-delay type, as applicable (Δt)		N/A
M1.4	maximum rated current of the monitored circuit (I_n), (Marked on the product for sensing means)		N/A
M1.5	test device with the letter "T"		N/A
	Marked on the product:		
M2.1	manufacturer's name or trade mark		N/A
M2.2	type designation or catalogue reference		N/A
M2.3	IEC 60947-2		N/A
M2.4	rated voltage of the voltage source (U_s)		N/A
M2.5	rated voltage of the monitored circuit (U_n)		N/A
M2.6	rated impulse voltage of the monitored circuit (U_{imp})		N/A
	Provided in the manufacturer's literature:		
M3.1	rated frequency of the voltage source		N/A
M3.2	rated impulse withstand voltage of the voltage source (U_{imp})		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
M3.3	output characteristics and/or specified current-breaking device(s)		N/A
M3.4	rated residual non-operating current if it differs from $0,5 I_{\Delta n}$ ($I_{\Delta no}$)		N/A
M3.5	rated frequency of the monitored circuit		N/A
M3.6	lowest residual current setting at $6 I_n$ for MRCDs with separate sensing means		N/A
M3.7	rated conditional short-circuit current (I_{cc}) and/or rated short-time withstand current (I_{cw}), and rated conditional residual short-circuit current ($I_{\Delta c}$)		N/A
M3.8	IP code, where applicable		N/A
M3.9	position of use and mounting precautions		N/A
M3.10	wiring diagram		N/A
M3.11	connection of the sensing means to the processing device (cable type, length, etc.)		N/A
M3.12	for a through conductor type MRCD, dimensions of the conductor aperture (s) and the position relative to the sensing means		N/A
M3.13	for a terminal type MRCD, the maximum cross-sectional area of the conductors to be connected		N/A
M3.14	distances to be respected with regard to nearby conductors		N/A
M3.15	conditions to be observed for the connections between the processing device and the current-breaking device		N/A
M3.16	the SCPDs to be associated with the MRCD		N/A
M3.17	for a non-time-delay type, the current-breaking device(s) to be associated with MRCD to meet the maximum combination times of Table B.1		N/A
M3.18	for a time-delay type having a limiting non-operating time of 0,06 s, the current-breaking device(s) to be associated with the MRCD to meet the combination times of Table B.2		N/A
MI	Test sequence MI		
M.8.3.4	Off-load tests at $20\text{ °C} \pm 5\text{ °C}$		N/A
M.8.3.4.2	Verification of operating in case of steady increase of the residual current (figure M.1)		N/A
	Increase the residual current from $0,2 I_{\Delta n}$ to $I_{\Delta n}$ in 30 sec. Required: value between $I_{\Delta no}$ and $I_{\Delta n}$		N/A
	Min. setting $I_{\Delta n}$.(mA): Interm. setting $I_{\Delta n}$.(mA): Max. setting $I_{\Delta n}$.(mA):		N/A
M.8.3.4.3	Verification of operating in case of closing on residual current (figure M.2)		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	The MRCD is closes on $I\Delta n$ or each specified setting Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
M.8.3.4.4	Verification of operating in case of a sudden appearance of the residual current (figure M.2 and M3)		N/A
	A residual current is sudden appear on the MRCD of $I\Delta n$ Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
	A residual current is sudden appear on the MRCD of $2 I\Delta n$ Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
	A residual current is sudden appear on the MRCD of $\square 5 I\Delta n$ or $\square 0,25 A$ Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
	A residual current is sudden appear on the MRCD of $\square 10 I\Delta n$ or $\square 0,5 A$ Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the MRCD of $I_{\Delta n}$: 5 A Required : no value exceeds the specified limiting value of Table B1 (40 ms)		N/A
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A
	A residual current is sudden appear on the MRCD of $I_{\Delta n}$: 10 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A
	A residual current is sudden appear on the MRCD of $I_{\Delta n}$: 20 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A
	A residual current is sudden appear on the MRCD of $I_{\Delta n}$: 50 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A
	A residual current is sudden appear on the MRCD of $I_{\Delta n}$: 100 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$.(ms): Interm. setting $I_{\Delta n}$.(ms): Max. setting $I_{\Delta n}$.(ms):		N/A
	A residual current is sudden appear on the MRCD of $I_{\Delta n}$: 200 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
	A residual current is sudden appear on the MRCD of $I\Delta n$: 500 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
M.8.3.4.5	Verification of the limiting non-operating time of time delayed type MRCDs (figure M3)		N/A
	A residual current is sudden appear on the MRCD of 2 $I\Delta n$ for a time declared by the manufacturer Required : The MRCD shall not operated		N/A
	Min. setting $I\Delta n$. Min. setting time delay (ms): Min. setting $I\Delta n$. Max. setting time delay (ms):		N/A
M.8.3.5	Tests at the temperature limits		
M.8.3.5.1	General (clause B.8.2.5 applies)		
	Minimum temperature (°C)		
	Maximum temperature (°C)		
M.8.3.5.2	Off load tests at -5°C or minimum temperature limit (figure M.2 and M3)		
	A residual current is sudden appear on the MRCD of $I\Delta n$ Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A
	A residual current is sudden appear on the MRCD of 2 $I\Delta n$ Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting $I\Delta n$.(ms): Interm. setting $I\Delta n$.(ms): Max. setting $I\Delta n$.(ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the MRCD of ☐ 5 I Δ n or ☐ 0,25 A Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ 10 I Δ n or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	Verification of the limiting non-operating time of time delayed type MRCDs at -5°C or minimum temperature limit (figure M3)		
	A residual current is sudden appear on the MRCD of 2 I Δ n for a time declared by the manufacturer Required : The MRCD shall not operated		N/A
	Min. setting I Δ n. Min. setting time delay (ms): Min. setting I Δ n. Max. setting time delay (ms):		N/A
M.8.3.5.3	Of load tests at +40°C or maximum temperature limit (figure M.2 and M3)		N/A
	A residual current is sudden appear on the MRCD of I Δ n Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A
	A residual current is sudden appear on the MRCD of 2 I Δ n Required : no value exceeds the specified limiting value of Table B1 (150 ms) or Table B2 (200 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I Δ n.(ms): Interm. setting I Δ n.(ms): Max. setting I Δ n.(ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the MRCD of ☐ 5 I _{Δn} or ☐ 0,25 A Required : no value exceeds the specified limiting value of Table B1 (40ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I _{Δn} .(ms): Interm. setting I _{Δn} .(ms): Max. setting I _{Δn} .(ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ 10 I _{Δn} or ☐ 0,5 A Required : no value exceeds the specified limiting value of Table B1 (40 ms) or Table B2 (150 ms) and a non-actuating time of 60 ms		N/A
	Min. setting I _{Δn} .(ms): Interm. setting I _{Δn} .(ms): Max. setting I _{Δn} .(ms):		N/A
	A residual current is sudden appear on the MRCD of 2 I _{Δn} for a time declared by the manufacturer Required : The MRCD shall not operated		N/A
	Min. setting I _{Δn} . Min. setting time delay (ms): Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
M.8.4.	Verification of dielectric properties		
M.8.4.1	Verification of rated impulse withstand voltage		
	rated impulse withstand voltage		
	test impulse voltage (see table 12 part 1)		
	test impulse voltage for isolating (see table 14 part 1)		
M.8.4.1.2	Verification of rated impulse withstand voltage with respect to the monitored circuit		N/A
M.8.4.1.2.1	Test for terminal type MRCD		N/A
M.8.4.1.2.2	Tests for MRCDs of through-conductor type		N/A
M.8.4.1.3	Verification of rated impulse withstand voltage of the voltage source circuit (if applicable)		N/A
M.8.5	Verification of the operation of the test device at the limits of the rated voltage		N/A
	For MRCDs having an adjustable time-delay the test is made at the maximum setting of time-delay:	_____ s	
M.8.5.a	Setting I _{Δn} or minimum setting of I _{Δn}	_____ A	
	Test voltage (1,1 x U _e max)	_____ V	
	Number of operations	25	
	Interval time	5 s	

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Tripping	☐ Yes / ☐ No	N/A
M.8.5.b	Setting $I_{\Delta n}$ or minimum setting of $I_{\Delta n}$	_____ A	
	Test voltage (0,85 x U_e max)	_____ V	
	Number of operations	3	
	Interval time	5 s	
	Tripping	☐ Yes / ☐ No	N/A
M.8.5.c	Setting $I_{\Delta n}$ or minimum setting of $I_{\Delta n}$	_____ A	
	Test voltage (1,1 x U_e max)	_____ V	
	Number of operations	1	
	Operating means of the test device held in close position	5 s	
	Tripping		N/A
M.8.6	Verification of the limiting value of non-operating current under overcurrent conditions, in case of a single phase load.		N/A
	Circuit diagram	Fig. M4 _____	
	Setting $I_{\Delta n}$ or minimum setting of $I_{\Delta n}$ if adjustable	_____ A	
	Test current equal to the lower value of: ☐ 6 x I_n or ☐ 80 % of the maximum short-circuit release current setting	_____ A	
	Test voltage: ☐ rated voltage or ☐ any convenient voltage	_____ V	
	Test frequency	_____ Hz	
	Power factor (0,5)	_____	
	Current flow time	2 s	
	Interval time	60 s	
	Calibration plot number	_____	
	No tripping / change of state		N/A
M.8.7	Resistance against unwanted tripping due to surge currents resulting from impulse voltages		N/A
M.8.7.2	Verification of the resistance to unwanted tripping in case of loading of the network capacitance		N/A
B.8.6.2	Current surge test for RMCDs (0,5 μ s / 100kHz ring wave test)		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	One pole of the MRCD is submitted to 10 applications of a surge current according to the following requirements:		
	- peak value: 200 A + 10/0%		
	- virtual front time: 0,5 μ s $\pm$ 30%		
	- period of the following oscillatory wave: 10 μ s $\pm$ 20%		
	- each successive peak: about 60% of the preceding peak		
	The polarity shall be inverted after every two applications		
	The interval between two consecutive applications shall be about 30 s		
	During the test the MRCD shall not trip:		N/A
M.8.7.3	Verification of the resistance to unwanted tripping in case of flashover without follow-on current.		N/A
B.8.6.3	Verification of behaviour at surge current up to 250 A (8/20 μ s surge current test)		N/A
	One pole of the MRCD is submitted to 10 applications of a surge current according to the following requirements:		
	- peak value: 250 A + 10/0%		
	- virtual front time: 8 μ s $\pm$ 20%		
	- virtual time to half value: 20 μ s $\pm$ 20%		
	- peak of reverse current: less than 30% of peak value		
	The polarity shall be inverted after every two applications		
	The interval between two consecutive applications shall be about 30 s		
	During the test the MRCD shall not trip:		N/A
B.8.6.3	Verification of behaviour at surge current up to 3000 A (8/20 μ s surge current test)		N/A
	One pole of the MRCD is submitted to 10 applications of a surge current according to the following requirements:		
	- peak value: 3000 A + 10/0%		
	- virtual front time: 8 μ s $\pm$ 20%		
	- virtual time to half value: 20 μ s $\pm$ 20%		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- peak of reverse current:: less than 30% of peak value		
	The polarity shall be inverted after every two applications		
	The interval between two consecutive applications shall be about 30 s		
	During the test, the MRCD with tripping allowed:		N/A
M.8.8	Verification of the behaviour in case of an earth fault current comprising a d.c. component.		N/A
M.8.8.2	Type A MRCD		N/A
	For MRCDs the operation of which depends on a voltage source the test are made at 1,1 and 0,85 times the rated voltage of the voltage source (Us).		
M.8.8.2.2	Verification of operation in case of a continuous rise of a residual pulsating direct current		N/A
	Rated voltage	_____ V	
B.8.7.2.1	- steady increase from zero to: 1,4 I Δ n for I Δ n > 0,030 A with 1,4 I Δ n/30 A/s (mA)	_____ mA	
	- steady increase from zero to: 2 I Δ n for I Δ n ≤ 0,030 A with 2 I Δ n/30 A/s (mA)	_____ mA	
	- angle = 0 (+/-) :		
	- angle = 90 (+/-) :		
	- angle = 135 (+/-) :		
	No value exceeds the relevant specified limiting values		N/A
M.8.8.2.3	Verification of operation in case of a suddenly appearing residual pulsating direct current		N/A
B.8.7.2.2	Verification of the correct operation in case of suddenly appearing residual pulsating direct currents by closing S2 (angle = 0°)		N/A
	Rated voltage	_____ V	
	MRCD's with I Δ n > 0,030 A:		
	- maximum break time (ms) at: 1,4 I Δ n (+/-) :		
	- maximum break time (ms) at: 2,8 I Δ n (+/-) :		
	- maximum break time (ms) at: 7 I Δ n (+/-) :		
	- maximum break time (ms) at: 14 I Δ n (+/-) :		
	No value exceeds the relevant specified limiting value		N/A
	MRCD's with I Δ n ≤ 0,030 A:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- maximum break time (ms) at: $2 I_{\Delta n}$ (+/-) :		
	- maximum break time (ms) at: $4 I_{\Delta n}$ (+/-) :		
	- maximum break time (ms) at: $10 I_{\Delta n}$ (+/-) :		
	- maximum break time (ms) at: $20 I_{\Delta n}$ (+/-) :		
	No value exceeds the relevant specified limiting value		N/A
M.8.8.2.4	Verification of operation with load at reference temperature		N/A
	Rated voltage	_____ V	
B.8.7.2.1	- steady increase from zero to: $1,4 I_{\Delta n}$ for $I_{\Delta n} > 0,030$ A with $1,4 I_{\Delta n}/30$ A/s (mA)	_____ mA	
	- steady increase from zero to: $2 I_{\Delta n}$ for $I_{\Delta n} \leq 0,015$ A with $2 I_{\Delta n}/30$ A/s (mA)	_____ mA	
	- angle = 0 (+/-) :		
	- angle = 90 (+/-) :		
	- angle = 135 (+/-) :		
	No value exceeds the relevant specified limiting values		N/A
M.8.8.2.5	Verification of operation of a residual pulsating direct current superimposed by a smooth direct current of 6 mA.		N/A
	Rated voltage	_____ V	
B.8.7.2.4	- steady increase from zero to: $1,4 I_{\Delta n}$ for $I_{\Delta n} > 0,030$ A with $1,4 I_{\Delta n}/30$ A/s (mA) + 6 mA	_____ mA	
	- steady increase from zero to: $2 I_{\Delta n}$ for $I_{\Delta n} \leq 0,030$ A with $2 I_{\Delta n}/30$ A/s (mA) + 6 mA	_____ mA	
	- angle = 0 (+/-) :		
	No value exceeds the relevant specified limiting values		N/A
M.8.8.3	Type B MRCD		N/A
M.8.8.3.1	Verification of the correct operation in the case of a steady increase of composite residual current		N/A
B.8.8.1	The ratio of the different frequencies shall be maintained from the initial value up to the operating value		N/A
	The test shall be repeated three times through one pole chosen at random.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Increase the residual current from 0,2 $I_{\Delta n}$ to 1,4 $I_{\Delta n}$ in 30 sec. Required: value between 0,5 $I_{\Delta n}$ and $I_{\Delta n}$		N/A
	Min. setting $I_{\Delta n}$.(mA):		N/A
M.8.8.3.2	Verification of the correct operation in the case of sudden appearance of composite residual current		N/A
B.8.8.2	The MRCD is closes on 5 x 1,4 $I_{\Delta n}$		
	Required : operating time, no value exceeds the value declared by the manufacturer for 5 $I_{\Delta n}$		
	Required : combination time, no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms).		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	Time-delayed MRCDs shall additionally be tested to verify the non-operating time:		N/A
	A residual current is sudden appear on the MRCD of 2 $I_{\Delta n}$ for a duration of 0,06 s or declared by the manufacturer		
	Required : Three tests are made, at an interval of at least 1 min. The MRCD shall not operate		
	Min. setting $I_{\Delta n}$. Min. setting time delay (ms):		N/A
M.8.8.3.3	Verification of the correct operation in the case of residual sinusoidal alternating currents up to 1 000 Hz		N/A
a)	The test shall be carried out on one pole taken at random and repeated five times:		N/A
	Test switch S_1 and S_2 and MRCD in closed position		N/A
	Test at 150 Hz:		N/A
	steady increase from max. 0,2 $I_{\Delta n}$ to 2,4 $I_{\Delta n}$ within 30s		N/A
	- tripping current between 0,5 $I_{\Delta n}$ and 2,4 $I_{\Delta n}$ (mA)		N/A
	Min. setting $I_{\Delta n}$.(mA):		N/A
	Test at 400 Hz:		N/A
	steady increase from max. 0,2 $I_{\Delta n}$ to 6 $I_{\Delta n}$ within 30s		N/A
	- tripping current between 0,5 $I_{\Delta n}$ and 6 $I_{\Delta n}$ (mA)		N/A
	Min. setting $I_{\Delta n}$.(mA):		N/A
	Test at 1000 Hz:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	steady increase from max. $0,2 I_{\Delta n}$ to $14 I_{\Delta n}$ within 30s		N/A
	- tripping current between $I_{\Delta n}$ and $14 I_{\Delta n}$ (mA)		N/A
	Min. setting $I_{\Delta n}$.(mA):		N/A
b)	Two measurements of the operating/combination time are made on one pole chosen at random:		N/A
	S1 and MRCD in the closed position, residual current correspond to 1000Hz suddenly established by closing S2, the MRCD is closes on $14 I_{\Delta n}$		N/A
	Required : operating time, no value exceeds the value declared by the manufacturer for $I_{\Delta n}$		N/A
	Required : combination time, no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms).		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
M.8.8.3.4	verification of the correct operation in the case of a residual alternating current superimposed by a residual smooth direct current		
B.8.8.4	Test switch S ₁ and S ₂ and MRCD in closed position		N/A
	Residual smooth direct current applied through one pole chosen at random and adjusted to		N/A
	- $0,4 I_{\Delta n}$ or		N/A
	- 10 mA		N/A
	whichever is the higher value		N/A
	Residual alternating current at rated frequency applied to another pole and:		N/A
	steady increase from max. $0,2 I_{\Delta n}$ to $I_{\Delta n}$ within 30 s		N/A
	alternating tripping current $\leq I_{\Delta n}$ (mA)		N/A
	MRCD tested twice at positions I of S ₃ :		N/A
	Min. setting $I_{\Delta n}$.(mA):		N/A
	MRCD tested twice at positions II of S ₃ :		N/A
	Min. setting $I_{\Delta n}$.(mA):		N/A
M.8.8.3.5	Verification of the correct operation in the case of a residual pulsating direct current superimposed by a residual smooth direct current		
B.8.8.5	Test switch S ₁ and S ₂ and MRCD in closed position		N/A
	Residual smooth direct current applied through one pole chosen at random and adjusted to		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	- 0,4 $I_{\Delta n}$ or		N/A
	- 10 mA		N/A
	whichever is the higher value		N/A
	Residual pulsating direct current applied to another pole with a current delay angle of 0° and:		N/A
	steady increase from max. 0,2 $I_{\Delta n}$ to 1,4 $I_{\Delta n}$ within 30 s (mA) for CBRs with $I_{\Delta n} \geq 0,030$ A		N/A
	residual pulsating tripping current $\leq 1,4 I_{\Delta n}$ (mA) for MRCDs with $I_{\Delta n} \geq 0,030$ A		N/A
	steady increase from max. 0,2 $I_{\Delta n}$ to 2 $I_{\Delta n}$ within 30 s (mA) for RCBOs with $I_{\Delta n} < 0,030$ A		N/A
	residual pulsating tripping current $\leq 2 I_{\Delta n}$ (mA) for MRCDs with $I_{\Delta n} < 0,030$ A		N/A
	MRCD tested twice at position I of S_3		N/A
	Min. setting $I_{\Delta n}$ (mA):		N/A
	MRCD tested twice at position II of S_3		N/A
	Min. setting $I_{\Delta n}$ (mA):		N/A
M.8.8.3.6	Verification of the correct operation in the case of residual direct currents which can result from rectifying circuits supplied from two phases		
B.8.8.6	The MRCD shall be connected to the test circuit at two line terminals chosen at random		N/A
a)	Test switch S_1 and S_2 and CBR in closed position		N/A
	Residual pulsating direct current:		N/A
	steady increase from max. 0,2 $I_{\Delta n}$ to 2 $I_{\Delta n}$ within 30 s		N/A
	tripping current between 0,5 $I_{\Delta n}$ and 2 $I_{\Delta n}$		N/A
	MRCD tested five times at positions I of S_3 :		N/A
	Min. setting $I_{\Delta n}$ (mA):		N/A
	MRCD tested five times at positions II of S_3 :		N/A
	Min. setting $I_{\Delta n}$ (mA):		N/A
b)	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S_1 and the MRCD being in the closed position, the residual current is suddenly establish by closing the switch S_2		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Five measurements of the operating/combination time are made at each of these values of residual current at each position I and II of S ₃		N/A
	MRCD tested at position I of S ₃ :		N/A
	A residual current is sudden appear on the MRCD of 2 I _{Δn} Required : Operating time: no value exceeds the value declared by the manufacturer for I _{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of 4 I _{Δn} Operating time: no value exceeds the value declared by the manufacturer for 2 I _{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ 10 I _{Δn} or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for 5 I _{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ 20 I _{Δn} or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 I _{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	CBR tested at position II of S ₃ :		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the MRCD of $2 I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-operating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of $4 I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $2 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-operating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ $10 I_{\Delta n}$ or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for $5 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ $20 I_{\Delta n}$ or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for $10 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
M.8.8.3.7	Verification of the correct operation in the case of residual direct currents which can result from rectifying circuits supplied from three phases		
B.8.8.7	This test does not apply to 2-pole type B MRCDs		
a)	Test switch S_1 and S_2 and MRCD in closed position		
	Residual pulsating direct current:		
	steady increase from max. $0,2 I_{\Delta n}$ to $2 I_{\Delta n}$ within 30 s		
	tripping current between $0,5 I_{\Delta n}$ and $2 I_{\Delta n}$		
	MRCD tested five times at positions I of S_3 :		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$ (mA):		N/A
	MRCD tested five times at positions II of S_3 :		
	Min. setting $I_{\Delta n}$ (mA):		N/A
b)	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S_1 and the MRCD being in the closed position, the residual current is suddenly establish by closing the switch S_2		
	Five measurements of the operating/combination time are made at each of these values of residual current at each position I and II of S_3		
	MRCD tested at position I of S_3 :		
	A residual current is sudden appear on the MRCD of $2 I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-operating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of $4 I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $2 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-operating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ $10 I_{\Delta n}$ or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for $5 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ $20 I_{\Delta n}$ or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for $10 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	MRCD tested at position II of S_3 :		
	A residual current is sudden appear on the MRCD of $2 I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-operating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of $4 I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $2 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-operating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of $\square 10 I_{\Delta n}$ or $\square 0,5 A$ Operating time: no value exceeds the value declared by the manufacturer for $5 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of $\square 20 I_{\Delta n}$ or $\square 1,0 A$ Operating time: no value exceeds the value declared by the manufacturer for $10 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
M.8.8.3.8	Verification of the correct operation in the case of residual smooth direct current		N/A
M.8.8.3.8.1	Verification of the correct operation in the case of residual smooth direct current without load		N/A
	One current path, chosen at random		N/A
a)	Switch S_1 and S_2 and S_a being closed		N/A
	Residual smooth direct current:		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	steady increase from zero to $2 I_{\Delta n}$ within 30 s		N/A
	tripping current between $0,5 I_{\Delta n}$ and $2 I_{\Delta n}$		N/A
	MRCD tested twice at positions I of S_3 :		N/A
	Min. setting $I_{\Delta n}$.(mA):		N/A
	MRCD tested twice at positions II of S_3 :		N/A
	Min. setting $I_{\Delta n}$.(mA):		N/A
b)	The test shall be carried out at each value of residual current specified in Table B.1, multiplied by two		N/A
	Two operating time measurements are performed for each value, the switch S_3 being in position I for the first measurement and in position II for the second measurement.		N/A
	the switch S_3 being in position I:		N/A
	A residual current is sudden appear on the MRCD of $2 I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-operating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $4 I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $2 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-operating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ $10 I_{\Delta n}$ or ☐ $0,5 A$ Operating time: no value exceeds the value declared by the manufacturer for $5 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of ☐ 20 I_{Δn} or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	the switch S3 being in position II::		N/A
	A residual current is sudden appear on the CBR of 2 I_{Δn} Operating time: no value exceeds the value declared by the manufacturer for I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I_{Δn} Operating time: no value exceeds the value declared by the manufacturer for 2 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I_{Δn} or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for 5 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 I_{Δn} or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
M.8.8.3.8.2	Verification of the correct operation in the case of residual smooth direct current with load		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	The current path under test and another current path of the MRCD being loaded with the rated current.		N/A
	Test current:		N/A
M.8.8.3.8.1 a)	One current path, chosen at random		N/A
	Switch S ₁ and S ₂ and S _a being closed		N/A
	Residual smooth direct current:		N/A
	steady increase from zero to 2 I _{Δn} within 30 s		N/A
	tripping current between 0,5 I _{Δn} and 2 I _{Δn}		N/A
	MRCD tested twice at positions I of S ₃ :		N/A
	Min. setting I _{Δn} .(mA):		N/A
	MRCD tested twice at positions II of S ₃ :		N/A
	Min. setting I _{Δn} .(mA):		N/A
M.8.8.3.9	Tests at the temperature limits		
M.8.8.3.6	Verification of the correct operation in the case of residual direct currents which can result from rectifying circuits supplied from two phases		
B.8.8.6	The MRCD shall be connected to the test circuit at two line terminals chosen at random		N/A
a)	Test switch S ₁ and S ₂ and CBR in closed position		N/A
	Residual pulsating direct current:		N/A
	steady increase from max. 0,2 I _{Δn} to 2 I _{Δn} within 30 s		N/A
	tripping current between 0,5 I _{Δn} and 2 I _{Δn}		N/A
	MRCD tested five times at positions I of S ₃ :		N/A
	Min. setting I _{Δn} .(mA): Max. setting I _{Δn} .(mA):		N/A
	MRCD tested five times at positions II of S ₃ :		N/A
	Min. setting I _{Δn} .(mA): Max. setting I _{Δn} .(mA):		N/A
b)	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S ₁ and the MRCD being in the closed position, the residual current is suddenly establish by closing the switch S ₂		N/A
	Five measurements of the operating time are made at each of these values of residual current at each position I and II of S ₃		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	MRCD tested at position I of S ₃ :		N/A
	A residual current is sudden appear on the MRCD of 2 I _{Δn} Required : Operating time: no value exceeds the value declared by the manufacturer for I _{Δn}		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of 4 I _{Δn} Operating time: no value exceeds the value declared by the manufacturer for 2 I _{Δn}		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ 10 I _{Δn} or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for 5 I _{Δn}		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ 20 I _{Δn} or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 I _{Δn}		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	CBR tested at position II of S ₃ :		N/A
	A residual current is sudden appear on the MRCD of 2 I _{Δn} Operating time: no value exceeds the value declared by the manufacturer for I _{Δn}		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of 4 I _{Δn} Operating time: no value exceeds the value declared by the manufacturer for 2 I _{Δn}		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the MRCD of ☐ 10 I _{Δn} or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for 5 I _{Δn}		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ 20 I _{Δn} or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 I _{Δn}		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
M.8.8.3.7	Verification of the correct operation in the case of residual direct currents which can result from rectifying circuits supplied from three phases		N/A
B.8.8.7	This test does not apply to 2-pole type B MRCDs		
a)	Test switch S ₁ and S ₂ and MRCD in closed position		
	Residual pulsating direct current:		
	steady increase from max. 0,2 I _{Δn} to 2 I _{Δn} within 30 s		
	tripping current between 0,5 I _{Δn} and 2 I _{Δn}		
	MRCD tested five times at positions I of S ₃ :		
	Min. setting I _{Δn} .(mA): Max. setting I _{Δn} .(mA):		N/A
	MRCD tested five times at positions II of S ₃ :		
	Min. setting I _{Δn} .(mA): Max. setting I _{Δn} .(mA):		N/A
b)	The test circuit being successively calibrated at 2 times value given in Table B.1, the switch S ₁ and the MRCD being in the closed position, the residual current is suddenly establish by closing the switch S ₂		
	Five measurements of the operating time are made at each of these values of residual current at each position I and II of S ₃		
	MRCD tested at position I of S ₃ :		
	A residual current is sudden appear on the MRCD of 2 I _{Δn} Operating time: no value exceeds the value declared by the manufacturer for I _{Δn}		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of 4 $I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for 2 $I_{\Delta n}$		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for 5 $I_{\Delta n}$		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ 20 $I_{\Delta n}$ or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 $I_{\Delta n}$		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	MRCD tested at position II of S_3 :		
	A residual current is sudden appear on the MRCD of 2 $I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $I_{\Delta n}$		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of 4 $I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for 2 $I_{\Delta n}$		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the MRCD of ☐ 10 $I_{\Delta n}$ or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for 5 $I_{\Delta n}$		
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the MRCD of ☐ 20 I _{Δn} or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 I _{Δn}		
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
a)	Ambient air temperature: -5 °C, without load		
M.8.8.3.8.1 b)	The test shall be carried out at each value of residual current specified in Table B.1, multiplied by two		N/A
	Two operating time measurements are performed for each value, the switch S ₃ being in position I for the first measurement and in position II for the second measurement.		N/A
	the switch S ₃ being in position I:		N/A
	A residual current is sudden appear on the MRCD of 2 I _{Δn} Operating time: no value exceeds the value declared by the manufacturer for I _{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I _{Δn} Operating time: no value exceeds the value declared by the manufacturer for 2 I _{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I _{Δn} or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for 5 I _{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms): Max. setting I _{Δn} . Max. setting time delay (ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of ☐ 20 I_{Δn} or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	the switch S3 being in position II::		N/A
	A residual current is sudden appear on the CBR of 2 I_{Δn} Operating time: no value exceeds the value declared by the manufacturer for I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I_{Δn} Operating time: no value exceeds the value declared by the manufacturer for 2 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I_{Δn} or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for 5 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 I_{Δn} or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
b)	Ambient air temperature: +40 °C, and, for terminal-type MRCDs, the MRCD having been previously loaded with its rated current until it has attained thermal steady-state conditions		N/A
	Test current (A):		N/A
M.8.8.3.8.1 b)	The test shall be carried out at each value of residual current specified in Table B.1, multiplied by two		N/A
	Two operating time measurements are performed for each value, the switch S3 being in position I for the first measurement and in position II for the second measurement.		N/A
	the switch S3 being in position I:		N/A
	A residual current is sudden appear on the MRCD of $2 I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-operating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of $4 I_{\Delta n}$ Operating time: no value exceeds the value declared by the manufacturer for $2 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-operating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ $10 I_{\Delta n}$ or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for $5 I_{\Delta n}$ Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the CBR of ☐ 20 I_{Δn} or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	the switch S3 being in position II::		N/A
	A residual current is sudden appear on the CBR of 2 I_{Δn} Operating time: no value exceeds the value declared by the manufacturer for I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (300 ms) or Table B.2 (500 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of 4 I_{Δn} Operating time: no value exceeds the value declared by the manufacturer for 2 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (150 ms) or Table B.2 (200 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 10 I_{Δn} or ☐ 0,5 A Operating time: no value exceeds the value declared by the manufacturer for 5 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A
	Min. setting I _{Δn} . Max. setting time delay (ms):		N/A
	Max. setting I _{Δn} . Max. setting time delay (ms):		N/A
	A residual current is sudden appear on the CBR of ☐ 20 I_{Δn} or ☐ 1,0 A Operating time: no value exceeds the value declared by the manufacturer for 10 I_{Δn} Combination time: no value exceeds the specified limiting value of Table B.1 (40 ms) or Table B.2 (150 ms) and a non-operating time of 60 ms		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Min. setting $I_{\Delta n}$. Max. setting time delay (ms): Max. setting $I_{\Delta n}$. Max. setting time delay (ms):		N/A
M.8.9.	Verification of the behaviour of MRCDs with separate sensing means in case of a failure of the sensing means connection		N/A
M.8.9.2	Test method 1		N/A
	Rated voltage of the sensing means		
	Interval time Required <5 sec		N/A
M.8.9.3	Test method 2		N/A
	Test shall be carried out as follows: - The test device is activated - The sensing means are disconnected and the test device is activated. The MRCD shall not operate		
	Rated voltage of the sensing means		
	Test device activated MRCD shall operate		N/A
	Rated voltage of the sensing means		
	Sensing device disconnected and Test device activated MRCD shall not operate		N/A
M.8.10	Verification of temperature-rise of terminal type MRCDs		N/A
M.8.10.2	T _{ambient} : _____ °C		
	Main circuits		
	Conventional thermal current I_{th}	_____ A	
	Conventional thermal current for enclosure I_{the}	_____ A	
	Conventional thermal current for the neutral pole	_____ A	
	Cabling characteristics		
	Cable	_____ mm ²	
	Bar / number / length	_____ mm / ____ / ____ m	
	Arrangement	☐ 3 phase - ☐ poles in series	
	Tightening torque	_____ Nm	
	Neutral pole (if applicable)		
	Cable	_____ mm ²	
	Bar / number / length	_____ mm / ____ / ____ m	
	Tightening torque	_____ Nm	
	Terminals(see table 2)		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Manual operating means		
	Parts which need not be touched but not hand held		
	Parts which need not be touched during normal operation		
M.8.11	Verification of mechanical and electrical endurance		N/A
	For MRCDs having more than one output rating, two tests shall be made: - a test at the highest rated current at the corresponding voltage; - a test at the highest rated voltage at the corresponding current.		
	500 off-load operations controlled by the test device		
	Rated voltage:	_____ V ☐ ac ☐ dc	
	Result:	after____ operations,	N/A
	500 off load operations by passing the rated residual operating current $I_{\Delta n}$ through one current path		
	Rated voltage:	_____ V ☐ ac ☐ dc	
	Rated residual current	_____ mA	
	Result:	after____ operations,	N/A
	500 on-load operations controlled by the test device		
	Rated voltage:	_____ V ☐ ac ☐ dc	
	Test current	_____ A	
	Power factor	_____	
	Result:	after____ operations,	N/A
	500 on-load operations by passing the rated residual operating current $I_{\Delta n}$ through one current path.		
	Rated voltage:	_____ V ☐ ac ☐ dc	
	Test current	_____ A	
	Power factor	_____	
	Rated residual current	_____ mA	
	Result:	after____ operations,	N/A
	Show no damage		N/A
	High voltage test: twice rated voltage	Test voltage: _____ V	N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	A residual current is sudden appear on the MRCD of $I_{\Delta n}$ (____ mA) Required : no value exceeds the specified limiting value of Table B1 (300 ms) or Table B2 (500 ms) and a non-actuating time of 60 ms		N/A
M.8.12.	Verification of the behaviour of MRCDs in case of failure of the voltage source for MRCDs classified under M.3.2.2.1		N/A
M.8.12.2	Determination of the limiting value of the voltage source		N/A
	Source voltage (U_s)	Max U_s : ____ V ☐ ac ☐ dc	
		Min U_s : ____ V ☐ ac ☐ dc	
	Adjustable residual current setting	____ mA (lowest)	
	Adjustable time-delay setting	____ s	
	Time period of voltage decreasing	30 s or a period enough with respect to delayed opening	
	Min voltage to automatic opening ($U > 0,85 \times U_s$)		
	A residual current is sudden appear on the MRCD of $I_{\Delta n}$ (____ mA) at a value just above highest measured value Required : no value exceeds the specified limiting value of Table B1: 300 ms		N/A
	It's not possible to switch "ON" by manual operating means at a lower value than the lower measured value.		N/A
M.8.12.3	Verification of automatic opening in case of voltage source failure		N/A
	Source voltage (U_s)	Max U_s : ____ V ☐ ac ☐ dc	
		Min U_s : ____ V ☐ ac ☐ dc	
	Adjustable residual current setting	____ mA (lowest)	
	Adjustable time-delay setting	____ s	
	Time period	Max 1 s or max. 1 s + time delay setting	
	Time period to automatic opening		
	No value exceeds the relevant specified limiting value		N/A
M.8.13	Verification of the behaviour of MRCDs with voltage source as classified under M.3.2.2.2 in case of failure of the voltage source.		N/A
	Source voltage (U_s)	Max U_s : ____ V ☐ ac ☐ dc	
		Min U_s : ____ V ☐ ac ☐ dc	
	Adjustable residual current setting	____ mA (lowest)	

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Adjustable time-delay setting	_____ s	
	Switch off and reclosed Sa or S1 and reduced the source voltage to 70 %	70% Us = _____ V ☐ ac ☐ dc	N/A
	Time period to automatic opening		N/A
MII	Test sequence MII		N/A
M.8.14	Verification of the behaviour of the MRCD under short-circuit conditions		N/A
	Type designation or catalogue reference		
	Sample no:		
M.8.14.3	Verification of the rated conditional short-circuit current (I_{cc})		N/A
	Verification of the coordination between the MRCD and the SCPD		
	Test circuit according to figure:		
	Point of test circuit which is directly earthed:		
	Grid distance "a" (mm):		
	Silver wire diameter (mm):		
	Used SCPD during the tests		
	Prospective current (A):		
	Prospective current obtained (A):		
	Power factor / ratio n :		
	Power factor / ratio n obtained:		
	Plot no.		
	Test sequence: O-t-O		
	I^2t (kA ² s); I_p (kA):	First O: I_p : _____ kA I^2t ; _____ kA ² s Plot no. _____	
		Second O: I_p : _____ kA I^2t ; _____ kA ² s Plot no.: _____	
	If tested at separate testing station see report	No.: _____ of _____ testing station _____	

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	During tests no endangering of operator, no permanent arcing, no flashover and no melting of fuse F		N/A
	After the tests no damage impairing further use		N/A
8.3.3.6	Dielectric strength test of the main circuit at test voltage of 2 Un for 1 min:		N/A
	Test voltage		N/A
B.8.10.4.2	The RCCB shall trip with a test current of 1,25 I _{Δn} (ms) in minimum setting:	I test: _____ mA trip time: _____ ms	N/A
M.8.12.3	Verification of automatic opening in case of voltage source failure		N/A
	Source voltage (Us)	Max Us: _____ V ☐ ac ☐ dc	
		Min Us: _____ V ☐ ac ☐ dc	
	Adjustable residual current setting	_____ mA (lowest)	
	Adjustable time-delay setting	_____ s	
	Time period	Max 1 s or max. 1 s + time delay setting	
	Time period to automatic opening		N/A
	No value exceeds the relevant specified limiting value		N/A
	The polyethylene sheet shows no holes		N/A
			N/A
M.8.14.4	Verification of rated short-time withstand current (I _{cw})		N/A
	Test circuit according to figure:		
	Point of test circuit which is directly earthed:		
	Grid distance "a" (mm):		
	Prospective current (A):		
	Prospective current obtained (A):		
	Power factor / ratio <i>n</i> :		
	Power factor / ratio <i>n</i> obtained:		
	Plot no.		
	Test sequence: O		
	I ² t (kA ² s); I _p (kA):	I _p : _____ kA I ² t; _____ kA ² s Test duration: _____ ms Plot no. _____	

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	If tested at separate testing station see report	No.: _____ of _____ testing station	
	During tests no endangering of operator, no permanent arcing, no flashover and no melting of fuse F		N/A
	After the tests no damage impairing further use		N/A
8.3.3.6	Dielectric strength test of the main circuit at test voltage of 2 Un for 1 min:		
	Test voltage	-	N/A
B.8.10.4.2	The RCCB shall trip with a test current of 1,25 IΔn (ms) in minimum setting:	I test: _____ mA trip time: _____ ms	N/A
			N/A
M.8.12.3	Verification of automatic opening in case of voltage source failure		
	Source voltage (Us)	Max Us: _____ V ☐ ac ☐ dc	
		Min Us: _____ V ☐ ac ☐ dc	
	Adjustable residual current setting	_____ mA (lowest)	
	Adjustable time-delay setting	_____ s	
	Time period	Max 1 s or max. 1 s + time delay setting	
	Time period to automatic opening		N/A
	No value exceeds the relevant specified limiting value		N/A
	The polyethylene sheet shows no holes		N/A
			N/A
M.8.14.5	Verification of the rated conditional residual short-circuit current (IΔc)		N/A
	Test circuit according to figure:		
	Point of test circuit which is directly earthed:		
	Grid distance "a" (mm):		
	Silver wire diameter (mm):		
	Used SCPD during the tests		
	Prospective current (A):		
	Prospective current obtained (A):		
	Power factor / ratio n:		
	Power factor / ratio n obtained:		
	Plot no.		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Test sequence: O-t-O		
	I^2t (kA ² s); I_p (kA):	First O: I_p : _____ kA I^2t ; _____ kA ² s Plot no. _____	
		Second O: I_p : _____ kA I^2t ; _____ kA ² s Plot no.: _____	
	If tested at separate testing station see report	No.: _____ of _____ testing station _____	
	During tests no endangering of operator, no permanent arcing, no flashover and no melting of fuse F		N/A
	After the tests no damage impairing further use		N/A
8.3.3.6	Dielectric strength test of the main circuit at test voltage of 2 U_n for 1 min:		N/A
	Test voltage	-	N/A
B.8.10.4.2	The RCCB shall trip with a test current of 1,25 $I_{\Delta n}$ (ms) in minimum setting:	I test: _____ mA trip time: _____ ms	N/A
M.8.12.3	Verification of automatic opening in case of voltage source failure		N/A
	Source voltage (U_s)	Max U_s : _____ V ☐ ac ☐ dc	
		Min U_s : _____ V ☐ ac ☐ dc	
	Adjustable residual current setting	_____ mA (lowest)	
	Adjustable time-delay setting	_____ s	
	Time period	Max 1 s or max. 1 s + time delay setting	
	Time period to automatic opening		N/A
	No value exceeds the relevant specified limiting value		N/A
	The polyethylene sheet shows no holes		N/A
			N/A
M.8.14.6	Verification of rated residual short-time withstand current ($I_{\Delta w}$)		N/A
	Test circuit according to figure:		
	Point of test circuit which is directly earthed:		
	Grid distance "a" (mm):		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Prospective current (A):		
	Prospective current obtained (A):		
	Power factor / ratio n :		
	Power factor / ratio n obtained:		
	Plot no.		
	Test sequence: O		
	I^2t (kA ² s); I_p (kA):	I_p : _____ kA I^2t ; _____ kA ² s Test duration: _____ ms Plot no. _____	
	If tested at separate testing station see report	No.: _____ of _____ testing station _____	
	During tests no endangering of operator, no permanent arcing, no flashover and no melting of fuse F		N/A
	After the tests no damage impairing further use		N/A
8.3.3.6	Dielectric strength test of the main circuit at test voltage of 2 U_n for 1 min:		N/A
	Test voltage	-	N/A
B.8.10.4.2	The RCCB shall trip with a test current of 1,25 $I_{\Delta n}$ (ms) in minimum setting:	I test: _____ mA trip time: _____ ms	N/A
			N/A
M.8.12.3	Verification of automatic opening in case of voltage source failure		N/A
	Source voltage (U_s)	Max U_s : _____ V ☐ ac ☐ dc	
		Min U_s : _____ V ☐ ac ☐ dc	
	Adjustable residual current setting	_____ mA (lowest)	
	Adjustable time-delay setting	_____ s	
	Time period	Max 1 s or max. 1 s + time delay setting	N/A
	Time period to automatic opening		N/A
	No value exceeds the relevant specified limiting value		N/A
	The polyethylene sheet shows no holes		N/A
M.III	Test sequence MIII		N/A
M.8.15	Verification of effects of environmental conditions		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Type designation or catalogue reference		
	Sample no:		
B.8.10.4.2	The RCCB shall trip with a test current of 1,25 I _{Δn} (ms) in minimum setting:	I test: _____ mA trip time: _____ ms	N/A
M.IV	Test sequence MIV		N/A
M.8.16	Verification of electromagnetic compatibility		
	See report		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Annex N	Electromagnetic compatibility (EMC) – Additional requirements and test methods for devices not covered by Annexes B, F and M		
	See report	180600077SHA	P

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Annex O	Instantaneous trip circuit-breakers (ICB)		
O.3.2	Rated current (I_n)		
O.3.3	Rated short-circuit making capacity		
O.3.4	Rated short-circuit breaking capacities		
	ICBs may be assigned rated short-circuit breaking capacities different to the equivalent circuit breaker. NOTE ICBs may be assigned a rated short-circuit breaking capacity equal to or greater than I_{cu} of the equivalent circuit-breaker when associated with specified motor starters or overload relays, and tested according to the relevant clauses of IEC 60947-4-1 (see O.6.2).		N/A
O.4	Product information		
	An ICB shall be marked according to 5.2 as relevant.		N/A
	In addition, the ICB shall be marked in accordance with Table O.1		N/A
	Visible from the front when the circuit-breaker is installed as in service and actuator is accessible		
O1.1	- initialism "ICB"		N/A
	Marked on the product		
O2.1	Rated instantaneous short-circuit current setting I_i (actual values or multiples of rated current)		N/A
O.5	Constructional and performance requirements		
	An ICB, being derived from the equivalent circuit-breaker (see O.2.1), complies with all the applicable construction and performance requirements of Clause 7, except 7.2.1.2.4, item b).		N/A
O.6	Tests		
O.6.1	O.6.1 Test sequence of the ICB alone		
	The tests of this subclause are not required if - the short-circuit characteristics of the short-circuit releases and the main current paths of the ICB are the same as those of the equivalent circuit-breaker, or - the ICB is only rated and tested as an association (see O.6.2).		N/A
O.6.1.2	Test sequences		
	Tests shall be made according to sequences II and III of this standard without the verification of overload releases.		N/A
O.6.1.3	Verification of short-circuit releases		

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Following the test of O.6.1.2, a tripping test is made in accordance with 8.3.3.1.2 on each phase pole in turn, at the maximum setting of the rated instantaneous short-circuit current. The test is made at the value of the tripping current declared by the manufacturer for individual poles . The ICB shall trip.		N/A
O.6.2	ICB associated with a specified protected device (i.e. motor starter or overload relay)		
	The applicable test requirements for these associations are covered in the relevant sections of IEC 60947-4-1, specifically the following clauses: - co-ordination with short-circuit protective devices; - additional requirements for combination starters and protected starters suitable for isolation; - performance under short-circuit conditions; - co-ordination at the crossover current between the starter and associated SCPD. NOTE The symbol SCPD in IEC 60947-4-1 applies to various short-circuit protective devices, including the ICB.		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Annex P	DC circuit-breakers for use in photovoltaic (PV) applications		
P.4	Characteristics of PV circuit breakers		
	A PV circuit-breaker rated for use not only in PV applications shall have only one rated current.		N/A
	Rated operational voltage (V)		
	impulse withstand voltage (Uimp)		
P.5	Product information		
	Clause 5 applies with the modifications and additions in Table P.3		N/A
	Marked on the product		N/A
P2.1	IEC 60947-2:2016 - Annex P		N/A
	Provided in the manufacturer's literature		
P3.1	Method and diagram of series connection of poles, as necessary for each rating		N/A
P.8	Tests		
P 8.2	Compliance with constructional requirements		
	See subclause 8.2		N/A
P.8.3	Type tests		
	Type designation or catalogue reference		
	Sample no:		
P.8.3.1	PV circuit-breaker is derived from a circuit-breaker on which identical or more severe tests have already been conducted		N/A
	With reference to 8.3.1.2, following tests are omitted from test sequence I		
	Tripping characteristics d.c. characteristics conducted		N/A
P.8.3.2	General test conditions		
	the series connection of poles of the circuit-breaker is in accordance with the manufacturer instructions		N/A
	Samples are selected and tested according to column "Terminals marked line/load-No" of Table 10.		N/A
	For 8.3.2.2.5 the time constant for operational performance capability, short-circuit tests and critical d.c. load current test is equal to 1 ms.		N/A
P.8.3.9	Critical d.c. load current test		
	Type designation or catalogue reference		
	Sample no:	P-1	

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	The circuit-breaker is closed and opened 10 times on to each of the test currents, 5 times with the current flowing in the forward direction, and 5 times with the current flowing in the reverse direction.		N/A
	The time constant shall comply with P.8.3.2.		N/A
	If applicable, during the operational performance verification the breaker is subjected to 100 operations instead of 50.		N/A
P.8.3.10	Thermal cycling test		
	Type designation or catalogue reference		
	Sample no:		
	PV circuit-breakers shall be subjected to temperature cycling according to IEC 60068-2-14, test Nb, consisting of 50 cycles, each cycle consisting of - 1 h at - 40 °C - followed by 1 h at + 85 °C. - Temperature change rate shall be 1 K/min.		N/A
	After the 50 cycles, the devices are returned to room temperature of 25 ± 5 °C for a minimum of 3 h.		N/A
	No distortion or damage to parts affect normal operation and protection		N/A
	Verification of overload releases according to 8.3.3.2.3		N/A
	Verification of temperature rise at the main terminals in accordance with 8.3.2.5		N/A
	Temperature rise does not exceed the values given in Table 7;		N/A
	Verification of dielectric withstand according to 8.3.3.6		N/A
P.8.3.11	Climatic tests		
	Type designation or catalogue reference		
	Sample no:		
	PV circuit-breakers shall be subjected to the climatic tests of IEC 60947-1:2007/AMD1:2010/AMD2:2014 Annex Q, category B (dry heat test and the low temperature test are not required)		N/A
	During the damp heat test a functional test (mechanical operation according 8.4.2)) is done during the first 2 h of the first cycle at the test temperature and during the last 2 h of the second cycle at the test temperature		N/A
	verification of operational performance capability: routine tests to clause 8.4 excluding dielectric tests of 8.4.6:	-	N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict
	Verification of the calibration of overcurrent releases (8.4.3)		N/A
	Verification of the operation of undervoltage and shunt releases (8.4.4)		N/A
	Additional tests for CBRs to Annex B (8.4.5)		N/A
	Verification of clearances (8.4.7)		N/A
	Number of samples tested in accordance with the requirements of Table 10		N/A

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

8.3.3.7	TABLE: Heating Test(DS 1, 3P, 415V, 400A, Test sequence I)		P
	Test voltage (V)	-	—
	Ambient (°C).....	23,0	—
Thermocouple Locations		Max. temperature measured, (°C)	Max. temperature limit, (°C)
Terminal 1		59	80
Terminal 2		61	80
Terminal 3		62	80
Terminal 4		62	80
Terminal 5		64	80
Terminal 6		63	80
Manual operating means: ☐ metallic / ☒ non-metallic		15	35
Parts intended to be touched but not hand-held: ☐ metallic / ☒ non-metallic		19	50
Parts which need not be touched during normal operation		23	60
Supplementary information: N/A			

8.3.4.5	TABLE: Heating Test(DS 1, 3P, 400V, 415A, 50kA, 1 sample, Test sequence II)		p
	Test voltage (V)	-	—
	Ambient (°C).....	23,8	—
Thermocouple Locations		Max. temperature measured, (°C)	Max. temperature limit, (°C)
Terminal 1		60	80
Terminal 2		63	80
Terminal 3		64	80
Terminal 4		64	80
Terminal 5		67	80
Terminal 6		68	80
Supplementary information: N/A			

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

	TABLE: Heating test, resistance method					N/A
	Test voltage (V) :					—
	Ambient, t1 (°C) :					—
	Ambient, t2 (°C) :					—
Temperature rise of winding	R1 (Ω)	R2 (Ω)	ΔT (K)	Max. dT (K)	Insulation class	
Supplementary information:						

	TABLE: Dielectric Strength (impulse withstand voltage)		P
Test voltage applied between:	Test potential applied (V)	Breakdown / flashover (Yes/No)	
Between the line and load terminals of the equipment with the contacts in the open position	18,7kV	No	
Between all the terminals of the main circuit connected (including the control and auxiliary circuits connected to the main circuit) and the enclosure or mounting plate, with the contacts in all normal positions of operation;	14,9kV	No	
Between each pole of the main circuit and the other poles connected together and to the enclosure or mounting plate, with the contacts in all normal positions of operation	14,9kV	No	

Supplementary information:

	TABLE: Dielectric Strength (frequency withstand voltage)		P
Test voltage applied between:	Test potential applied (V)	Breakdown / flashover (Yes/No)	
Between the line and load terminals of the equipment with the contacts in the open position	2500V a.c.	No	
Between all the terminals of the main circuit connected (including the control and auxiliary circuits connected to the main circuit) and the enclosure or mounting plate, with the contacts in all normal positions of operation;	2500V a.c.	No	
Between each pole of the main circuit and the other poles connected together and to the enclosure or mounting plate, with the contacts in all normal positions of operation	2500V a.c.	No	

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Supplementary information:

TABLE: Electrical Data (in normal conditions)						N/A
fuse #	I rated (A)	U (V)	P (W)	I (mA)	I fuse (mA)	condition/status
Supplementary information:						

TABLE: Power Input Deviation						N/A
Input deviation of/at:	P rated (W)	P measured (W)	ΔP	Required ΔP	Remark	
Supplementary information:						

TABLE: insulation resistance measurements			N/A
Insulation resistance R between:	R (M Ω)	Required R (M Ω)	
Between mains poles (primary fuse disconnected)			
Between parts separated by basic or supplementary insulation			
Between parts separated by double or reinforced insulation			
Supplementary information:			

TABLE: Impact Resistance			N/A
Impacts per surface	Surface tested	Impact energy (Nm)	Comments

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

Supplementary information:

TABLE: Clearance And Creepage Distance Measurements						P
clearance cl and creepage distance dcr at/of:	Up (V)	U r.m.s. (V)	Required cl (mm)	cl (mm)	required dcr (mm)	dcr (mm)
Between each pole and the other poles	12,0kV	1000	14	17,36	20	27,46
Between live part and exposed conductive parts	12,0kV	1000	14	57,02	20	58,22
Between open contacts	12,0kV	1000	14	17,36	20	-
Supplementary information:						

TABLE: Distance Through Insulation Measurements					
Distance through insulation di at/of:	U r.m.s. (V)	Test voltage (V)	Required di (mm)	di (mm)	
Supplementary information:					

	TABLE: Ball Pressure Test of Thermoplastics			
Allowed impression diameter (mm) :				—
Object/ Part No./ Material	Manufacturer/ trademark	Test temperature (°C)	Impression diameter (mm)	
Supplementary information:				

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

TABLE: Needle- flame test (NFT)					
Object/ Part No./ Material	Manufacturer/ trademark	Duration of application of test flame (ta); (s)	Ignition of specified layer Yes/No	Duration of burning (tb) (s)	Verdict
Supplementary information: NFT not relevant (or applicable) for Parts of material classified as V-0 or V-1 NFT not relevant (or applicable) for Base material of PCBs classified as V-0 or if relevant VTM-0					

TABLE: Resistance to heat and fire - Glow wire tests								P
Object/ Part No./ Material	Manufacturer / trademark	Glow wire test (GWT); (°C)						Verdict
		550	650		750		850	
			te	ti	te	ti		
Base/ DMC	/	-	-	-	-	-	960°C	No flame
Shaft /	/	-	-	-	-	-	-	N/A
Handle / PA66	/	-	650°C	-	-	-	-	No flame
Cover / DMC	/	-	-	-	-	-	960°C	No flame
Object/ Part No./ Material	Manufacturer / trademark	Glow-wire flammability index (GWFI), °C				GW ignition temp. (GWIT), °C		Verdict
		550	650	750	850	675	775	
The test specimen passed the glow wire test (GWT) with no ignition [(te – ti) ≤ 2s] (Yes/No):								Yes
If no, then surrounding parts passed the needle-flame test of annex E (Yes/No) :								N/A
The test specimen passed the test by virtue of most of the flaming material being withdrawn with the glow-wire (Yes/No)?.....:								N/A
Ignition of the specified layer placed underneath the test specimen (Yes/No):								N/A
Supplementary information:								
550 °C GWT not relevant (or applicable) to parts of material classified at least HB40 or if relevant HBF The GWIT pre-selection option, the 850 °C GWFI pre-selection option, and the 850 °C GWT are not relevant (or applicable) for attended appliances.								

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

TABLE: Threaded Part Torque Test			
Threaded part identification	Diameter of thread (mm)	Column number (I, II, or III)	Applied torque (Nm)
Supplementary information:			

TABLE: Over-voltage and Under-voltage Test					
Test	Operating condition	Rated voltage (V)	Test voltage (V)	Temperature (oC)	Comments
Supplementary information:					

IEC 60947-2			
Clause	Requirement + Test	Result - Remark	Verdict

TABLE: Critical components information					
Object / part No.	Manufacturer/ trademark	Type / model	Technical data	Standard	Mark(s) of conformity ¹⁾
- Description:					
- Description:					
- Description:					
Supplementary information:					
1) Provided evidence ensures the agreed level of compliance. See OD-CB2039.					

Attachment 1 Photo documentation

